

阅读申明

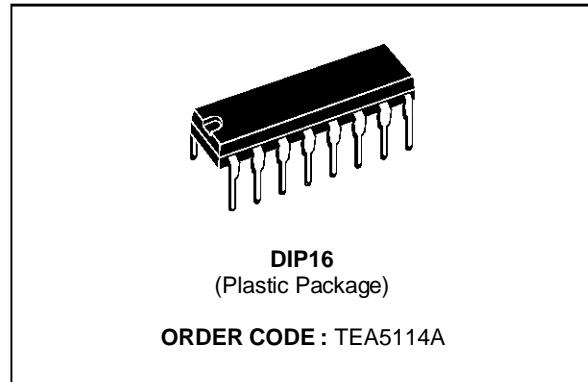
1. 本站收集的数据手册和产品资料都来自互联网，版权归原作者所有。如读者和版权方有任何异议请及时告之，我们将妥善解决。
2. 本站提供的中文数据手册是英文数据手册的中文翻译，其目的是协助用户阅读，该译文无法自动跟随原稿更新，同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。
3. 本站提供的产品资料，来自厂商的技术支持或者使用者的心得体会等，其内容可能存在描述上的差异，建议读者做出适当判断。
4. 如需与我们联系，请发邮件到marketing@iczoom.com，主题请标有“数据手册”字样。

Read Statement

1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.
2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.
3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.
4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets".

RGB SWITCHING CIRCUIT

- 25MHz BANDWIDTH
- CROSSTALK : 55dB
- SHORT CIRCUIT TO GROUND OR V_{CC} PROTECTED
- ANTI SATURATION GAIN CHANGING
- VIDEO SWITCHING



DESCRIPTION

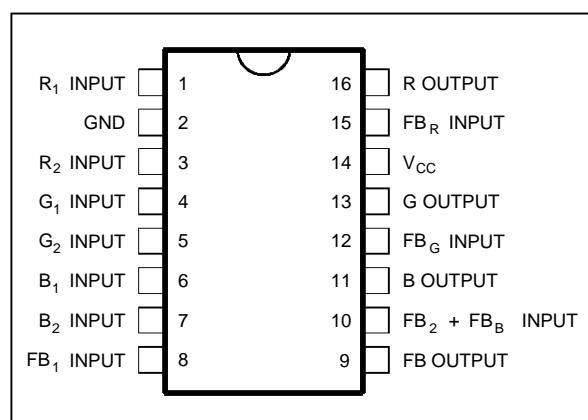
This integrated circuit provides RGB switching allowing connections between peri TV plug, internal RGB generator and video processor in a TV set.

The input signal black level is tied to the same reference voltage on each input in order to have no differential voltage when switching two RGB generators.

An AC output signal higher than 2 Vpp makes gain going slowly down to 0dB to protect the TV set video amplifier from saturation.

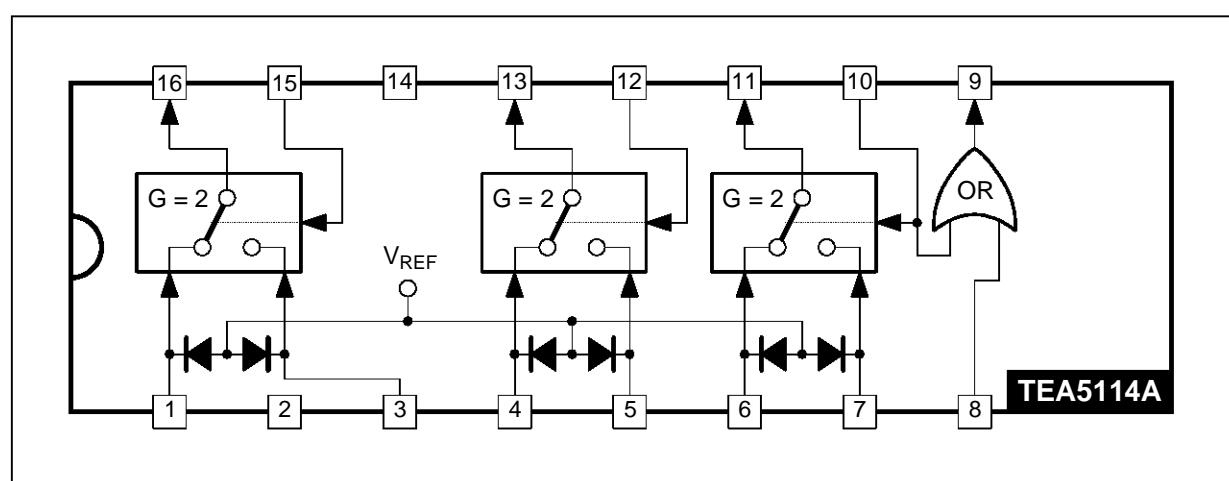
Fast blanking output is a logical OR between FB1 (Pin 8) and FB2 (Pin 10).

PIN CONNECTIONS



5114A-01.EPS

BLOCK DIAGRAM



5114A-02.EPS

TEA5114A

ABSOLUTE MAXIMUM RATINGS

| Symbol | Parameter | Value | Unit |
|------------------|--|-------------|--------|
| V _{CC} | Supply Voltage | 18 | V |
| T _j | Junction Temperature | - 40 to 150 | °C |
| T _{stg} | Storage Temperature | - 40 to 150 | °C |
| Z _L | Minimum Load Resistor on Each Output V _{CC} = 12 V V _{CC} = 10 V | 300 150 | Ω Ω |
| T _{amb} | Operating Ambient Temperature | 0 to 70 | °C |

5114A-01.TBL

THERMAL DATA

| Symbol | Parameter | Value | Unit |
|----------------------|-------------------------------------|-------|------|
| R _{th(j-a)} | Junction-ambient Thermal Resistance | 80 | °C/W |

5114A-02.TBL

ELECTRICAL OPERATING CHARACTERISTICS

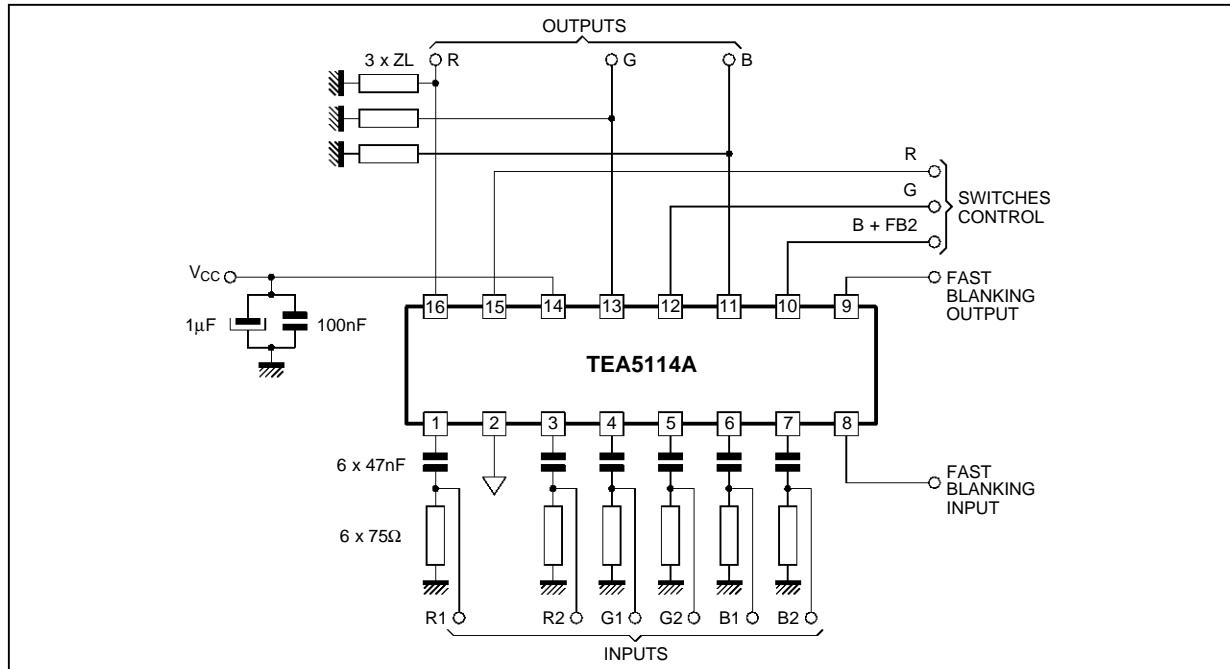
T_{amb} = 25 °C, V_{CC} = 12 V, Z_L (RGB) = 300 Ω

V_{CC} = 10 V, Z_L (RGB) = 150 Ω (unless otherwise specified)

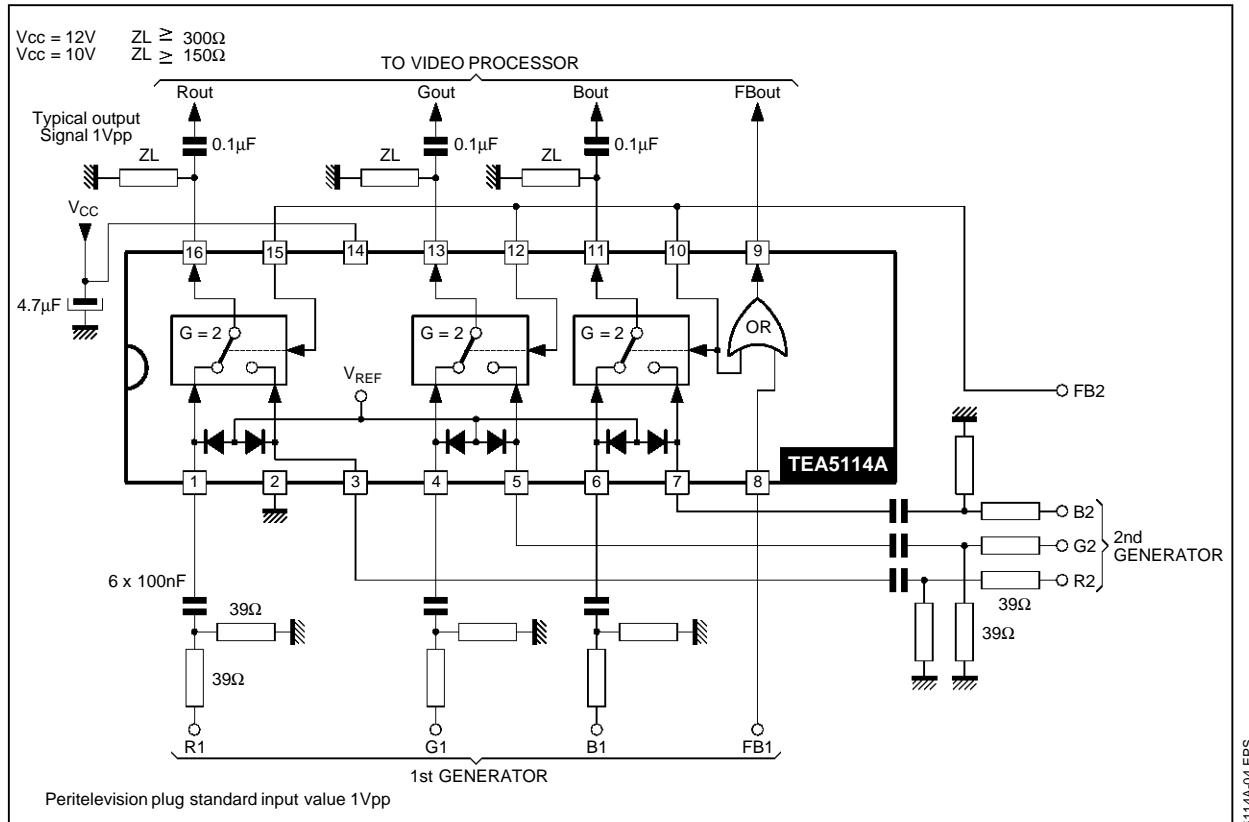
| Symbol | Parameter | Min. | Typ. | Max. | Unit |
|---------------------|---|------|------|------|-----------------|
| V _{CC} | Supply Voltage | 9 | 12 | 13.2 | V |
| I _{CC} | Supply Current without Load V _{CC} = 12 V | 20 | 30 | 40 | mA |
| V _{ON} | Black Level Output Voltage (on pins 11, 13, 16 square wave output signal 1 kHz - 1 V _{pp}) T _j = 25 °C (5mV/°C typical variation) | 1.8 | 2.5 | 2.9 | V |
| G _{RGB} | Gain of Each Channel Pins 11, 13, 16 F = 1 MHz, V _{in} = 0.5 V _{pp} | 5 | 5.5 | 6 | dB |
| B _{RGB} | Bandwidth (- 3 dB) V _O = 1 V _{pp} | 18 | 22 | | MHz |
| V _{GC} | Threshold Output Voltage for Gain Changing (- 0.5 dB) | 2 | | | V _{pp} |
| V _R | Video Rejection between Two Inputs R, G or B F = 1 MHz Sinus V _O = 1 V _{pp} | 50 | 55 | | dB |
| Z _{IRGB} | Input Impedance on Pins 1, 3, 4, 5, 6, 7 V _O = 1 V _{pp} | 10 | | | kΩ |
| Z _{ORGB} | R, G, B Output Impedance on Pins 11, 13, 16 | | | 15 | Ω |
| T _{FB} | FB rising and falling time on pin 9. 1 V _{pp} Input Voltage Pins 8, 10 | | 20 | | ns |
| V _{IHF} | FB High Level Input Voltage on Pins 8, 10, 12, 15 | 1 | | 4 | V |
| V _{ILF} | FB Low Level Input Voltage on Pins 8, 10, 12, 15 | 0 | | 0.4 | V |
| Z _{IF} | Input Impedance on Pins 8, 10, 12, 15 | 0.7 | 1 | 1.3 | kΩ |
| V _{OHF} | High Level FB Output Voltage (pin 9) Input 1 V on Pins 8, 10 | 0.8 | 1 | 1.2 | V |
| V _{OLF} | Low Level FB Output (pin 9) Input 0 V on Pins 8, 10 | | | 0.3 | V |
| Z _{OF} | FB Output Impedance Pin 9 High Level | | | 30 | Ω |
| T _{dFBRGB} | Delay Time between FB Inputs and R, G, B Switching | | 20 | | ns |

5114A-03.TBL

TEST DIAGRAM

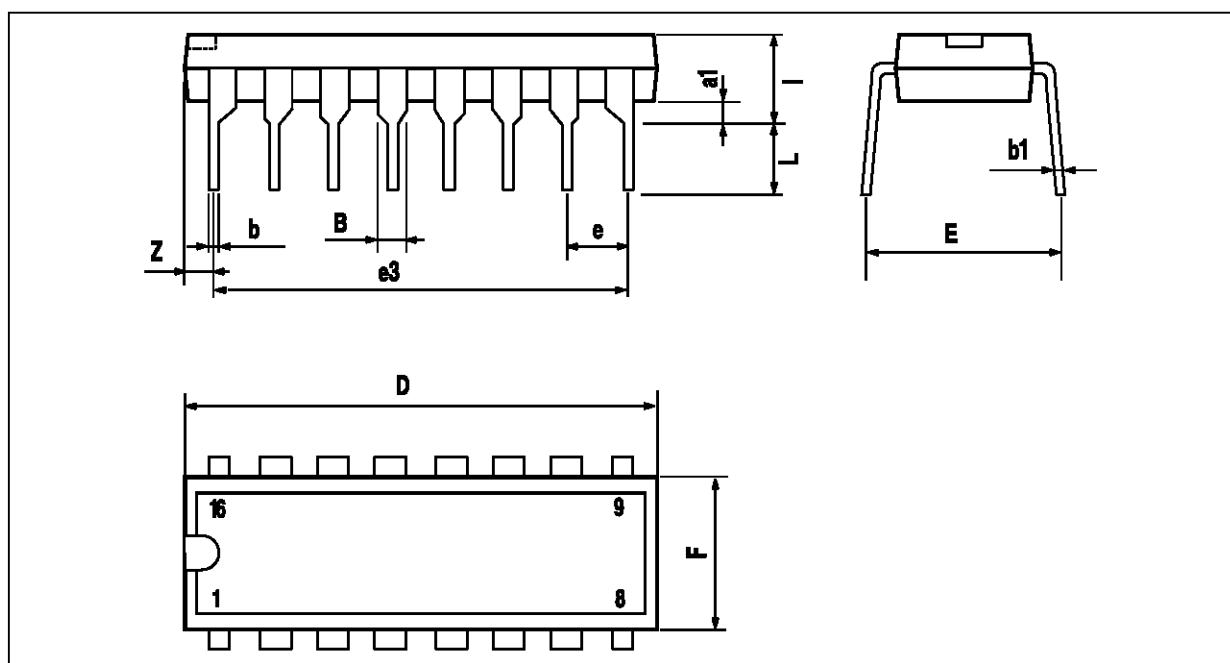


TYPICAL R, G, B SWITCHING APPLICATION



PACKAGE MECHANICAL DATA

16 PINS – PLASTIC DIP



| Dimensions | Millimeters | | | Inches | | |
|------------|-------------|-------|------|--------|-------|-------|
| | Min. | Typ. | Max. | Min. | Typ. | Max. |
| a1 | 0.51 | | | 0.020 | | |
| B | 0.77 | | 1.65 | 0.030 | | 0.065 |
| b | | 0.5 | | | 0.020 | |
| b1 | | 0.25 | | | 0.010 | |
| D | | | 20 | | | 0.787 |
| E | | 8.5 | | | 0.335 | |
| e | | 2.54 | | | 0.100 | |
| e3 | | 17.78 | | | 0.700 | |
| F | | | 7.1 | | | 0.280 |
| I | | | 5.1 | | | 0.201 |
| L | | 3.3 | | | 0.130 | |
| Z | | | 1.27 | | | 0.050 |

DIP16.TBL

Information furnished is believed to be accurate and reliable. However, SGS-THOMSON Microelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No licence is granted by implication or otherwise under any patent or patent rights of SGS-THOMSON Microelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. SGS-THOMSON Microelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of SGS-THOMSON Microelectronics.

© 1996 SGS-THOMSON Microelectronics - All Rights Reserved

Purchase of I²C Components of SGS-THOMSON Microelectronics, conveys a license under the Philips I²C Patent. Rights to use these components in a I²C system, is granted provided that the system conforms to the I²C Standard Specifications as defined by Philips.

SGS-THOMSON Microelectronics GROUP OF COMPANIES

Australia - Brazil - Canada - China - France - Germany - Hong Kong - Italy - Japan - Korea - Malaysia - Malta - Morocco
The Netherlands - Singapore - Spain - Sweden - Switzerland - Taiwan - Thailand - United Kingdom - U.S.A.