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Standard Recovery Diodes (Stud Version), 12 A



PRODUCT SUMMARY			
I _{F(AV)}	12 A		
Package	DO-203AA (DO-4)		
Circuit configuration	Single diode		

FEATURES

- · High surge current capability
- Stud cathode and stud anode version



- Wide current range
- Types up to 1200 V V_{RRM}
- Designed and qualified for industrial and consumer level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

- Battery charges
- Converters
- Power supplies
- Machine tool controls

MAJOR RATINGS AND CHARACTERISTICS				
PARAMETER	TEST CONDITIONS	VALUES	UNITS	
1		12	А	
I _{F(AV)}	T _C	144	°C	
I _{F(RMS)}		19	А	
I _{FSM}	50 Hz	265	٨	
	60 Hz	280	Α	
l ² t	50 Hz	351	A ² s	
1-1	60 Hz	320	A-S	
V _{RRM}	Range	100 to 1200	V	
T _J		-65 to +175	°C	

ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS					
TYPE NUMBER	VOLTAGE CODE	V _{RRM} , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK VOLTAGE V	I _{RRM} MAXIMUM AT T _J = 175 °C mA	
	10	100	150		
	20	200	275		
	40	400	500		
VS-12F(R)	60	600	725	12	
	80	800	950		
	100	1000	1200		
	120	1200	1400		



FORWARD CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average forward current	le.a.a	180° conduction, half sine wave			12	А
at case temperature	I _{F(AV)}	100 conduc	otion, nan sine we	100	144	°C
Maximum RMS forward current	I _{F(RMS)}				19	Α
		t = 10 ms	No voltage		265	
Maximum peak, one-cycle forward,		t = 8.3 ms	reapplied		280	_
non-repetitive surge current	I _{FSM}	t = 10 ms	100 % V _{RRM}		225	Α
	t = 8.3 ms reapplied	Sinusoidal half wave.	235			
Maximum I ² t for fusing	l ² t	t = 10 ms	No voltage	initial $T_J = T_J$ maximum	351	A ² s
		t = 8.3 ms	reapplied		320	
		t = 10 ms	100 % V _{RRM} reapplied		250	
		t = 8.3 ms			226	
Maximum I ² √t for fusing	I²√t	t = 0.1 to 10 ms, no voltage reapplied			3510	A²√s
Low level value of threshold voltage	V _{F(TO)1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum			0.77	V
High level value of threshold voltage	V _{F(TO)2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			0.97	V
Low level value of forward slope resistance	r _{f1}	(16.7 % x π x $I_{F(AV)}$ < I < π x $I_{F(AV)}$), $T_J = T_J$ maximum			10.70	mΩ
High level value of forward slope resistance	r _{f2}	$(I > \pi \times I_{F(AV)}), T_J = T_J \text{ maximum}$			6.20	1/152
Maximum forward voltage drop	V_{FM}	I _{pk} = 38 A, T _J = 25 °C, t _p = 400 μs rectangular wave		1.26	V	

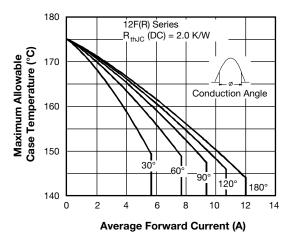
THERMAL AND MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction operating temperature range	T _J -65		-65 to +175	°C
Maximum storage temperature range	T_{Stg}	-6		
Maximum thermal resistance, junction to case	R_{thJC}	R _{thJC} DC operation		K/W
Maximum thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, flat and greased	0.5	rv VV
		Not lubricated threads	1.5 + 0 - 10 %	N⋅m
Allowable mounting torque		Not lublicated tilleads	13	lbf ⋅ in
Allowable mounting torque	Lubricated threads		1.2 + 0 - 10 %	N⋅m
		Lubricated tilleads	10	lbf ⋅ in
Approximate weight			7	g
Approximate weight			0.25	OZ.
Case style		See dimensions - link at the end of datasheet	DO-203AA	A (DO-4)

△R _{thJC} CONDUCTION					
CONDUCTION ANGLE	SINUSOIDAL CONDUCTION	RECTANGULAR CONDUCTION	TEST CONDITIONS	UNITS	
180°	0.33	0.26			
120°	0.41	0.44			
90°	0.53	0.58	$T_J = T_J$ maximum	K/W	
60°	0.78	0.81			
30°	1.28	1.29			

Note

• The table above shows the increment of thermal resistance R_{thJC} when devices operate at different conduction angles than DC





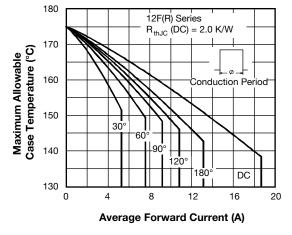


Fig. 1 - Current Ratings Characteristics

Fig. 2 - Current Ratings Characteristics

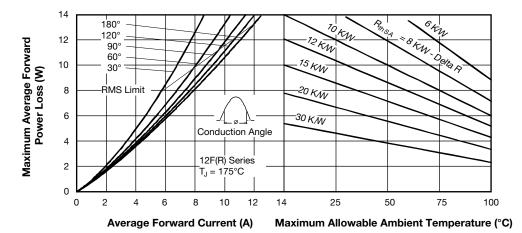


Fig. 3 - Forward Power Loss Characteristics

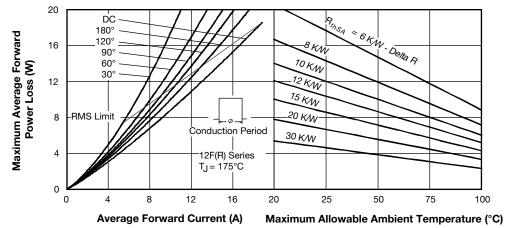


Fig. 4 - Forward Power Loss Characteristics

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Vishay Semiconductors

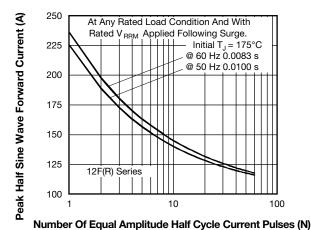
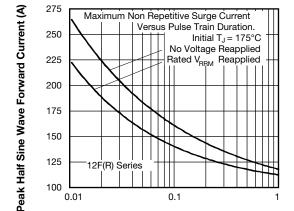


Fig. 5 - Maximum Non-Repetitive Surge Current



Pulse Train Duration (s) Fig. 6 - Maximum Non-Repetitive Surge Current

0.1

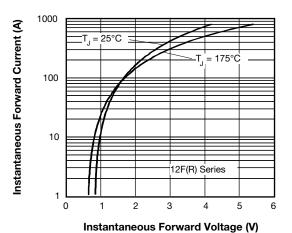


Fig. 7 - Forward Voltage Drop Characteristics

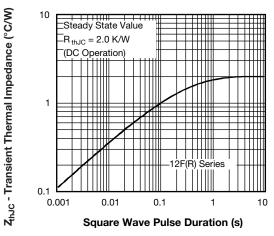


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

ORDERING INFORMATION TABLE

0.01

Device code	VS-	12	F	R	120	M
	(1)	(2)	(3)	(4)	(5)	6

- Vishay Semiconductors product
- Current rating: code = $I_{F(AV)}$
- F = standard device
- None = stud normal polarity (cathode to stud)
 - R = stud reverse polarity (anode to stud)
- Voltage code x 10 = V_{RRM} (see Voltage Ratings table)
- None = stud base DO-203AA (DO-4) 10-32UNF-2A

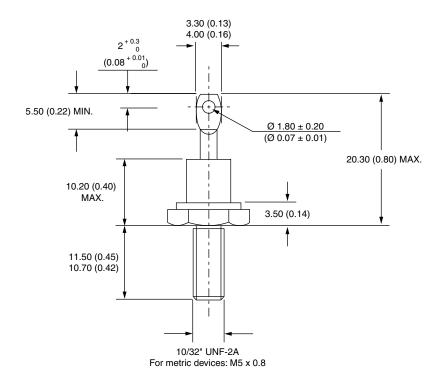
M = stud base DO-203AA (DO-4) M5 x 0.8

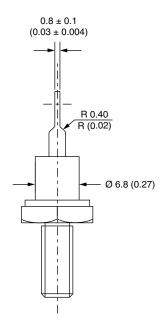
LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95311		

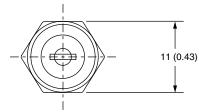


DO-203AA (DO-4)

DIMENSIONS in millimeters (inches)









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