


阅读申明

- 1.本站收集的数据手册和产品资料都来自互联网，版权归原作者所有。如读者和版权方有任何异议请及时告之，我们将妥善解决。
- 2.本站提供的中文数据手册是英文数据手册的中文翻译，其目的是协助用户阅读，该译文无法自动跟随原稿更新，同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。
- 3.本站提供的产品资料，来自厂商的技术支持或者使用者的心得体会等，其内容可能存在描述上的差异，建议读者做出适当判断。
- 4.如需与我们联系，请发邮件到marketing@iczoom.com，主题请标有“数据手册”字样。

Read Statement

1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.
2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.
3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.
4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets" .

Clock Generator

Part No.	NCT1063Y-585
Datasheet	 Request PDF
Description	<p>The NCT1063Y-585 is a Clock Synthesizer for Intel Calpella platform which compliant with the Intel CK505 specification. The NCT1063Y-585 provides most of the clocks required for a high-speed microprocessor and integrates the regulator and series terminated resistors.</p> <p>The NCT1063Y-585 provides SMBus serial bus interface to program the registers to enable or disable each clock output and provides -0.5% down type spread spectrum or programmable S.S.T. scale to reduce EMI.</p> <p>The NCT1063Y-585 accepts a 14.318 MHz reference crystal as its input and runs on single 3.3V supply.</p>
Features	<ul style="list-style-type: none"> › 2 pairs of push-pull Differential clock outputs for CPU. › 1 pairs of push-pull Differential clock outputs for DOT_96. › 1 pair push-pull Differential clock outputs for SRC/SATA selectable. › 1 pair push-pull Differential clock outputs for SRC. › Amplitude controllable for differential clocks. › 2 27MHz outputs, 1 support spread spectrum. › 1 14.318MHz REF clock output. › SMBus 2-wire serial interface and support byte read/write and block read/write. › -0.5% down type spread spectrum in software select mode. › Programmable S.S.T. scale to reduce EMI. › Programmable registers to enable/disable each output and select modes. › Programmable clock outputs slew rate control.
Diagram	N/A
Package	QFN32 Halogen free (Green package)
Other Files	N/A
Development Tools	N/A
Others	N/A

Contact us: ComputerIC@nuvoton.com