阅读申明

- 1.本站收集的数据手册和产品资料都来自互联网,版权归原作者所有。如读者和版权方有任何异议请及时告之,我们将妥善解决。
- 2.本站提供的中文数据手册是英文数据手册的中文翻译,其目的是协助用户阅读,该译文无法自动跟随原稿更新,同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。
- 3.本站提供的产品资料,来自厂商的技术支持或者使用者的心得体会等,其内容可能存在描 叙上的差异,建议读者做出适当判断。
- 4.如需与我们联系,请发邮件到marketing@iczoom.com,主题请标有"数据手册"字样。

Read Statement

- 1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.
- 2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.
- 3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.
- 4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets".

Audio digital potentiometers BH3532FS

The BH3532FS is a digital potentiometer designed for use in audio devices. Its built-in $22k\Omega$ resistance systems can be used to set the data from the microcomputer in 256 steps.

Applications

Volume of recording and playing

Features

- Resistance can be set to any of 256 steps using digital codes (serial data).
- 2) Two built-in channels (Lch, Rch)
- 3) SSOP-A20 package

● Absolute maximum ratings (Ta = 25°C)

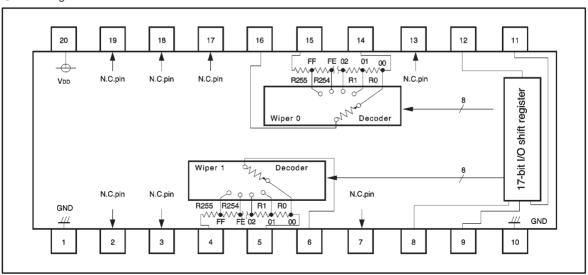
Parameter	Symbol	Limits	Unit
Power supply voltage	Vcc	7	V
Power dissipation	Pd	600*	mW
Operating temperature	Topr	−25~ +75	c
Storage temperature	Tstg	−55∼ +125	°

^{*} Reduced by 6mW for each increase inTa of 1°C over 25°C

• Recommended operating conditions (Ta = 25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit
Power supply voltage	V _{DD}	3	_	5.5	٧

Block diagram

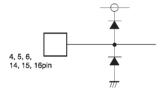


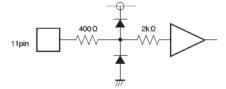
Pin descriptions

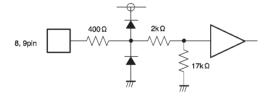
Pin No.	Pin name	Function	Pin No.	Pin name	Function
1	GND	GND	11	DIN	Serial data input
2	N.C.	N.C.	12	DOUT	Serial data output
3	N.C.	N.C.	13	N.C.	N.C.
4	H1	Ch 1 high position resistance	14	L0	Ch 0 low position resistance
5	L1	Ch 1 low position resistance	15	H0	Ch 0 high position resistance
6	W1	Pin for ch 1 wiper	16	Wo	Pin for Ch 0 wiper
7	N.C.	N.C.	17	N.C.	N.C.
8	EN	Overwrite authorization input	18	N.C.	N.C.
9	CLK	Clock input	19	N.C.	N.C.
10	GND	GND	20	Voo	Vod

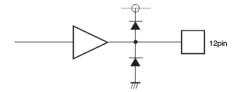
^{*} Do not connect anything to the N.C. pin.

●Input/output circuits









●Electrical characteristics (unless otherwise noted, Ta = 25°C, Vcc = 3.5V)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
⟨DC characteristics⟩						
Quiescent current	la	50	100	150	μΑ	
Input leakage current	lu	-1.0	_	1.0	μΑ	*1
Input high level voltage	Іін	3.0	_	_	V	
Input low level voltage	lı∟	_	_	0.5	V	
Output high level voltage	Іон	3.0	_	_	V	I _{OH} =-100 μ A
Output low level voltage	loL	_	_	0.5	V	IoL=100 μ A
Total resistance	R⊤	17.6	22	26.4	kΩ	
Wiper resistance	Rw	0.4	0.8	1.6	kΩ	Iop=500 μ A
〈AC characteristics〉*2	⟨AC characteristics⟩ *2					
Clock frequency	FCLK	_	_	1	MHz	
Clock pulse width	Tw	500	_	_	ns	
Data setup time	Tsu	300	_	_	ns	
Data hold time	Тн	100	_	_	ns	
Transmission lag time CLK→DOUT	Tolh Tohl	_	_	500 500	ns	
Transmission lag time EN→CLK	Тсін Тсні	500 500	<u>-</u>	_	ns	

ONot designed for radiation resistence

- *1 CLK input and EN input are pulled down when internal resistance is 17 k $\!\Omega$.
- *2 VDD=3.5V
- *3 Input capacity (reference value): 5 pF (Max.) Output capacity (reference value): 7 pF (Max.)

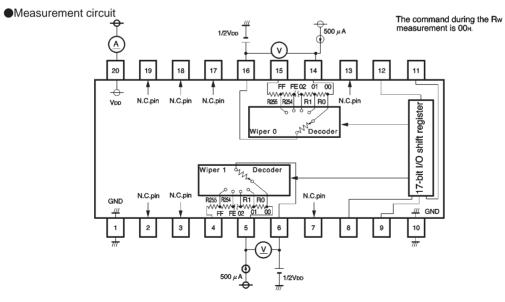


Fig. 1

Circuit operation

The BH3532FS has two $22k\Omega$ variable resistance systems which can be set in 256 steps (86Ω intervals). Resistance can be set in 256 steps using the MSB first 8-bit data.

Input data is 17-bit serial data. The first bit is always "L". The next eight bits set the resistance for wiper 1. The last eight bits set the resistance for wiper 0.

Input data is effective when the EN terminal is set to "H", and is put on hold when the EN terminal is set to "L". Also, the reading of the data is performed when CLK rises. When input data is effective, the previous output data is output serially to the DOUT terminal.

See the figures below for more details.

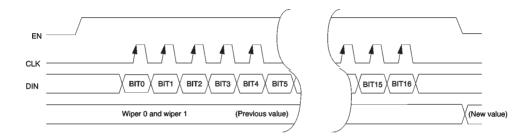


Fig. 2 Timing chart 1

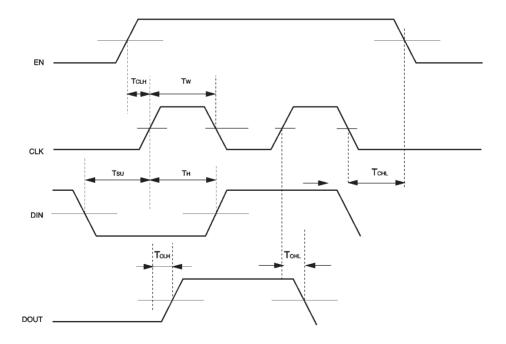


Fig. 3 Timing chart 2

Electrical characteristic curves

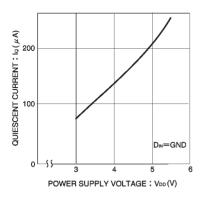


Fig. 4 Power supply voltage vs. quiescent curve

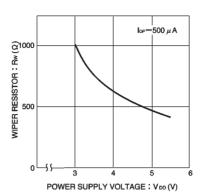
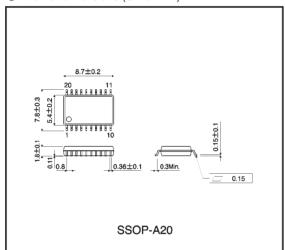


Fig. 5 Power supply voltage vs. wiper resistance

●External dimensions (Units: mm)



Notes

- No technical content pages of this document may be reproduced in any form or transmitted by any
 means without prior permission of ROHM CO.,LTD.
- The contents described herein are subject to change without notice. The specifications for the
 product described in this document are for reference only. Upon actual use, therefore, please request
 that specifications to be separately delivered.
- Application circuit diagrams and circuit constants contained herein are shown as examples of standard
 use and operation. Please pay careful attention to the peripheral conditions when designing circuits
 and deciding upon circuit constants in the set.
- Any data, including, but not limited to application circuit diagrams information, described herein are intended only as illustrations of such devices and not as the specifications for such devices. ROHM CO.,LTD. disclaims any warranty that any use of such devices shall be free from infringement of any third party's intellectual property rights or other proprietary rights, and further, assumes no liability of whatsoever nature in the event of any such infringement, or arising from or connected with or related to the use of such devices.
- Upon the sale of any such devices, other than for buyer's right to use such devices itself, resell or
 otherwise dispose of the same, no express or implied right or license to practice or commercially
 exploit any intellectual property rights or other proprietary rights owned or controlled by
- ROHM CO., LTD. is granted to any such buyer.
- Products listed in this document use silicon as a basic material.
 Products listed in this document are no antiradiation design.

The products listed in this document are designed to be used with ordinary electronic equipment or devices (such as audio visual equipment, office-automation equipment, communications devices, electrical appliances and electronic toys).

Should you intend to use these products with equipment or devices which require an extremely high level of reliability and the malfunction of with would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

About Export Control Order in Japan

Products described herein are the objects of controlled goods in Annex 1 (Item 16) of Export Trade Control Order in Japan.

In case of export from Japan, please confirm if it applies to "objective" criteria or an "informed" (by MITI clause) on the basis of "catch all controls for Non-Proliferation of Weapons of Mass Destruction.

