

1.本站收集的数据手册和产品资料都来自互联网,版权归原作者所有。如读者和版权方有任 何异议请及时告之,我们将妥善解决。

本站提供的中文数据手册是英文数据手册的中文翻译,其目的是协助用户阅读,该译文无法自动跟随原稿更新,同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。

3.本站提供的产品资料,来自厂商的技术支持或者使用者的心得体会等,其内容可能存在描 叙上的差异,建议读者做出适当判断。

4.如需与我们联系,请发邮件到marketing@iczoom.com,主题请标有"数据手册"字样。

# **Read Statement**

1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.

2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.

3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.

4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets".



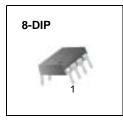
# KA5532 Dual Operational Amplifier

#### Features

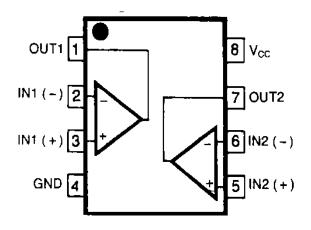
- Internal Frequency Compensation
- Slew Rate: 8V/µs
- Input Noise Voltage:  $8nV/\sqrt{Hz}$  (fo = 30Hz)
- Full Power Bandwidth: 140KHz

#### Description

The KA5532 is a internally compensated dual low noise OP AMP. The high small signal and power bandwidth provides superior performance in high quality AMP, all control circuits, and telephone applications.



#### Internal Block Diagram



## Absolute Maximum Ratings

Parameter	Symbol	KA5532	Unit
Power Supply Voltage	Vcc	±22	V
Differential Input Voltage	V(DIFF)	±13	V
Input Voltage	VI	Supply Voltage	V
Power Dissipation, T <sub>A</sub> = 25°C 8-DIP	PD	1100	mW
Operating Temperature Range	TOPR	0 ~ +70	°C

### **Thermal Data**

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-Ambient Max. 8-DIP	Rθja	110	°C/W

## **Electrical Characteristics**

(V<sub>CC</sub> = 15V, V<sub>EE</sub> = -15V, T<sub>A</sub> = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input Offset Voltage	Vio	-	-	0.5	4.0	mV
Input Offset Current	liO	-	-	10	150	nA
Input Bias Current	IBIAS	-	-	200	800	nA
Supply Current	Icc	-	-	6.0	16	mA
Input Voltage Range	VI(R)	-	±12	±13	-	V
Common Mode Rejection Range	CMRR	$T_A = 25^{\circ}C$	70	100	-	dB
Power Supply Rejection Ratio	PSRR	TA = 25°C	80	100	-	dB
Output Voltage Swing	VO(P-P)	RL≥600Ω	±12	±13	-	V
Input Resistance	Rı	$T_A = 25^{\circ}C$	30	300	-	KΩ
Short Circuit Current	Isc	-	-	38	-	mA
Overshoot	OS	RL =600Ω, CL =100pF	-	10	20	%
Voltage Gain	Gv	f = 10KHz	2	2.2	-	V/mV
Gain Bandwidth Product	GBW	$C_L = 100 pF, R_L = 600 \Omega$	8	10	-	MHz
Slew Rate	SR	$R_L = 1K$ , $C_L = 100pF$ , $R_L = 600\Omega$	6	8.0	-	V/µs
Input Noise Voltage	e <sub>N</sub>	fo = 30Hz fo = 1KHz	-	8.0 5.0	-	nV/√Hz

#### **Typical Performance Characteristics**

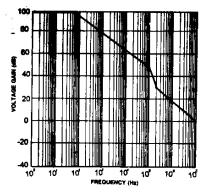


Figure 1. Open Loop Frequency Response

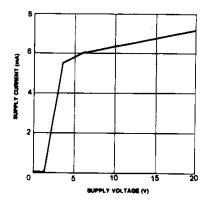


Figure 3. Supply Current vs Supply Voltage

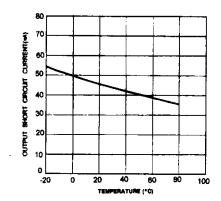


Figure 5. Output Circuit Current vs Temperature

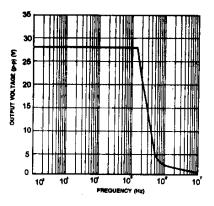


Figure 2. Large Signal Frequency Response

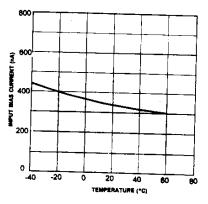


Figure 4. Input Bias Current vs Temperature

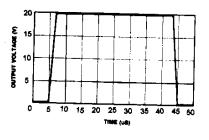
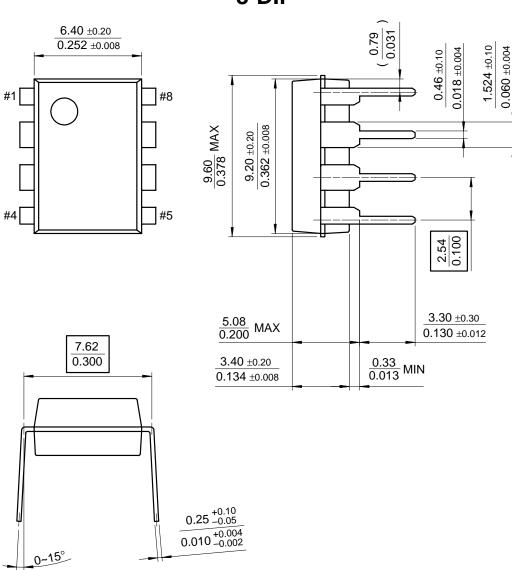


Figure 6. Slew Rate

#### **Mechanical Dimensions**

#### Package

#### **Dimensions in millimeters**



8-DIP

# **Ordering Information**

Product Number	Package	Operating Temperature		
KA5532	8-DIP	0 ~ + 70°C		

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

www.fairchildsemi.com