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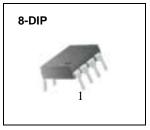
## KF351 Single Operational Amplifier (JFET)

## Features

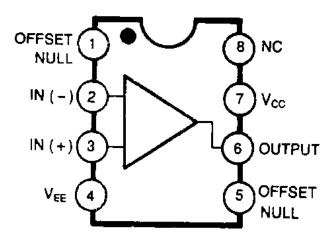
- Internally trimmed offset voltage: 10mV
- Low input bias current : 50pA
- Wide gain bandwidth : 4MHz
- High slew rate : 13V/µs
- High input impedance :  $10^{12}\Omega$

## Description

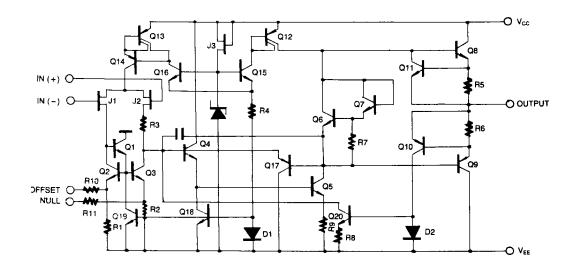
The KF351 is JFET input operational amplifier with an internally compensated input offset voltage. The JFET input device provides wide bandwidth, low input bias currents and offset currents.



## Internal Block Diagram



## Schematic Diagram



## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	Vcc	±18	V
Differential Input Voltage	VI(DIFF)	30	V
Input Voltage Range	VI	±15	V
Output Short Circuit Duration	-	Continuous	-
Power Dissipation	PD	500	mW
Operating Temperature	TOPR	0 ~ +70	°C
Storage Temperature Range	TSTG	-65 ~ +150	°C

## **Electrical Characteristics**

Parameter	Symbol	Conditions		Min.	Тур.	Max.	Unit
Input Offset Voltage	Vio	$R_S = 10k\Omega$		-	5.0	10	mV
			0 °C≤TA≤70 °C	-	-	13	111V
Input Offset Voltage Drift (Note1)	$\Delta V_{IO}/\Delta T$	$R_S = 10k\Omega$	0 °C≤T <sub>A</sub> ≤70 °C	-	10	-	μV/ °C
Input Offset Current	liO			-	25	100	pА
			0 °C≤TA≤70 °C	-	-	4	nA
Input Bias Current	IBAIS			-	50	200	pА
			0 °C≤TA≤70 °C	-	-	8	nA
Input Resistance (Note1)	RI	-		-	10 <sup>12</sup>	-	Ω
Large Signal Voltage Gain	Gv	VO(P-P)= ± 10V		25	100	-	V/mV
		RL=2kΩ	0 °C≤TA≤70 °C	15	-	-	V/IIIV
Output Voltage Swing	VO(P-P)	$R_L = 10k\Omega$		±12	±13.5	-	V
Input Voltage Range	VI(R)	-		±11	+15 -12	-	V
Common Mode Rejection Ratio	CMRR	$R_S \le 10 k\Omega$		70	100	-	dB
Power Supply Rejection Ratio	PSRR	Rs≤10kΩ		70	100	-	dB
Power Supply Current	Icc	-		-	2.3	3.4	mA
Slew Rate (Note1)	SR	Gv = 1		-	13	-	V/µs
Gain-Bandwidth Product (Note1)	GBW	-		-	4	-	MHz

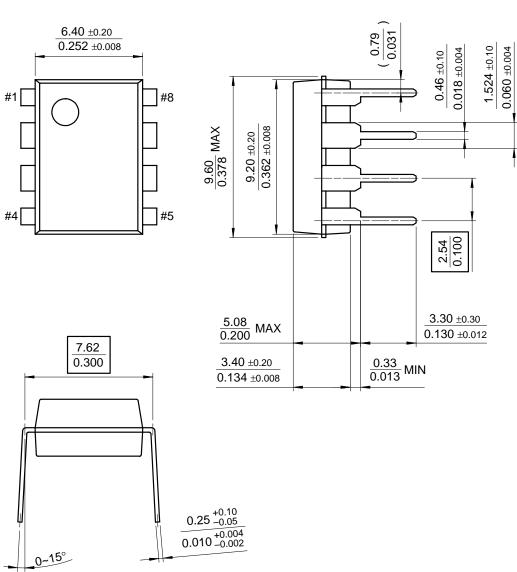
#### Note :

1. Guaranteed by design.

## **Mechanical Dimensions**

### Package

#### **Dimensions in millimeters**



8-DIP

## **Ordering Information**

Product Number	Package	Operating Temperature
KF351	8-DIP	0 ~ + 70°C

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