# 阅读申明

- 1.本站收集的数据手册和产品资料都来自互联网,版权归原作者所有。如读者和版权方有任何异议请及时告之,我们将妥善解决。
- 2.本站提供的中文数据手册是英文数据手册的中文翻译,其目的是协助用户阅读,该译文无法自动跟随原稿更新,同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。
- 3.本站提供的产品资料,来自厂商的技术支持或者使用者的心得体会等,其内容可能存在描 叙上的差异,建议读者做出适当判断。
- 4.如需与我们联系,请发邮件到marketing@iczoom.com,主题请标有"数据手册"字样。

# **Read Statement**

- 1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.
- 2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.
- 3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.
- 4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets" .

Note: xxxFW (JEDEC SOP) is not available in

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

## TC74ACT374P,TC74ACT374F,TC74ACT374FW,TC74ACT374FT

#### Octal D-Type Flip-Flop with 3-State Output

The TC74ACT374 is an advanced high speed CMOS OCTAL FLIP-FLOP fabricated with silicon gate and double-layer metal wiring  $C^2MOS$  technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

This devices may be used as a level converter for interfacing TTL or NMOS to High Speed CMOS. The inputs are compatible with TTL, NMOS and CMOS output voltage levels.

These 8-bit D-type flip-flops are controlled by a clock input (CK) and a output enable input (  $\overline{OE}$  ).

When the  $\overline{\,{
m OE}\,}$  input is high, the eight outputs are in a high impedance state.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

#### **Features**

- High speed:  $f_{max} = 180 \text{ MHz}$  (typ.) at  $V_{CC} = 5 \text{ V}$
- Low power dissipation: ICC = 8 μA (max) at Ta = 25°C
- Compatible with TTL outputs:  $V_{IL} = 0.8 \text{ V (max)}$   $V_{IH} = 2.0 \text{ V (min)}$
- Symmetrical output impedance: |IOH| = IOL = 24 mA (min) Capability of driving 50  $\Omega$  transmission lines.
- Balanced propagation delays: tpLH ≃ tpHL
- Pin and function compatible with 74F374

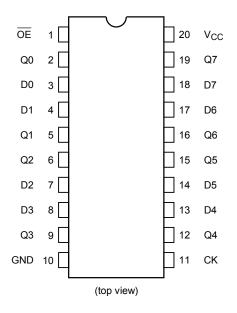
Japan. TC74ACT374P DIP20-P-300-2.54A TC74ACT374F SOP20-P-300-1.27A SOP20-P-300-1.27 TC74ACT374FW SOL20-P-300-1.27 TC74ACT374FT

Weight

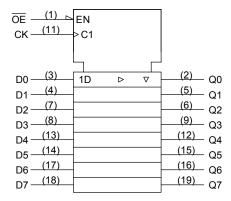
DIP20-P-300-2.54A : 1.30 g (typ.) SOP20-P-300-1.27A : 0.22 g (typ.) SOP20-P-300-1.27 : 0.22 g (typ.) SOL20-P-300-1.27 : 0.46 g (typ.) TSSOP20-P-0044-0.65A : 0.08 g (typ.)

TSSOP20-P-0044-0.65A

## Pin Assignment



### **IEC Logic Symbol**



#### **Truth Table**

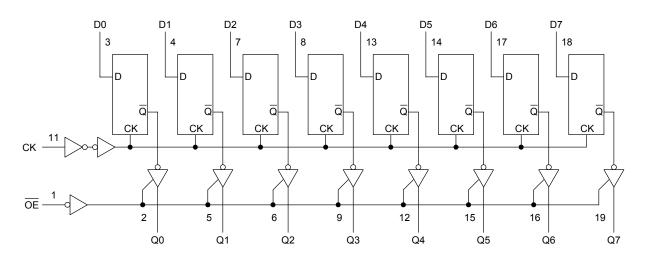
|    | Inputs | Output |                |  |  |  |  |
|----|--------|--------|----------------|--|--|--|--|
| ŌE | CK     | D      | Q              |  |  |  |  |
| Н  | Х      | Х      | Z              |  |  |  |  |
| L  |        | Х      | Q <sub>n</sub> |  |  |  |  |
| L  |        | L      | L              |  |  |  |  |
| L  | L      | Н      | Н              |  |  |  |  |

X: Don't care

Z: High impedance

Q<sub>n</sub>: No change

## **System Diagram**



2



### **Absolute Maximum Ratings (Note 1)**

| Characteristics                    | Symbol           | Rating                             | Unit |
|------------------------------------|------------------|------------------------------------|------|
| Supply voltage range               | $V_{CC}$         | −0.5 to 7.0                        | V    |
| DC input voltage                   | V <sub>IN</sub>  | -0.5 to V <sub>CC</sub> + 0.5      | V    |
| DC output voltage                  | V <sub>OUT</sub> | -0.5 to V <sub>CC</sub> + 0.5      | V    |
| Input diode current                | I <sub>IK</sub>  | ±20                                | mA   |
| Output diode current               | I <sub>OK</sub>  | ±50                                | mA   |
| DC output current                  | I <sub>OUT</sub> | ±50                                | mA   |
| DC V <sub>CC</sub> /ground current | Icc              | ±200                               | mA   |
| Power dissipation                  | P <sub>D</sub>   | 500 (DIP) (Note 2)/180 (SOP/TSSOP) | mW   |
| Storage temperature                | T <sub>stg</sub> | -65 to 150                         | °C   |

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Note 2: 500 mW in the range of Ta = -40 to 65°C. From Ta = 65 to 85°C a derating factor of -10 mW/°C should be applied up to 300 mW.

### **Recommended Operating Conditions (Note)**

| Characteristics          | Symbol           | Rating               | Unit     |
|--------------------------|------------------|----------------------|----------|
| Supply voltage           | V <sub>CC</sub>  | 4.5 to 5.5           | V        |
| Input voltage            | V <sub>IN</sub>  | 0 to V <sub>CC</sub> | <b>V</b> |
| Output voltage           | V <sub>OUT</sub> | 0 to V <sub>CC</sub> | <b>V</b> |
| Operating temperature    | T <sub>opr</sub> | −40 to 85            | °C       |
| Input rise and fall time | dt/dV            | 0 to 10              | ns/V     |

Note: The recommended operating conditions are required to ensure the normal operation of the device.

Unused inputs must be tied to either VCC or GND.



#### **Electrical Characteristics**

#### **DC Characteristics**

| Characteristics                  | Symbol          | Test Condition                                                                                    |                          |                     | Ta = 25°C        |      |      | Ta =<br>-40 to 85°C |      | Unit  |    |
|----------------------------------|-----------------|---------------------------------------------------------------------------------------------------|--------------------------|---------------------|------------------|------|------|---------------------|------|-------|----|
| Ondiductionstics                 | Cymbol          |                                                                                                   |                          | V <sub>CC</sub> (V) | Min              | Тур. | Max  | Min                 | Max  | Offic |    |
| High-level input voltage         | V <sub>IH</sub> | _                                                                                                 |                          |                     | 4.5<br>to<br>5.5 | 2.0  | ı    | ı                   | 2.0  | ı     | V  |
| Low-level input voltage          | V <sub>IL</sub> | _                                                                                                 |                          | 4.5<br>to<br>5.5    | _                | ı    | 0.8  |                     | 0.8  | V     |    |
|                                  | V <sub>OH</sub> | V <sub>IN</sub><br>= V <sub>IH</sub> or<br>V <sub>IL</sub>                                        | I <sub>OH</sub> = -50 μA |                     | 4.5              | 4.4  | 4.5  | _                   | 4.4  | _     |    |
| High-level output voltage        |                 |                                                                                                   | I <sub>OH</sub> = -24 mA |                     | 4.5              | 3.94 | _    | _                   | 3.80 | _     | V  |
|                                  |                 |                                                                                                   | I <sub>OH</sub> = -75 mA | (Note)              | 5.5              | _    | _    | _                   | 3.85 | _     |    |
|                                  | V <sub>OL</sub> | V <sub>IN</sub><br>= V <sub>IH</sub> or<br>V <sub>IL</sub>                                        | I <sub>OL</sub> = 50 μA  |                     | 4.5              | _    | 0.0  | 0.1                 | _    | 0.1   |    |
| Low-level output voltage         |                 |                                                                                                   | I <sub>OL</sub> = 24 mA  | 4.5                 | _                | _    | 0.36 | _                   | 0.44 | V     |    |
|                                  |                 |                                                                                                   | I <sub>OL</sub> = 75 mA  | (Note)              | 5.5              | _    | _    | _                   | _    | 1.65  |    |
| 3-state output off-state current | I <sub>OZ</sub> | V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub><br>V <sub>OUT</sub> = V <sub>CC</sub> or GND |                          | 5.5                 | ı                |      | ±0.5 | ı                   | ±5.0 | μΑ    |    |
| Input leakage current            | I <sub>IN</sub> | V <sub>IN</sub> = V <sub>CC</sub> or GND                                                          |                          | 5.5                 | -                | 1    | ±0.1 | -                   | ±1.0 | μΑ    |    |
| Quiescent supply current         | Icc             | V <sub>IN</sub> = V <sub>CC</sub> or GND                                                          |                          |                     | 5.5              |      |      | 8.0                 | _    | 80.0  | μA |
|                                  | I <sub>C</sub>  | Per input: V <sub>IN</sub> = 3.4 V  Other input: V <sub>CC</sub> or GND                           |                          |                     | 5.5              | _    | _    | 1.35                | _    | 1.5   | mA |

Note: This spec indicates the capability of driving 50  $\Omega$  transmission lines.

One output should be tested at a time for a 10 ms maximum duration.

## Timing Requirements (input: $t_r = t_f = 3 \text{ ns}$ )

| Characteristics     | Symbol             | Test Condition | ondition            |       | Ta =<br>-40 to<br>85°C | Unit |  |
|---------------------|--------------------|----------------|---------------------|-------|------------------------|------|--|
|                     |                    |                | V <sub>CC</sub> (V) | Limit | Limit                  |      |  |
| Minimum pulse width | t <sub>w (H)</sub> | _              | 5.0 ± 0.5           | 5.0   | 5.0                    | ns   |  |
| (CK)                | t <sub>w (L)</sub> |                |                     |       |                        |      |  |
| Minimum set-up time | ts                 | _              | $5.0 \pm 0.5$       | 3.0   | 3.0                    | ns   |  |
| Minimum hold time   | t <sub>h</sub>     | _              | 5.0 ± 0.5           | 2.0   | 2.0                    | ns   |  |

4



#### AC Characteristics ( $C_L$ = 50 pF, $R_L$ = 500 $\Omega$ , input: $t_r$ = $t_f$ = 3 ns)

| Characteristics               | Symbol                               | Test Condition |                     | Ta = 25°C |      |      | Ta =<br>-40 to 85°C |      | Unit |
|-------------------------------|--------------------------------------|----------------|---------------------|-----------|------|------|---------------------|------|------|
|                               | Í                                    |                | V <sub>CC</sub> (V) | Min       | Тур. | Max  | Min                 | Max  |      |
| Propagation delay time (CK-Q) | t <sub>pLH</sub><br>t <sub>pHL</sub> | _              | 5.0 ± 0.5           | _         | 6.1  | 9.6  | 1.0                 | 11.0 | ns   |
| Output enable time            | t <sub>pZL</sub>                     | _              | 5.0 ± 0.5           | _         | 6.2  | 10.1 | 1.0                 | 11.5 | ns   |
| Output disable time           | t <sub>pLZ</sub><br>t <sub>pHZ</sub> | _              | 5.0 ± 0.5           | _         | 5.6  | 7.9  | 1.0                 | 9.0  | ns   |
| Maximum clock frequency       | f <sub>max</sub>                     | _              | 5.0 ± 0.5           | 95        | 160  | _    | 95                  | _    | MHz  |
| Input capacitance             | C <sub>IN</sub>                      | _              |                     | _         | 5    | 10   | _                   | 10   | pF   |
| Output capacitance            | C <sub>OUT</sub>                     | _              |                     | _         | 10   | _    | _                   | _    | pF   |
| Power dissipation capacitance | C <sub>PD</sub>                      |                | (Note)              | _         | 34   | _    | _                   | _    | pF   |

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

5

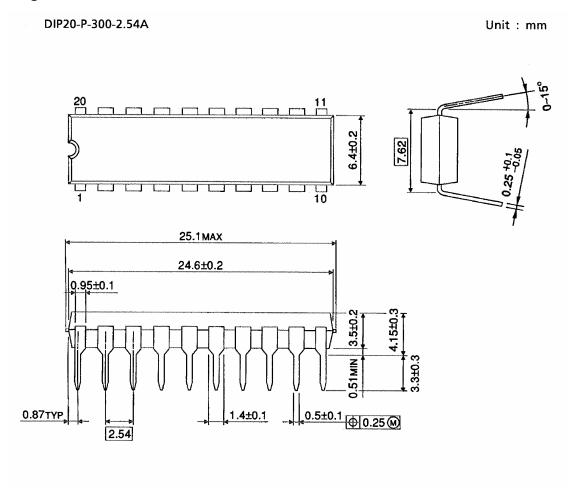
Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 (per F/F)$$

And the total C<sub>PD</sub> when n pcs. of F/F operate can be gained by the following equation:

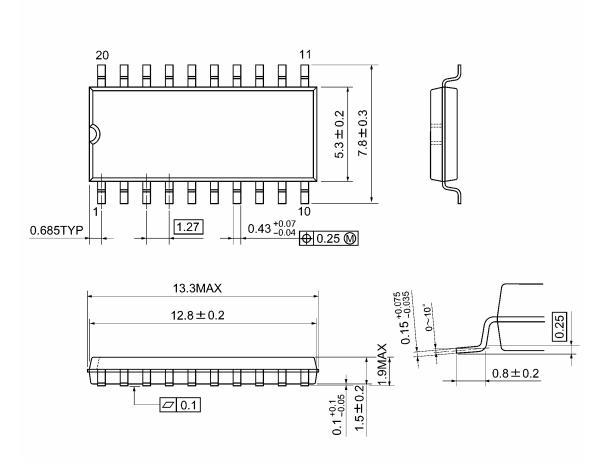
$$C_{PD}$$
 (total) = 22 + 12·n





Weight: 1.30 g (typ.)

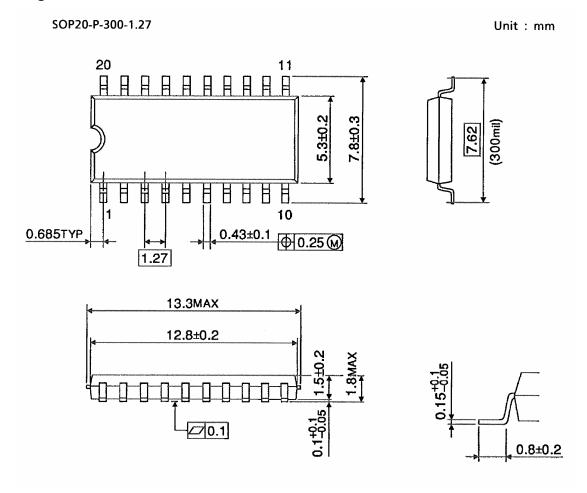
SOP20-P-300-1.27A Unit: mm



7

Weight: 0.22 g (typ.)

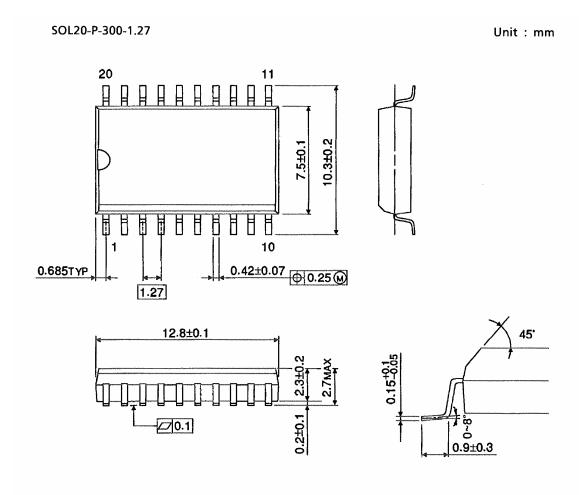




Weight: 0.22 g (typ.)



## **Package Dimensions (Note)**



9

Note: This package is not available in Japan.

Weight: 0.46 g (typ.)



TSSOP20-P-0044-0.65A Unit: mm 6.4±0.2  $0.22\substack{+0.09 \\ -0.06}$ 0.65 0.325TYP **⊕**0.13**M** 6.9MAX 6.5±0.1 1.2MAX 0.15 +0.03 0~10

1.0±0.05 0.1±0.05

(0.5)

0.45~0.75

Weight: 0.08 g (typ.)

S

Ø.1|S

Note: Lead (Pb)-Free Packages

DIP20-P-300-2.54A SOP20-P-300-1.27A TSSOP20-P-0044-0.65A

#### **RESTRICTIONS ON PRODUCT USE**

060116EBA

- The information contained herein is subject to change without notice. 021023\_D
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc. 021023\_A
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk. 021023 B
- The products described in this document shall not be used or embedded to any downstream products of which manufacture, use and/or sale are prohibited under any applicable laws and regulations. 060106\_Q
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of TOSHIBA or others. 021023\_c
- The products described in this document are subject to the foreign exchange and foreign trade laws. 021023\_E