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TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

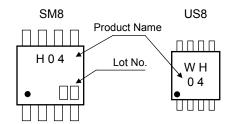
TC7WH04FU,TC7WH04FK

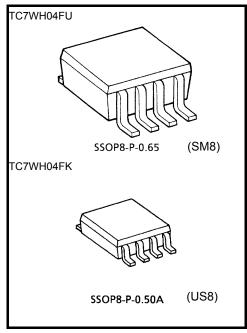
Triple Inverter

Features

- High speed: t_{pd} = 3.8ns (typ.) at V_{CC} = 5V, C_L = 15pF
- Low power dissipation: I_{CC} = 2μA (max) at Ta = 25°C
- High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min)
- 5.5-V Tolerant inputs.
- Wide operating voltage range: V_{CC} = 2 to 5.5V
- Identical pin assignment and function with TC7W04

Marking



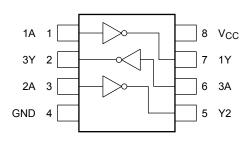


Weight SSOP8-P-0.65: 0.02 g (typ.) SSOP8-P-0.50A: 0.01 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	-0.5 to 7.0	V	
DC input voltage	V _{IN}	−0.5 to 7.0	٧	
DC output voltage	V _{OUT}	-0.5 to V_{CC} + 0.5	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	lok	±20 (Note1)	mA	
DC output current	lout	±25	mA	
DC V _{CC} /ground current	I _{CC}	±50	mA	
Power dissination	PD	300 (SM8)	mW	
Power dissipation	РΒ	200 (US8)	IIIVV	
Storage temperature	T _{stg}	-65 to 150	°C	
Lead temperature (10 s)	TL	260	°C	

Pin Assignment (top view)



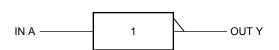
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

IEC Logic Symbol

Truth Table



Α	Υ
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V
	ui/uv	0 to 20 ($V_{CC} = 5.0 \pm 0.5 \text{ V}$)	115/V

2



Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition			Ta = 25°C		Ta = -40 to 85°C		Unit	
Characteristics	Gridiaciensiles Symbol Fest Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic	
		_		2.0	1.50	_	_	1.50		٧
High-level input voltage	V _{IH}			3.0 to 5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	
				2.0		_	0.50	_	0.50	
Low-level input voltage	V_{IL}		_		ı		V _{CC} × 0.3	_	V _{CC} × 0.3	٧
				2.0	1.9	2.0		1.9		V
High-level output voltage V _{OH} V _{IN} =		$V_{IN} = V_{IL}$	I _{OH} = -50 μA	3.0	2.9	3.0		2.9		
	V _{OH}			4.5	4.4	4.5		4.4		
			$I_{OH} = -4 \text{ mA}$	3.0	2.58	_		2.48		
		$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_		
		V _{IN} = V _{IH}	I _{OL} = 50 μA	2.0	_	0.0	0.1	_	0.1	V
				3.0	_	0.0	0.1	_	0.1	
Low-level output voltage	V_{OL}			4.5	_	0.0	0.1	_	0.1	
			$I_{OL} = 4 \text{ mA}$	3.0	_	_	0.36	_	0.44	
			$I_{OL} = 8 \text{ mA}$	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μΑ



AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics Symbo	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
	Syllibol		V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Offic
Frobauation delay time	^t pLH ^t pHL —	3.3 ± 0.3 5.0 ± 0.5	3.3 ± 0.3	15		5.0	7.1	1.0	8.5	- ns
				50		7.5	10.6	1.0	12.0	
			E O + O E	15	_	3.8	5.5	1.0	6.5	
			5.0 ± 0.5	50		5.3	7.5	1.0	8.5	
Input capacitance	C _{IN}		_			4	10	_	10	pF
Power dissipation capacitance	C_{PD}			(Note2)		18		_	_	pF

Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

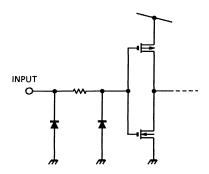
Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$$

Noise Characteristics (Ta = 25°C, input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Тур.	Limit	Unit
Characteriolics	Cymbol	rest definition	V _{CC} (V)	1) [7]	Limit	O m.c
Quiet output maximum dynamic V _{OL}	V _{OLP}	C _L = 50 pF	5.0	0.3	0.8	V
Quiet output minimum dynamic V _{OL}	V_{OLV}	C _L = 50 pF	5.0	-0.3	-0.8	V
Minimum high level dynamic V _{IH}	V _{IHD}	C _L = 50 pF	5.0	_	3.5	V
Maximum low level dynamic V _{IH}	V_{ILD}	C _L = 50 pF	5.0	_	1.5	V

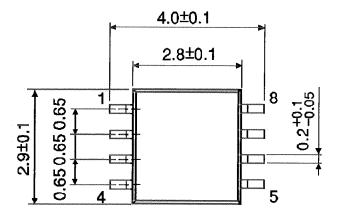
Input Equivalent Circuit

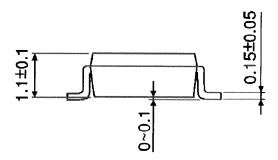




Package Dimensions

SSOP8-P-0.65 Unit: mm





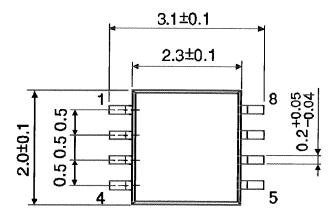
5

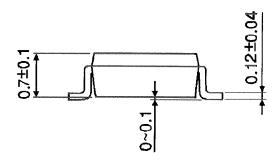
Weight: 0.02 g (typ.)



Package Dimensions

SSOP8-P-0.50A Unit: mm





6

Weight: 0.01 g (typ.)

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