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# Panasonic ideas for life 

## VS MODULE



Weight 0.7 g
Pin Grid 2.54
Dimensions $18.5 \times 3.3 \times 6.3$
PCB hole dia 0.6

Relay connection - In contrast to connecting between pins 2 and 4 connecting contact $K$ across pins 2 and 3 of the new VS monolithic module (as shown above) ensures reliable switchig even with bouncing signals. On the earlier design of thick film VS module, the contact K had to be connected between pins 2 and 4 in all cases.

Traditional stepper relays maintain their latched position mechanically, but by using the VS switching principle with polarised 2 coil relays this is not required. The already well known operating characteristics of polarised relays remain unchanged. Switching of the relay using the VS module is achieved as shown in the above diagram. A relay contact is required for the internal circuitry and is thus not available for other switching circuits. This applies to changeover contacts.
The VS5-24V Electronic module is suitable for 4 V to 30 V coil voltage. It withstands temperatures between $-55^{\circ} \mathrm{C}$ to $+125^{\circ} \mathrm{C}$ and can be operated between

A logic level of 3.6 V for the logic circuitry is obtained as shown in the block diagram, from the voltage regulator. A relay contact K is connected between pin 2 and the positive terminal 3 of the power supply. On receiving a signal from the output of the schmitt trigger the pulse generator gives a 10 micro second impulse which changes the logical state at input 1 of both AND gates to "high". The logical state of input 2 of gate $A$ is „low" since the relay contact $K$ is open: input 2 of gate $B$ is „high" due to the inverter I depending on the input signals, the output of the AND gate $B$ is driven "high" and the flip flop F/F is set switching the output "Q" high and turning on the transistor Ts.
The relay is then latched by coil L2 and the contact K closes.
With contact $K$ now closed and the voltage $U$ applied, the logical state of input 2 goes „high" so that both inputs 2 of the AND gates change state from their previous condition.
(Input 2 of AND gate A goes high and input 2 of AND gate B goes „low"). Simultaneously a 10 microsecond impulse from the pulse generator is fed to the input 1 of both AND gates consequently the output of gate A goes „high" and the output of gate B goes "low". The F/F is reset, the transistor Tr is driven on by output "Q", and the relay coil L1 is energized, opening the "ontact K.
The maximum output current of the VS module is 100 mA . Thus depending on coil resistance several relays can be controlled by a single VS module.

The more important characteristics of relay types which can be combined with the VS module are described on the rear of this datasheet.
$-20^{\circ} \mathrm{C}$ and $+80^{\circ} \mathrm{C}$.

## Operation

- High reliability and efficiency
- Low power requirement
- Short switching times
- Economic and spacesaving
- Universal application in stepper devices or counters
- Suitable for most common washing methods except ultrasonic cleaning


## Block diagram of VS module (For connection diagram see over)



## Stepping devices



Binary counters, Frequency dividers


## Counters



Characteristics Further realy data is availabe on the appropiaie data sheets.


