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BUK127-50DL

DESCRIPTION

Monolithic temperature and overload protected logic level power MOSFET in TOPFET2 technology assembled in a 3 pin surface mount plastic package.

APPLICATIONS

General purpose switch for driving

- lamps
- motors
- solenoids
- heaters

in automotive systems and other applications.

FEATURES

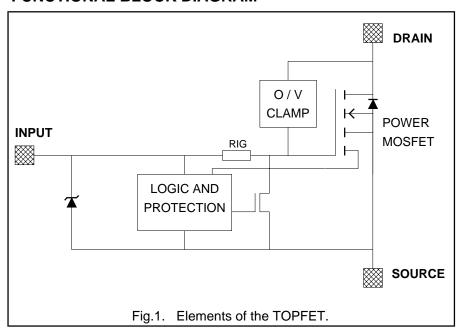
- TrenchMOS output stage
- **Current limiting**
- Overload protection

- Overtemperature protection
 Protection latched reset by input
 5 V logic compatible input level
- Control of output stage and supply of overload protection circuits derived from input
- Low operating input current permits direct drive by micro-controller
- ESD protection on all pins
- Overvoltage clamping for turn off of inductive loads

QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	UNIT
V _{DS}	Continuous drain source voltage		V
I _D	Continuous drain current	0.7	А
P _D	P _D Total power dissipation		W
T _j	Continuous junction temperature	150	°C
R _{DS(ON)}	Drain-source on-state resistance	200	mΩ

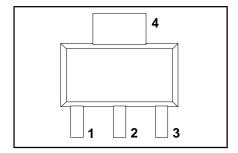
FUNCTIONAL BLOCK DIAGRAM



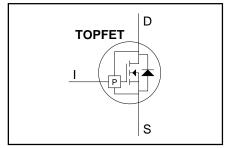
PINNING - SOT223

PIN	DESCRIPTION
1	input
2	drain
3	source
4	drain (tab)

PIN CONFIGURATION



SYMBOL



BUK127-50DL

LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{DS}	Continuous drain source voltage ¹	-	-	50	V
I _D	Continuous drain current ²	-	-	self limiting	A
l li	Continuous input current	clamping	-	3	mA
I _{IRM}	Non-repetitive peak input current	$t_p \le 1 \text{ ms}$	-	10	mA
P_{D}	Total power dissipation	$T_a = 25^{\circ}C$	-	1.8	W
T _{stq}	Storage temperature	-	-55	150	°C
T _j	Continuous junction temperature	normal operation ³	-	150	°C

ESD LIMITING VALUE

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _C	Electrostatic discharge capacitor voltage	Human body model; C = 250 pF; R = 1.5 kΩ	-	2	kV

OVERVOLTAGE CLAMPING LIMITING VALUES

At a drain source voltage above 50 V the power MOSFET is actively turned on to clamp overvoltage transients.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
E _{DSM}	Non-repetitive clamping energy	$T_a \le 25^{\circ}C; I_{DM} < I_{D(lim)};$	-	100	mJ
E _{DRM}	Repetitive clamping energy	inductive load $T_{sp} \le 125$ °C; $I_{DM} = 50$ mA; $f = 250$ Hz	-	5	mJ

OVERLOAD PROTECTION LIMITING VALUES

With the protection supply provided via the input pin, TOPFET can protect itself from short circuit loads. Overload protection operates by means of drain current limiting and activating the overtemperature protection.

SYMBOL	PARAMETER	REQUIRED CONDITION	MIN.	MAX.	UNIT
V_{DDP}	Protected drain source supply voltage	$V_{IS} \ge 4 \text{ V}$	-	35	V

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
R _{th j-sp} R _{th j-b} R _{th j-a}	Thermal resistance Junction to solder point Junction to board ⁴ Junction to ambient	Mounted on any PCB Mounted on PCB of fig. 22		12 40 -	18 - 70	K/W K/W K/W

¹ Prior to the onset of overvoltage clamping. For voltages above this value, safe operation is limited by the overvoltage clamping energy.

² Refer to OVERLOAD PROTECTION CHARACTERISTICS.

³ Not in an overload condition with drain current limiting.

⁴ Temperature measured 1.3 mm from tab.

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OUTPUT CHARACTERISTICS

Limits are for -40°C \leq T_{mb} \leq 150°C; typicals are for T_{mb} = 25 °C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Off-state	V _{IS} = 0 V				
$V_{(CL)DSS}$	Drain-source clamping voltage	I _D = 10 mA	50	-	-	V
		$I_D = 200 \text{ mA}; t_p \le 300 \mu\text{s}; \delta \le 0.01$	50	60	70	V
I _{DSS}	Drain source leakage current	$V_{DS} = 40 \text{ V}$	-	-	100	μΑ
		$T_{mb} = 25 ^{\circ}C$	-	0.1	10	μΑ
	On-state	$V_{IS} \ge 4 \text{ V; } t_p \le 300 \mu\text{s; } \delta \le 0.01$				
R _{DS(ON)}	Drain-source resistance	I _D = 100 mA	-	-	380	mΩ
, ,		$T_{mb} = 25 ^{\circ}C$	-	150	200	$m\Omega$

INPUT CHARACTERISTICS

The supply for the logic and overload protection is taken from the input. Limits are for $-40^{\circ}\text{C} \le T_{mb} \le 150^{\circ}\text{C}$; typicals are for $T_{mb} = 25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS		MIN.	TYP.	MAX.	UNIT
V _{IS(TO)}	Input threshold voltage	$V_{DS} = 5 \text{ V}; I_{D} = 1 \text{ mA}$	T 0=00	0.6	-	2.4	٧
			$T_{mb} = 25^{\circ}C$	1.1	1.6	2.1	V
I _{IS}	Input supply current	normal operation;	$V_{IS} = 5 V$	100	220	400	μΑ
			$V_{IS} = 4 V$	80	195	330	μΑ
I _{ISL}	Input supply current	protection latched;	$V_{IS} = 5 V$	200	400	650	μΑ
			$V_{IS} = 3 V$	130	250	430	μΑ
V_{ISR}	Protection reset voltage ¹	reset time $t_r \ge 100 \mu s$		1.5	2	2.9	V
t _{Ir}	Latch reset time	$V_{IS1} = 5 \text{ V}, V_{IS2} < 1 \text{ V}$		10	40	100	μs
$V_{(CL)IS}$	Input clamping voltage	I _I = 1.5 mA		5.5	-	8.5	V
R _{IG}	Input series resistance ² to gate of power MOSFET		$T_{mb} = 25^{\circ}C$	1	33	1	kΩ

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¹ The input voltage below which the overload protection circuits will be reset.

² Not directly measureable from device terminals.

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OVERLOAD PROTECTION CHARACTERISTICS

TOPFET switches off to protect itself when one of the overload thresholds is exceeded. It remains latched off until reset by the input.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
	Overload protection	-40° C $\leq T_{j} \leq 150^{\circ}$ C				
I _D	Drain current limiting	$V_{IS} = 5 \text{ V}$ $V_{IS} = 4.5 \text{ V}$ $V_{IS} = 4 \text{ V to } 5.5 \text{ V}$	0.8 0.7 0.6	1.3 - -	1.7 - 1.8	A A A
	Short circuit load protection	$V_{IS} = 5 V$				
P _{D(TO)}	Overload power threshold	for protection to operate	-	17	-	W
T _{DSC}	Characteristic time	which determines trip time ¹	-	1.6	ı	ms
	Overtemperature protection	from $I_D \ge 280 \text{ mA}$ or $V_{DS} \ge 100 \text{ mV}$				
$T_{j(TO)}$	Threshold junction temperature	V _{IS} = 4 V to 5.5 V	150	165	-	°C

SWITCHING CHARACTERISTICS

 $T_a = 25$ °C; resistive load $R_L = 50 \Omega$; adjust V_{DD} to obtain $I_D = 250$ mA; refer to test circuit and waveforms

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
t _{d on}	Turn-on delay time	V_{IS} : 0 V \Rightarrow 5 V	-	5	12	μs
t _r	Rise time		-	11	30	μs
t _{d off}	Turn-off delay time	V_{IS} : 5 V \Rightarrow 0 V	-	25	65	μs
t _f	Fall time		-	14	35	μs

REVERSE DIODE LIMITING VALUE

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Is	Continuous forward current	$T_{mb} \le 25$ °C; $V_{IS} = 0$ V	-	2	Α

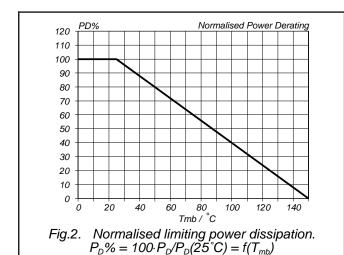
REVERSE DIODE CHARACTERISTICS

Limits are for -40°C \leq T_{mb} \leq 150°C; typicals are for T_{mb} = 25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{SDO}	Forward voltage	$I_S = 2 \text{ A}; V_{IS} = 0 \text{ V}; t_p = 300 \mu\text{s}$	-	0.83	1.1	V
t _{rr}	Reverse recovery time	not applicable ²	-	-	-	-

¹ Trip time $t_{d\,sc}$ varies with overload dissipation P_D according to the formula $t_{d\,sc} \approx T_{DSC}$ / [P_D / $P_{D(TO)}$ - 1].

² The reverse diode of this type is not intended for applications requiring fast reverse recovery.



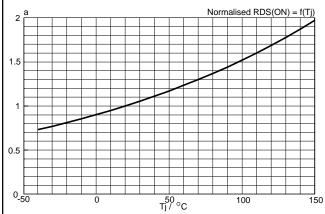
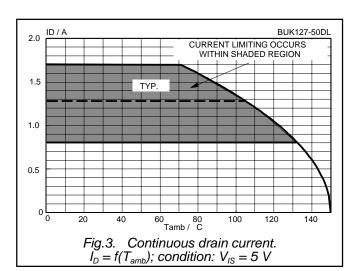
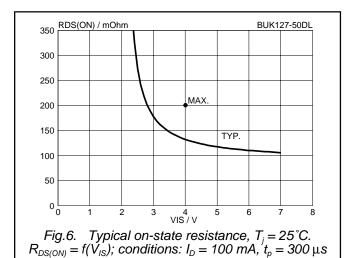
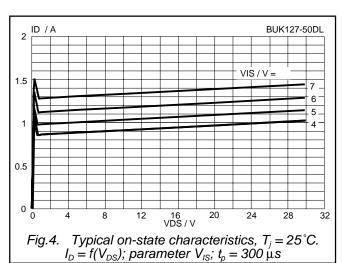
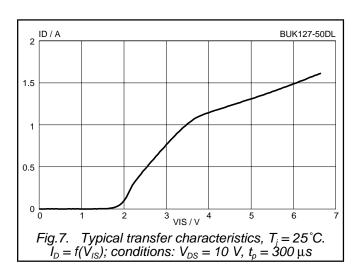


Fig.5. Normalised drain-source on-state resistance. $a = R_{DS(ON)}/R_{DS(ON)}25^{\circ}C = f(T_i); I_D = 100 \text{ mA}; V_{IS} = 4.4 \text{ V}$









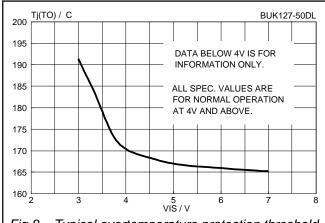
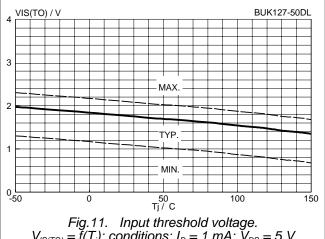


Fig.8. Typical overtemperature protection threshold. $T_{i(TO)} = f(V_{IS})$



 $V_{IS(TO)} = \tilde{f}(T_i)$; conditions: $I_D = 1$ mA; $\tilde{V}_{DS} = 5$ V

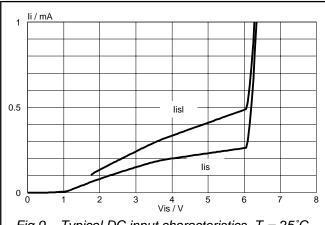


Fig.9. Typical DC input characteristics, $T_j = 25$ °C. I_{IS} & $I_{ISL} = f(V_{IS})$; normal operation & protection latched

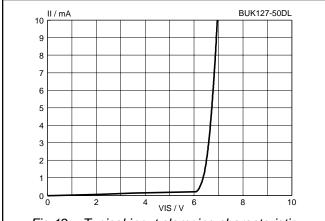
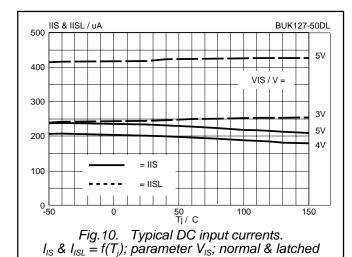


Fig.12. Typical input clamping characteristic. $I_1 = f(V_{IS})$; normal operation, $T_1 = 25$ °C.



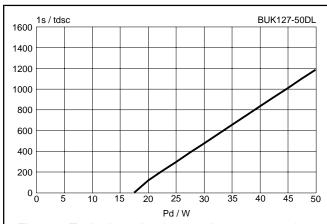
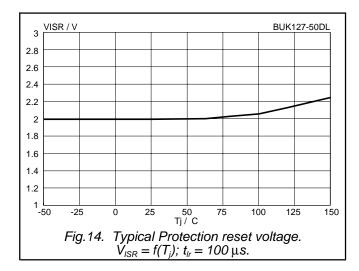


Fig.13. Typical overload protection response time. $1/t_{dsc} = f(P_D); \ V_{IS} \ge 4 \ V, \ T_j \le 125 \ C.$



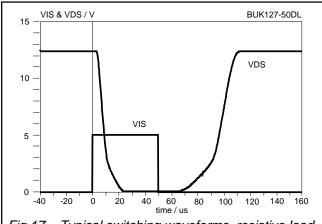


Fig.17. Typical switching waveforms, resistive load . $R_L = 50 \,\Omega$; adjust V_{DD} to obtain $I_D = 250$ mA; $T_j = 25^{\circ}$ C

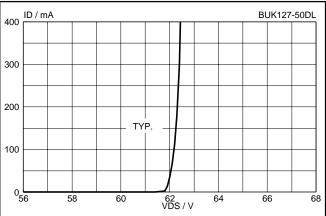


Fig.15. Overvoltage clamping characteristic, 25°C. $I_D = f(V_{DS})$; conditions: $V_{IS} = 0$ V; $t_p \le 300 \,\mu\text{s}$

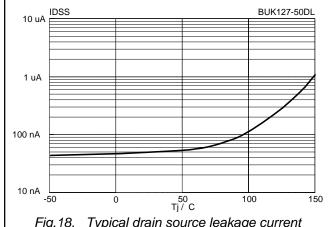


Fig. 18. Typical drain source leakage current $I_{DSS} = f(T_j)$; conditions: $V_{DS} = 40 \text{ V}$; $V_{IS} = 0 \text{ V}$.

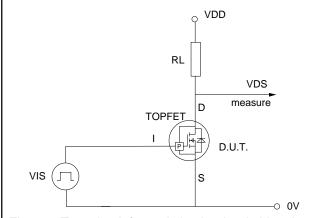
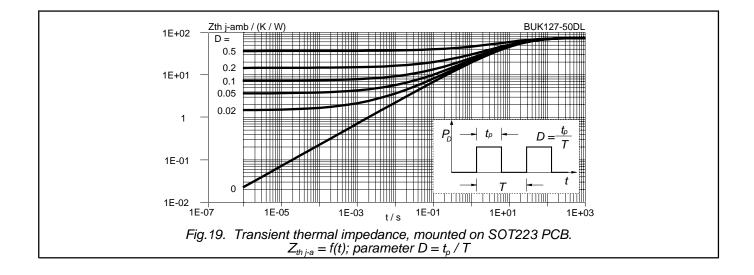
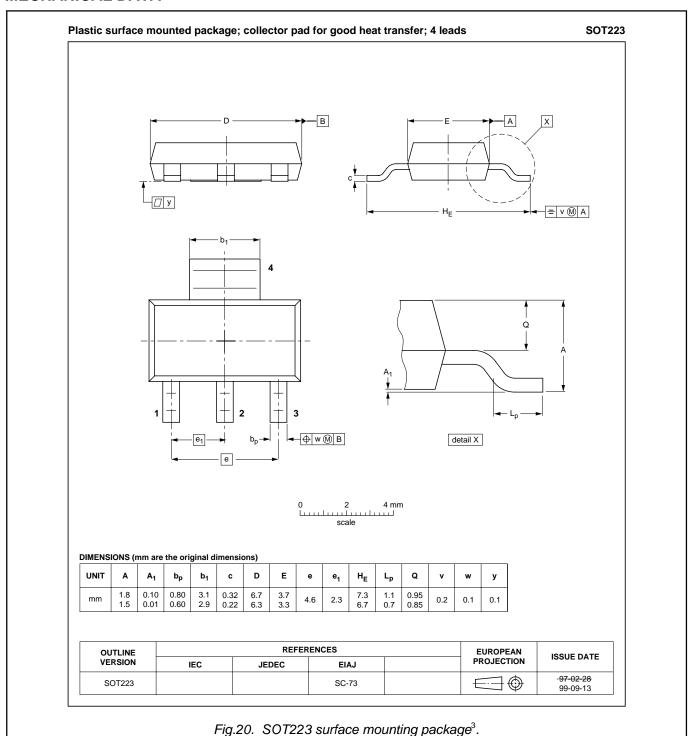


Fig.16. Test circuit for resistive load switching times. $V_{IS} = 5 \text{ V}$



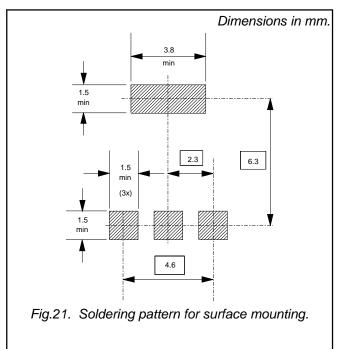
BUK127-50DL

MECHANICAL DATA



³ For further information, refer to surface mounting instructions for SOT223 envelope. Epoxy meets UL94 V0 at 1/8". Net Mass: 0.11 g

MOUNTING INSTRUCTIONS



PRINTED CIRCUIT BOARD

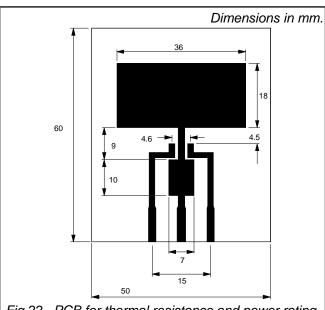


Fig.22. PCB for thermal resistance and power rating. PCB: FR4 epoxy glass (1.6 mm thick), copper laminate (35 μm thick).

Philips Semiconductors Product specification

PowerMOS transistor Logic level TOPFET

BUK127-50DL

DEFINITIONS

DATA SHEET STATUS				
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	PRODUCT STATUS ⁵ Development Qualification			

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