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SUPPLY VOLTAGE MONITOR

ISSUE 2 – NOVEMBER 1995

ZM33164

DEVICE DESCRIPTION

The ZM33164 is a three terminal under voltage monitor circuit for use in microprocessor systems. The threshold voltage of the device has been set to 4.3volts making it ideal for 5 volt circuits.

Included in the device is a precise voltage reference and a comparator with built in hysteresis to prevent erratic operation. The ZM33164 features an open collector output capable of sinking at least I0mA which only requires a single external resistor to interface to following circuits.

Operation of the device is guaranteed from one volt upwards, from this level to the device threshold voltage the output is held low providing a power on reset function. Should the supply voltage, once established, at any time drop below the threshold level then the output again will pull low.

The device is available in a TO92 package for through hole applications as well as SO8 and SOT223 for surface mount requirements.

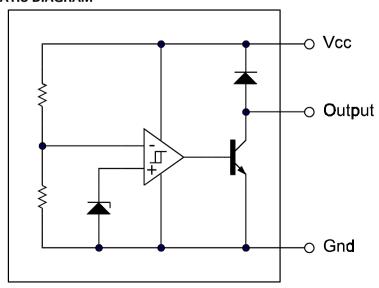
FEATURES

- SO8, SOT223 and TO92 packages
- Power on reset generator
- Automatic reset generation
- Low standby current
- Guaranteed operation from 1 volt
- Wide supply voltage range
- Internal clamp diode to discharge delay capacitor
- 4.3 volt threshold for 5 volt logic
- 60mV hysteresis prevents erratic operation

APPLICATIONS

- Microprocessor systems
- Computers
- Computer peripherals
- Instrumentation
- Automotive
- Battery powered equipment

SCHEMATIC DIAGRAM



ABSOLUTE MAXIMUM RATING

-1 to 12V **Power Dissipation** Input Supply Voltage

Offstate Output Voltage 12V TO92 780mW **Onstate Output** SOT223 2W(Note 2)

Sink Current(Note 1) Internally limited 780mW(Note 2) SO8 Clamp diode

Forward Current(Note 1) 100mA

Operating junction

temperature 150°C -40 to 85°C Operating Temperature -65 to 150°C Storage Temperature

TEST CONDITIONS

(T_{amb}=25°C for typical values, T_{amb}=-40 to 85°C for min/max values (Note3))

COMPARATOR

PARAMETER	SYMBOL	MIN	TYP.	MAX.	UNITS
Threshold Voltage High state output (Vcc increasing)	V _{IH}	4.15	4.33	4.45	V
Threshold Voltage Low state output (Vcc decreasing)	V _{IL}	4.15	4.27	4.45	V
Hysteresis	V _H	0.02	0.06		V

OUTPUT

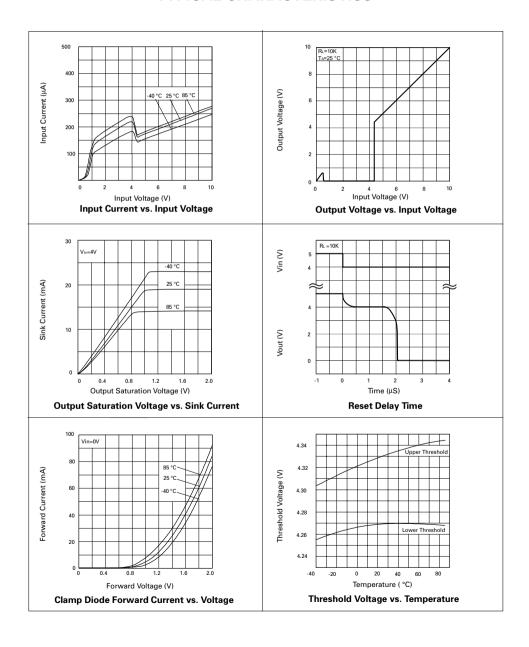
Output sink saturation:	V _{OL}				
(V _{cc} =4.0V, I _{sink} =8.0mA)			0.46	1.0	V
(V _{cc} =4.0V, I _{sink} =2.0mA)			0.15	0.4	V
(V _{cc} =1.0V, I _{sink} =0.1mA)				0.25	V
Onstate output sink current (V _{cc} , Output=4V)	I _{sink}	10	20	50	mA
Offstate output leakage current (V _{cc} , Output=5V)	I _{oh}		0.02	0.5	μΑ
Clamp diode forward voltage (I _f =10mA)	V _f	0.6	1.2	1.5	V
Propagation delay (V _{in} 5V to 4V, R _I =10k, T _{amb} =25°C)	T _d		2.0		μs

TOTAL DEVICE

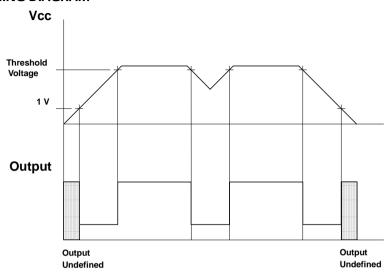
Operating input voltage range	V _{cc}	1.0 to 10			V
Quiescent input current (V _{cc} =5V)	Iq		175	260	μΑ

^{1.} Maximum package power dissipation must be observed
2. Maximum power dissipation, for the SOT223 and SO8 packages, is calculated assuming that the device is mounted on a PCB measuring 2 inches square.
3. Low duty cycle pulse techniques are used during test to maintain junction temperatures as close to ambient as possible

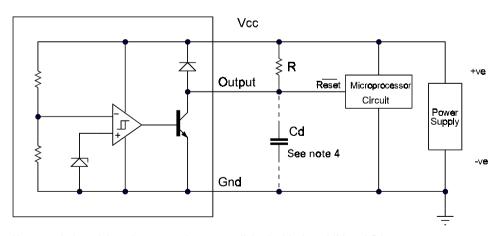
TYPICAL CHARACTERISTICS



TIMING DIAGRAM



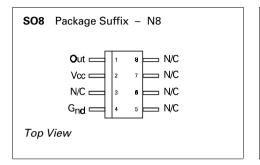
APPLICATION CIRCUIT

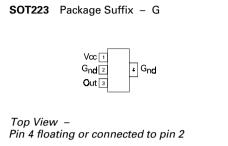


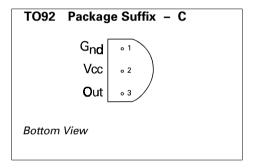
Note 4: A time delayed reset can be accomplished with the additional Cd.

$$T_{DY} = RCd \ln \left(\frac{1}{1 - \frac{V_{TH(mpu)}}{V_{in}}} \right)$$
 $T_{DY} = Time (Seconds)$ $V_{TH} = Microprocessor Reset Threshold $V_{in} = Power Supply Voltage$$

CONNECTION DIAGRAMS







ORDERING INFORMATION

Part Number	Package	Part Mark
ZM33164N8	S08	ZM33164
ZM33164G	SOT223	ZM33164
ZM33164C	TO92	ZM33164