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	CPC1973Y	Units
Blocking Voltage	400	V_P
Load Current	0.35	A_{rms}
On-Resistance	5	Ω

Features

- Power SIP Package
- Handle Load Currents Up to 0.5A
- High Reliability
- No Moving Parts
- Low Drive Power Requirements (TTL/CMOS Compatible)
- Arc-Free With No Snubbing Circuits
- 2500V_{rms} Input/Output Isolation
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable

Applications

- Industrial Controls
- Motor Control
- Robotics
- Medical Equipment—Patient/Equipment Isolation
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Meters (Watt-Hour, Water, Gas)
- IC Equipment
- Home Appliances

Description

Clare and IXYS have combined to bring OptoMOS® technology, reliability and compact size to a new family of high power solid state relays. As part of that family, the CPC1973Y is a 1-Form-A solid state relay. The CPC1973Y employs optically coupled MOSFET technology to provide 2500V_{rms} of input to output isolation. The efficient MOSFET switches and photovoltaic die use Clare's patented OptoMOS architecture while the input is controlled by a highly efficient GaAlAs infrared LED. The combination of low on resistance and high load current handling capabilities makes the relay suitable for a variety of high performance switching applications.

Approvals

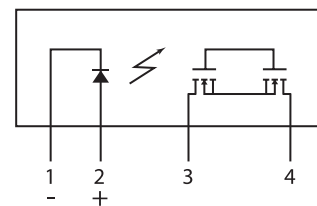
- UL recognized component: File # E69938
- Certified to: UL 508

Ordering Information

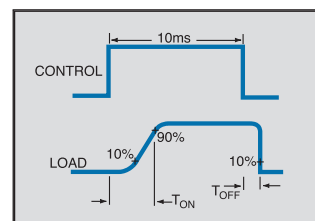
Part #	Description
CPC1973Y	Power SIP Package (25 per tube)

Pin Configuration

CPC1973Y Pinout



Switching Characteristics of Normally Open (Form A) Devices



Absolute Maximum Ratings (@ 25° C)

Parameter	Ratings	Units
Blocking Voltage	400	V _P
Reverse Input Voltage	5	V
Input control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
Isolation voltage Input to Output	2500	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate Linearly 3.33 mw / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Electrical Characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics @ 25°C						
Load Current, Continuous	free air	I _L	-	-	0.35	A _{rms}
Peak Load Current	T=10ms	I _{LPK}	-	-	3.5	A _{rms}
On-Resistance ¹	I _L =350mA	R _{ON}	-	3.4	5	Ω
Off-State Leakage Current	V _L =400V	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =10mA, V _L =10V	T _{ON}	-	-	5	ms
Turn-Off	I _F =10mA, V _L =10V	T _{OFF}	-	-	3	ms
Input Characteristics @ 25°C						
Input Control Current	I _L =350mA	I _F	10	-	-	mA
Input Dropout Current	-	I _F	-	-	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Input/Output Characteristics @ 25°C						
Capacitance Input/Output	f=1MHz	C _{I/O}	-	2	-	pF

¹ Load current derates linearly from 600mA @ 25°C to 480mA @80°C.

² Measurement taken within 1 second of on time.

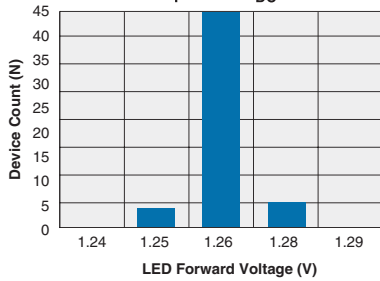
³ For applications requiring high temperature operation (greater than 60°C) an LED drive current of 3mA is recommended.

Thermal Characteristics

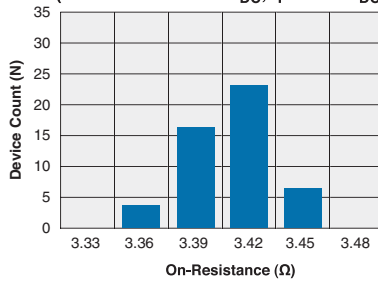
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Thermal Resistance (junction to case)	-	R _{θJC}	-	1.5	-	°C/W

PERFORMANCE DATA*

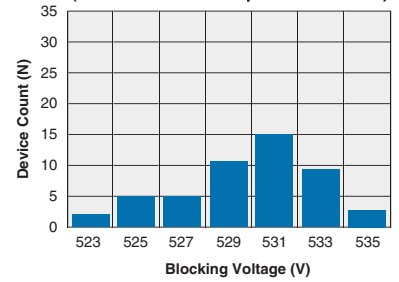
CPC1973Y
Typical LED Forward Voltage Drop
(N=50 Ambient Temperature = 25°C)
 $I_F = 10\text{mA}_{DC}$



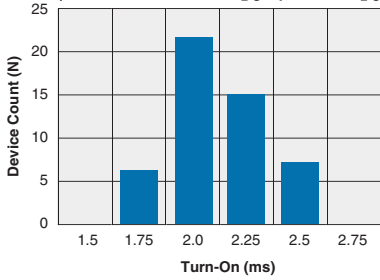
CPC1973Y
Typical On-Resistance Distribution
(N=50 Ambient Temperature = 25°C)
(Load Current = $0.5A_{DC}$; $I_F = 10\text{mA}_{DC}$)



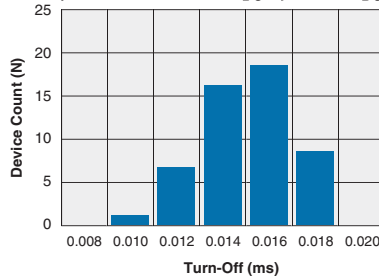
CPC1973Y
Typical Blocking Voltage Distribution
(N=50 Ambient Temperature = 25°C)



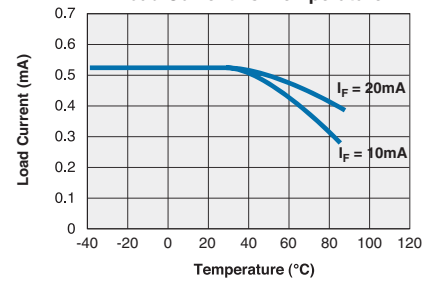
CPC1973Y
Typical Turn-On Time
(N=50 Ambient Temperature = 25°C)
(Load Current = $0.5A_{DC}$; $I_F = 10\text{mA}_{DC}$)



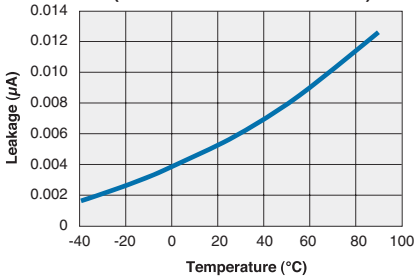
CPC1973Y
Typical Turn-Off Time
(N=50 Ambient Temperature = 25°C)
(Load Current = $0.5A_{DC}$; $I_F = 10\text{mA}_{DC}$)



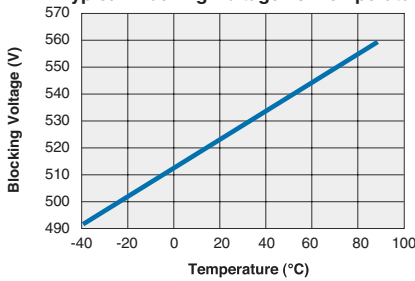
CPC1973Y
Typical Maximum DC
Load Current vs. Temperature



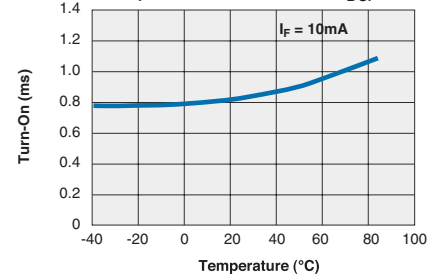
CPC1973Y
Typical Leakage vs. Temperature at
Maximum Rated Voltage
(Measured across Pins 3 & 4)



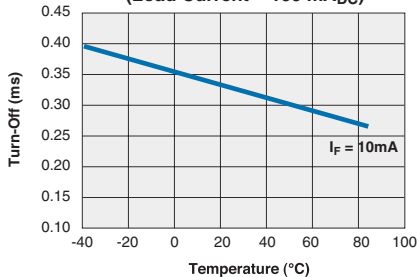
CPC1973Y
Typical Blocking Voltage vs. Temperature



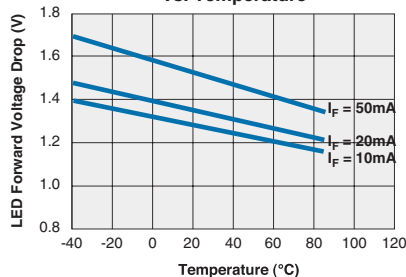
CPC1973Y
Typical Turn-On vs. Temperature
(Load Current = 100mA_{DC})



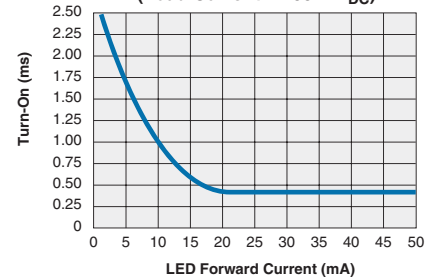
CPC1973Y
Typical Turn-Off vs. Temperature
(Load Current = 100mA_{DC})



CPC1973Y
Typical LED Forward Voltage Drop
vs. Temperature

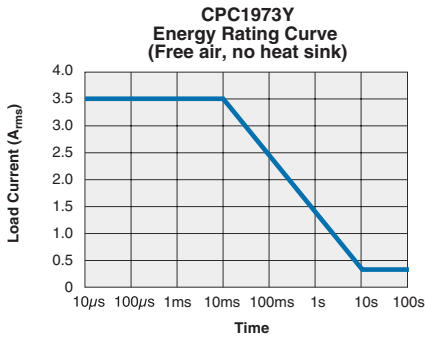
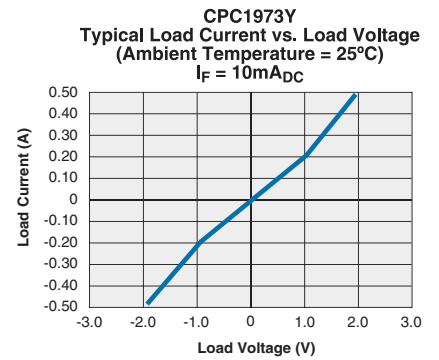
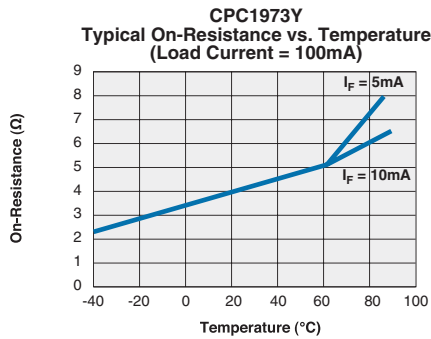
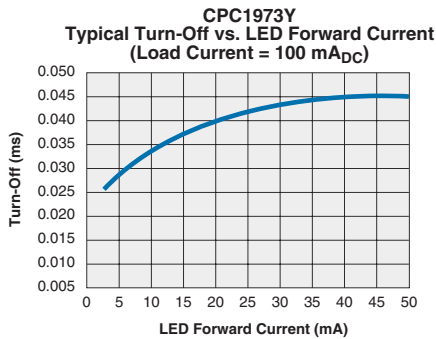


CPC1973Y
Typical Turn-On vs. LED Forward Current
(Load Current = 100mA_{DC})



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA *



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

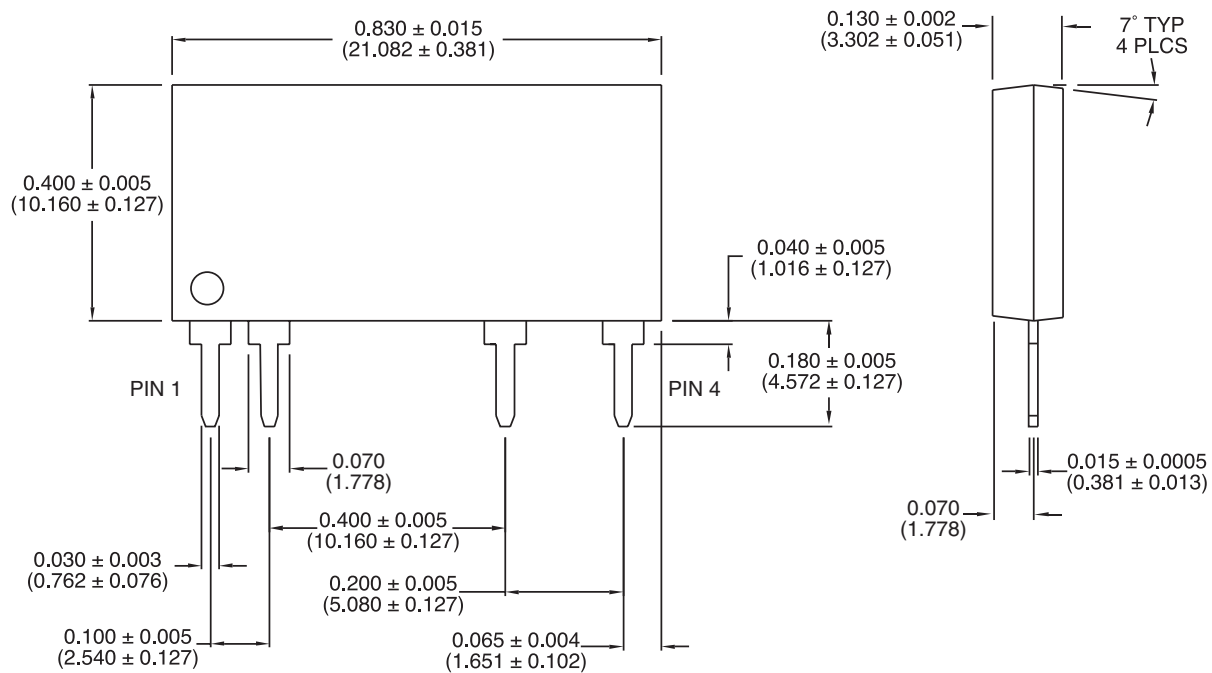
Soldering

Recommended soldering processes are limited to 245°C component body temperature for 10 seconds.

Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.

MECHANICAL DIMENSIONS



NOTE: Pin location tolerances are non-cumulative.

Dimensions:
inches
(mm)

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