## 阅读申明

- 1.本站收集的数据手册和产品资料都来自互联网,版权归原作者所有。如读者和版权方有任何异议请及时告之,我们将妥善解决。
- 2.本站提供的中文数据手册是英文数据手册的中文翻译,其目的是协助用户阅读,该译文无法自动跟随原稿更新,同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。
- 3.本站提供的产品资料,来自厂商的技术支持或者使用者的心得体会等,其内容可能存在描 叙上的差异,建议读者做出适当判断。
- 4.如需与我们联系,请发邮件到marketing@iczoom.com,主题请标有"数据手册"字样。

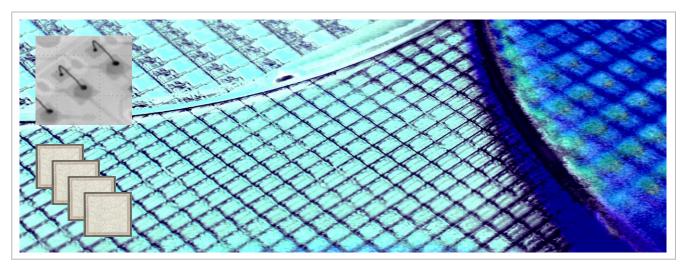
# **Read Statement**

- 1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.
- 2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.
- 3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.
- 4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets".



# Wire Bonding Silicon Vertical Capacitor WBSC142.xxx

**Rev 3.2** 



#### **Key features**

- Full compatible to monolithic ceramic capacitors
- Ultra high stability of capacitance value:
  - Temperature ± 1 % (-55 °C to +150 °C)
  - Voltage < 0.1 % / Volts</li>
  - Negligible capacitance loss through ageing
- Custom sizes, values, shapes, tolerances and higher voltage
- Low leakage current down to 100 pA
- Low profile

## **Key applications**

- Any demanding applications, such as medical, aerospace, automotive industrial...
- Applicable for standard wire bonding approach (Top & Bottom Gold metalizations)
- Decoupling / Filtering / Charge pump (i.e: Pacemakers / defibrillators)
- High reliability applications
- Downsizing

Thanks to the unique IPDiA Silicon capacitor technology, most of the problems encountered in demanding applications can be solved.

The capacitor integration capability (up to 250nF/mm²) allows **smaller footprint** than ceramic alternative to answer strong volumes constraints.

This technology provides industry leading performances relative to the capacitor stability over the full operating voltage & temperature range.

This technology also offers **high reliability**, up to 10 times better than ceramic capacitors and eliminates cracks phenomenon.

The IPDiA technology is the most appropriate solution for Chip On Board, Chip On Foil, Chip On Glass, Chip On Ceramic, flip chip and embedded applications, when designers are looking at **utmost decoupling behaviours.** 

This Silicon based technology is ROHS compliant and compatible with lead free reflow soldering process.







#### **Electrical specification**

(\*) Other values on request

		Capacitance value									
		10	15	22	33	47	68				
Unit	1pF	10pF: 935.142.522.210 935.142.528.210	15pF: 935.142.528.215	22pF: 935.142.528.222	33pF: 935.142.528.233	47pF: 935.142.528.247	68pF: 935.142.528.268				
	10pF	935.142.522.310 935.142.522.310 935.142.521.310	935.142.526.215 150pF: 935.142.522.315 935.142.528.315	220pF: 935.142.528.322	935.142.528.233 330pF: 935.142.528.333	Contact IPDIA Sales	680pF: 935.142.521.368				
	0.1nF	1nF: 935.142.521.410	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales				
	1nF	10nF: 935.142.620.510	Contact IPDIA Sales	22nF: 935.142.827.522 935.142.624.522	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales				
	10nF	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales	Contact IPDIA Sales:	Contact IPDIA Sales				

<u>Parameters</u>	<u>Value</u>			
Capacitance range	10pF to 22nF			
Capacitance tolerances	±15% <sup>(*)</sup>			
Operating temperature range	-55 to 150 °C <sup>(*)</sup>			
Storage temperatures	- 70 to 165 °C			
Temperature coefficient	±1%, from -55 to +150°C			
Breakdown Voltage (BV)	150, 50, 30 <sup>(*)</sup>			
Capacitance variation versus RVDC	0.1 % /V (from 0 V to RVDC)			
Equivalent Serial Inductor (ESL)	Max 100 pH			
Equivalent Serial Resistor (ESR)	Max 100 mΩ			
Insulation resistance	100GΩ @ 16V, from -55 to 150°C			
Aging	Negligible, < 0.001% / 1000h			
Reliability	FIT<0.017 parts / billions hours, RVDC, from -55 to +150°C			
Capacitor height	Мах 250µm <sup>(*)</sup>			

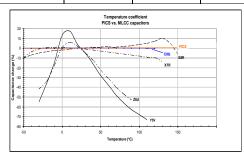


Fig.1 Capacitance change versus temperature variation compared to alternative technologies

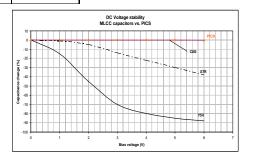
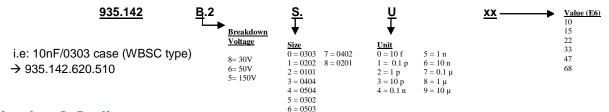


Fig.2 Capacitance change versus voltage variation compared to alternative

#### **Part Number**



#### **Termination & Outline**

#### Termination

Ti  $(0.1\mu m)/Ni(0.3\mu m)/Au(0.2\mu m)$  for bottom

Electrode metalization Top electrode : TiW/Au

Other finishings are available on request such as

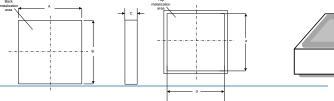
Aluminum or Copper (Cu:5um).

Applicable for standard wire bonding approach (ball and wedge).

Typical dimensions, all dimensions in mm.

#### Package outline

Тур.		0101	0201	0202	0303	0402	0404	0504
Comp.	Α	0.26 ±0.02	0.463 ±0.05	0.463 ±0.05	0.80 ±0.05	1.02 ±0.05	1.02 ±0.05	1.37 ±0.05
size	В	0.26 ±0.02	0.26 ±0.02	0.463 ±0.05	0.80 ±0.05	0.463 ±0.05	1.02 ±0.05	1.02 ±0.05



### **Packaging**

Tape and reel, tray, waffle pack or wafer delivery.

Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.



For more information, please visit: http://www.ipdia.com To contact us, email to: sales@ipdia.com

> Date of release: 28<sup>th</sup> February 2014 Document identifier: CL 431 111 615 147