

1.本站收集的数据手册和产品资料都来自互联网,版权归原作者所有。如读者和版权方有任 何异议请及时告之,我们将妥善解决。

本站提供的中文数据手册是英文数据手册的中文翻译,其目的是协助用户阅读,该译文无法自动跟随原稿更新,同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。

3.本站提供的产品资料,来自厂商的技术支持或者使用者的心得体会等,其内容可能存在描 叙上的差异,建议读者做出适当判断。

4.如需与我们联系,请发邮件到marketing@iczoom.com,主题请标有"数据手册"字样。

Read Statement

1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.

2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.

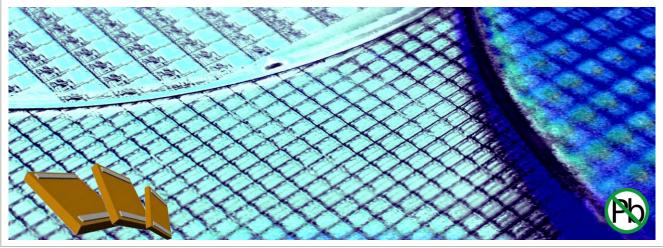
3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.

4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets".



XTSC429.xxx - 1812 Extreme Temperature Silicon Capacitor

Rev 3.1



Key features

- Ultra High temperature up to 250°C:
 - Temperature Coeff : <1.5% (-55 °C to +250 °C)
 - Voltage <0.1 %/V
 - Negligible capacitance loss through aging
- Unique high capacitance in EIA/1812 package size, up to 3.3 µF
- High reliability (FIT <0.017 parts / billion hours)</p>
- Low leakage current technology down to 3nA
- Low ESL and Low ESR
- Suitable for lead free reflow-soldering *Please refer to our assembly Application Note for further recommendations

Thanks to the unique IPDiA Silicon capacitor technology, most of the problems encountered in demanding applications can be solved.

EXtreme Temperature Silicon Capacitors are appropriate for applications used in extreme operating temperature range (up to 250°C).

XTSC industry leading performances allows to propose a **3.3µF in 1812** with a **TC<1,5%** over the full -55°C/+250°C temperature range.

This technology also offers a **negligible ageing** and a stable insulation resistance, even at very high temperature, as well as a stable capacitor value over the full operating.

Key applications

- 250°C requirements, High temperature applications, such as military, aerospace, automotive and down-hole industries.
- High reliability applications
- Replacement of X8R and COG dielectrics
- Decoupling / Filtering / Charge pump (i.e.: pressure sensor, motor management)
- Downsizing

The IPDiA technology features a capacitor integration capability (up to 250nF/mm²) which allows a capacitance value similar to X8R dielectric, but with better electrical performances than COG/NPO dielectrics.

This technology also offers **high reliability**, up to 10 times better than alternative capacitor technologies, such as Tantalum or MLCC, and eliminates cracking phenomena.

This Silicon based technology is RoHS compliant and compatible with lead free reflow soldering process.



Electrical specification

| | | Capacitance value | | | | | |
|-------------|--------|------------------------|--------------------------|--------------------------|--------------------------|----|----|
| | | 10 | 22 | 27 | 33 | 47 | 68 |
| <u>Unit</u> | 1 nF | | | | | | |
| | 10 nF | Contact IPDIA Sales | Contact IPDIA Sales | Contact IPDIA Sales | Contact IPDIA Sales | | |
| | 0,1 μF | 1µF 935.xxx.xxx.xxx | 2.2µF 935.xxx.xxx.xxx | 2.7µF 935.xxx.xxx.xxx | 3.3µF 935.133.429.733 | | |
| | 1 µF | | | | | | |

(*) Thinner thickness (as low as 100 µm thick) available, see Low Profile Silicon Capacitor product: LPSC

(**) Other values on request.

| Parameters | Value | | |
|--------------------------------------|---|--|--|
| Capacitance range | $1\mu F$ to $3.3\mu F^{(m)}$ | | |
| Capacitance tolerances | ±15 % ^(**) | | |
| Operating temperature range | -55 °C to 250 °C | | |
| Storage temperatures | - 70 °C to 265 °C | | |
| Temperature coefficient | <±1.5 %, from -55 °C to +250 °C | | |
| Breakdown voltage (BV) | 11 VDC ^(**) | | |
| Capacitance variation versus RVDC | 0.1 % /V (from 0 V to RVDC) | | |
| Equivalent Serial Inductor (ESL) | Max 1nH | | |
| Equivalent Serial Resistor (ESR) | Max 800mΩ ^(**) | | |
| Insulation resistance | 1GΩ min @ 3V,25°C 100MΩ min @ 3V,250°C | | |
| Ageing | Negligible, < 0.001 % / 1000 h | | |
| Reliability | FIT<0.017 parts / billion hours, | | |
| Capacitor height | Мах 400 µm ^(*) | | |

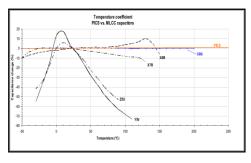


Fig.1 Capacitance change versus temperature variation compared with alternative dielectrics

Part Number

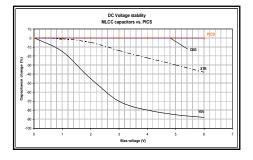


Fig.2 Capacitance change versus voltage variation compared with alternative dielectrics

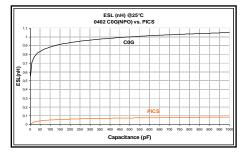
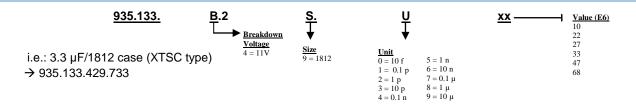


Fig.3 ESL versus capacitance value compared with alternative dielectrics



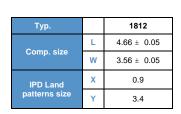
Termination and Outline

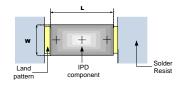
Termination

Lead-free nickel/solder coating compatible with automatic soldering technologies: reflow and manual

Typical dimensions, all dimensions in mm

Package outline





Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner. The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.



For more information, please visit: <u>http://www.ipdia.com</u> To contact us, email to: <u>sales@ipdia.com</u>

> Date of release: 28th February 2014 Document identifier: xxxxxxxxxxx