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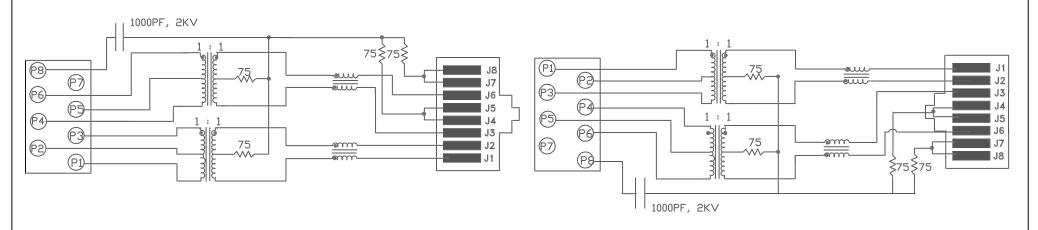
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LOWER ROW

UPPER ROW



ELECTRICAL SPECIFICATIONS:

1.0 TURNS RATIO (P1-P2-P3) : (J1-J2)

(P4-P5-P6) : (J3-J6)

(P3-P1)

2.0 INDUCTANCE (P6-P4)

3.0 LEAKAGE INDUCTANCE P6-P4 (WITH J6 AND J3 SHORT)

P3-P1 (WITH J2 AND J1 SHORT)

4.0 INTERWINDING CAPACITANCE (P6,P5,P4) TO (J6,J3)

(P3,P2,P1) TO (J2,J1)

<u>NOTES</u>

1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.

: 1CT : 1 ± 3%

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: 350uH MIN. @ 0.1V , 100KHz, 8mA DC Bias

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: 0,3uH MAX. @ 1MHz

: 0.3uH MAX. @ 1MHz

: 25pf TYP @ 1MHz

: 25pf TYP @ 1MHZ

5.0 DC RESISTANCE (J6-J3)=(J1-J2) : 1.2 ohms Max.

(P6-P4) : .7 ohms Max.

(P3-P1) : .7 ohms Max.

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6.0 RETURN LOSS:

1MHz T□ 30MHz : 18dB MIN.

60MHz T□ 80MHz : 12dB MIN.

7.0 VOLTAGE WITHSTAND:

(J1, J2) TD (P1, P2) : 1500 VAC

(J3, J6) TD (P3,P6) : 1500 VAC

8.0 INSERTION LOSS:

100KHz T□ 100MHz : 1.1 dB TYP

9.0 RISE TIME:

DUTPUT VOLTAGE = 1 V peak 3.0 nS TYP

PULSE WIDTH= 112nS 3.0 nS TYP

10.0 CROSS TALK:

1MHz T□ 100MHz 40 dB TYP

11.0 COMMON TO COMMON MODE ATTENUATION:

1MHz T□ 100MHz 35dB TYP

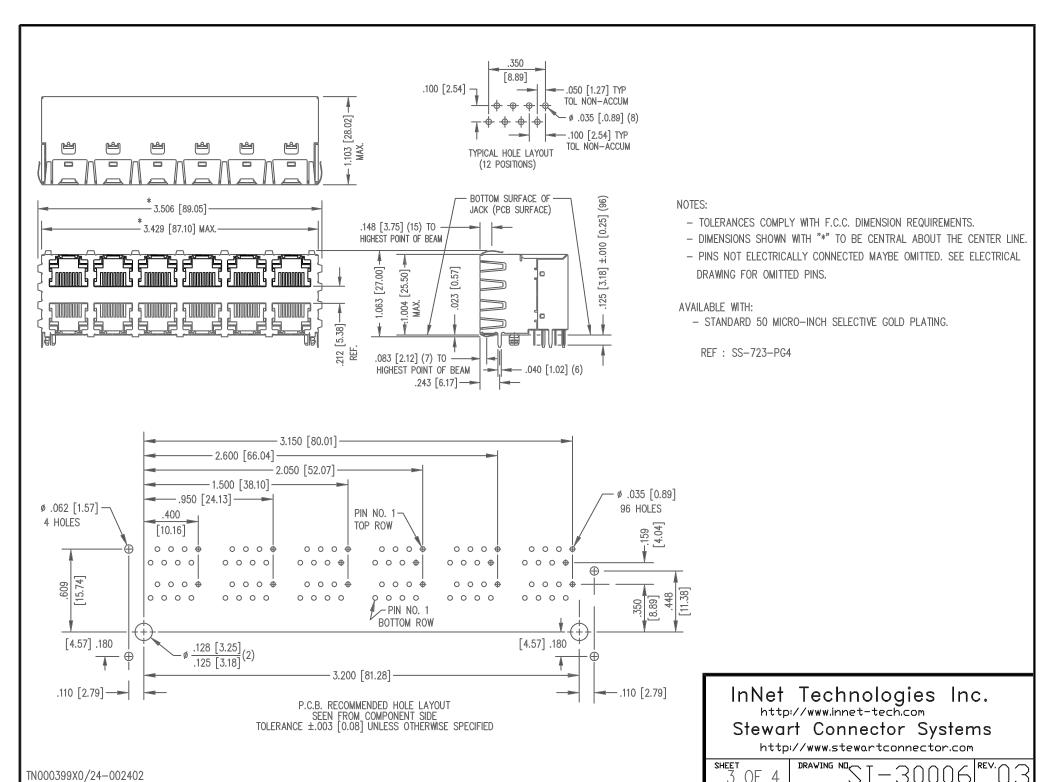
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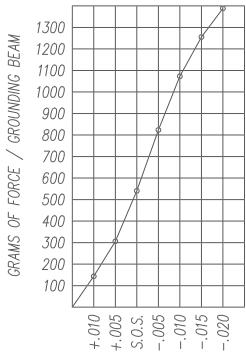
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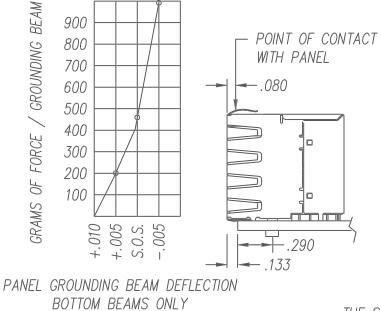
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PANEL GROUNDING BEAM DEFLECTION
TOP AND SIDE BEAMS
S.O.S. = SUGGESTED OPENING SIZE



S.O.S. = SUGGESTED OPENING SIZE

R.010 (4) -.015 FROM TOP

MAX. OF PCB TO BOTTOM

OF OPENING

SUGGESTED PANEL OPENING (N.T.S.)

TN000399X0/24-002402

THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH(S) ARE GENERAL AVERAGES TAKEN AT A POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE TOP AND SIDES, WITH .015 CLEARANCE ON THE BOTTOM.

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