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MODEL TC70



32.7680 KHZ CLOCK OSCILLATOR

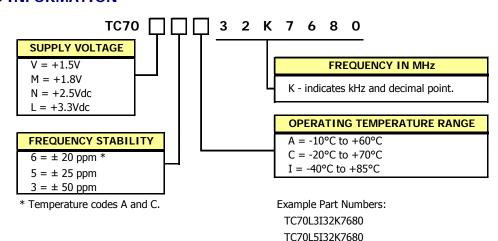
FEATURES

- 32.7680 kHz Frequency Reference
- Package Size 7.0mm x 5.0mm
- Fundamental Crystal Design
- Hermetic Ceramic Package
- Frequency Stability, ±50 ppm Standard
- Operating Temperature, -40°C to +85°C Standard
- Tape & Reel Packaging, EIA-418
- RoHS/Green Compliant (6/6)

APPLICATIONS

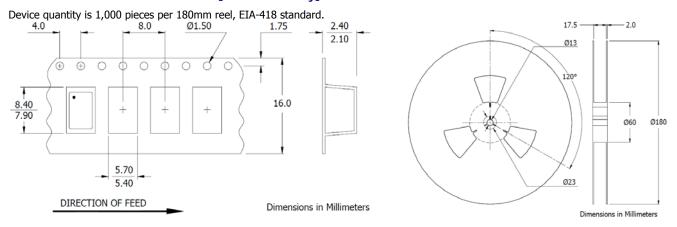
Model TC70 is ideal for use in a wide range of communication equipment, measurement equipment, industrial applications, automotive electronics, wireless communications, PDAs, mobile phones and notebooks.

ORDERING INFORMATION



Not all performance combinations and frequencies may be available. Contact your local CTS Representative or CTS Customer Service for availability.

PACKAGING INFORMATION [Reference Only]



DOCUMENT NO. 008-0359-0

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REV. A

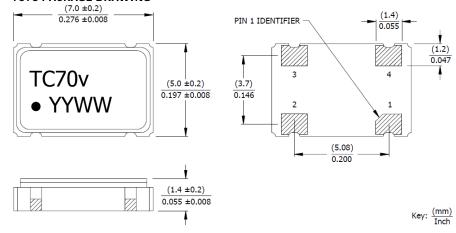


ELECTRICAL CHARACTERISTICS

	PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT	
	Frequency	f_0			32.7680		kHz	
	Operating Mode	-		AT Fundamental		-		
	Output Type			CMOS				
	Supply Voltage		±10%	1.5, 1.8, 2.5, 3.3		V		
S	Current			-	-	3.0	mA	
TER	Frequency Stability		See Ordering Information	20, 25, 50			ppm	
PARAMET	Operating Temperature Range	T _A		-10	-	+60	°C	
				-20	-	+70		
				-40	-	+85		
	Load Capacitance	C_L	CMOS	-	15	-	pF	
ICAL	Voltage Level [V _{OH}]			0.9V _{CC}	-	-	V	
	Voltage Level [V _{OL}]			-	-	$0.1V_{CC}$		
TR	Rise and Fall Time	T_r, T_f		-	50	200	ns	
ELEC	Symmetry			45	-	55	%	
EL	Start-up Time			-	5.0	10	ms	
	Tri-State Voltage [V _{OL}]			$0.7V_{CC}$	-	-	V	
	Tri-State Voltage [V _{OH}]			-	-	$0.3V_{CC}$	v	
	Aging	$\Delta f/f_0$	@+25°C, 1st year	1	3.0	5.0	± ppm	
	Storage Temperature Range	T_{STR}		-55	-	+125	°C	

MECHANICAL SPECIFICATIONS

TC70 PACKAGE DRAWING



MARKING INFORMATION

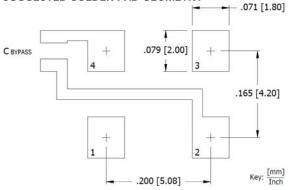
- 1. TC70 CTS Model Series.
- 4. v Voltage code. [L=3.3V, N=2.5V, M=1.8V, V=1.5V]
- 2. − Pin 1 identifier.
- 3. YYWW Date code, YY year, WW week.

Complete CTS part number, frequency value, date code and manufacturing site code information must appear on reel and carton labels.

NOTES

- 1. Termination pads (e4); barrier plating is nickel [Ni] with gold [Au] flash plate.
- 2. Reflow conditions per JEDEC J-STD-020; 260°C maximum, 20 seconds.
- 3. MSL = 1.

SUGGESTED SOLDER PAD GEOMETRY



 C_{BYPASS} should be ≥ 0.01 uF.

PIN ASSIGNMENTS

PIN	SYMBOL	DESCRIPTION	
1	EOH	Enable Input	
2	GND	Circuit & Package Ground	
3	Output	RF Output	
4	V_{CC}	Supply Voltage	