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- * LVPECL Output
- * Industry Standard Outline

- Applications**
- * Serial Communications
 - * Routers
 - * Switches
 - * WAN Interfaces
 - * Test Equipment



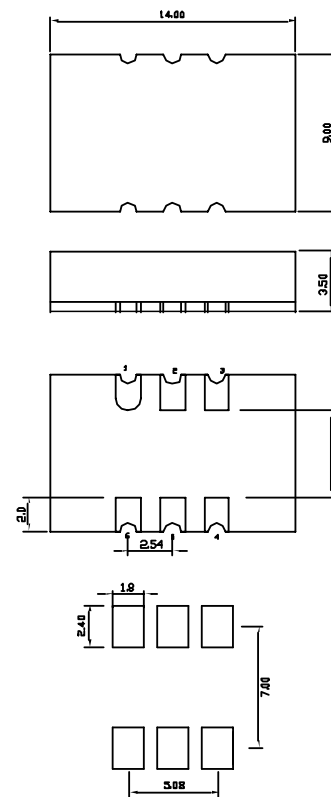
Part Numbering Example: CFED Z - A5 BP - 222.5792 TS

| CFED | Z | A5 | BP | 222.5792 | TS |
|--------|-----------------------------------|-----------------------------------------------------------------|----------------------------------|-----------|----------------|
| SERIES | PACKAGING OPTIONS | OPERATING TEMP. | STABILITY | FREQUENCY | TRI-STATE |
| CFED | Blank = Bulk Z = Tape and Reel | Blank = 0°C ~ +70°C A5 = -20°C ~ +70°C A7 = -40°C ~ +85°C | Blank = ±100 ppm BP = ±50 ppm | | TS = Tri-State |

| Specifications: | Min | Typ | Max | Unit |
|-------------------------------------------|---------------------------------|------|------------|--------|
| Frequency Range: | 38 | | 640 | MHz |
| Stability: | -50 | | + 50 | ppm |
| Supply Voltage: | 3.135 | 3.3 | 3.465 | V |
| Operating Temperature: | -40 | | + 85 | °C |
| Storage Temperature: | -55 | | + 125 | °C |
| Duty Cycle: | 45 | | 55 | % |
| Start-Up Time: | | 3 | 10 | mS |
| Aging: (ppm/1st Year) Ta=25C, Vdd=3.3V | | | 5 | ppm |
| Supply Current: | | | 100 | mA |
| Short Circuit Current: | | ± 50 | | mA |
| RMS Period Jitter: | | 5 | | pS |
| RMS Integrated Jitter: 12kHz to 20MHz | | 0.7 | | pS |
| Phase Noise @ 10kHz: | | | -120 | dBc/Hz |
| Output Voltage: Voh Vol | Vdd - 1.025 | | Vdd - 1.62 | V V |
| Rise/Fall Time: | | 0.4 | 0.8 | nS |
| Output Level: | LVPECL | | | |
| Packaging: | Tape and Reel 1000 pcs per Reel | | | |

CFED

Dimensions are in mm



- PIN Function**
- 1 DISABLE
 - 2 NO CONNECT
 - 3 Ground
 - 4 LVPECL +
 - 5 LVPECL -
 - 6 Vdd

LVPECL Levels Test Circuit

