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Molding Type Module IGBT, 2-in-1 Package, 1200 V, 400 A



PRODUCT SUMMARY						
V_{CES}	1200 V					
I _C at T _C = 80 °C	400 A					
$V_{CE(on)}$ (typical) at $I_C = 400$ A, 25 °C	1.90 V					
Speed	8 kHz to 30 kHz					
Package	Double INT-A-PAK					
Circuit	Half bridge					

FEATURES

- Low V_{CE(on)} trench IGBT technology
- 10 µs short circuit capability



- \bullet $\,V_{\text{CE(on)}}$ with positive temperature coefficient
- Maximum junction temperature 175 °C
- · Low inductance case
- Fast and soft reverse recovery antiparallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

TYPICAL APPLICATIONS

- Inverter for motor drive
- AC and DC servo drive amplifier
- Uninterruptible power supply (UPS)

DESCRIPTION

Vishay's IGBT power module provides ultralow conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
Gate to emitter voltage	V _{GES}		± 30	V	
Collector current		T _C = 25 °C	750		
Collector current	I _C	T _C = 80 °C	400		
Pulsed collector current	I _{CM}	t _p = 1 ms	800	А	
Diode continuous forward current	I _F		400		
Diode maximum forward current	I _{FM}	t _p = 1 ms	800		
Maximum power dissipation	P_{D}	T _J = 175 °C	2344	W	
RMS isolation voltage	V _{ISOL}	f = 50 Hz, t = 1 min	2500	V	
Operating junction temperature range	TJ		-40 to +150	°C	



IGBT ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS MIN. TY		TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-		
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 400 A, T _J = 25 °C	-	1.90	2.35		
Collector to enfitter voltage		$V_{GE} = 15 \text{ V}, I_{C} = 400 \text{ A}, T_{J} = 125 ^{\circ}\text{C}$	-	2.30	-] v	
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_{C} = 20$ mA, $T_{J} = 25$ °C	5.0	5.9	7.5		
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0$ V, $T_{J} = 25$ °C	-	-	5.0	mA	
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}, V_{CE} = 0 \text{ V}, T_J = 25 ^{\circ}\text{C}$	-	-	400	nA	

SWITCHING CHARACTERISTICS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	187	-	
Rise time	t _r]	-	57	-	ns mJ
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, I_{C} = 400 \text{ A}, R_{q} = 1.4 \Omega,$	-	180	-	
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	149	-	
Turn-on switching loss	E _{on}	7	-	19.9	-	
Turn-off switching loss	E _{off}	1	-	18.8	-	
Turn-on delay time	t _{d(on)}		-	189	-	ns ns
Rise time	t _r	7	-	58	-	
Turn-off delay time	t _{d(off)}	$\begin{split} &V_{CC}=600~V,~I_{C}=400~A,~R_{g}=1.4~\Omega,\\ &V_{GE}=\pm~15~V,~T_{J}=125~^{\circ}C \end{split}$	-	187	-	
Fall time	t _f		-	220	-	
Turn-on switching loss	E _{on}]	-	31.2	-	I
Turn-off switching loss	E _{off}]	-	23.4	-	- mJ
Input capacitance	C _{ies}		-	51.2	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 30 V, f = 1.0 MHz	-	1.84	-	nF
Reverse transfer capacitance	C _{res}]	-	1.28	-	
SC data	I _{SC}	$t_p \leq 10~\mu s,~V_{GE} = 15~V,~T_J = 125~^{\circ}C,\\ V_{CC} = 900~V,~V_{CEM} \leq 1200~V$	-	3560	-	Α
Internal gate resistance	R _{gint}		-	0.59	-	Ω
Stray inductance	L _{CE}		-	-	18	nΗ
Module lead resistance, terminal to chip	R _{CC'+EE'}		-	0.32	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS (T _C = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Diode forward voltage	V _F	I _E = 400 A	T _J = 25 °C	-	1.80	2.20	V
blode forward voltage		1 _F = 400 A	T _J = 125 °C	-	1.85	-	
Deceyory charge	0		T _J = 25 °C	-	26	-	
Recovery charge	Q _{rr}	r	T _J = 125 °C	-	49	-	μC
Dools was away was a summent	I _{rr}	$I_F = 400 \text{ A}, V_R = 600 \text{ V},$ $R_g = 4.1 \Omega, V_{GE} = -15 \text{ V}$	T _J = 25 °C	-	212	-	^
Peak reverse recovery current			T _J = 125 °C	-	281	-	Α
Poverse recovery energy	Е		T _J = 25 °C	-	23.4	-	ml
Reverse recovery energy	E _{rec}		T _J = 125 °C	-	33.8	-	- mJ



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction tempera	ture	T _J max.		-	-	175	
Operating junction tempera	ture range	T_Jop		-40	-	150	°C
Storage temperature range		T _{STG}		-40	-	125	
Junction to case	IGBT			=.	-	0.064	
Junction to case	Diode	R_{thJC}		-	-	0.098	K/W
Case to sink		R _{thCS}	Conductive grease applied	-	0.032	-	
Mounting torque			Power terminal screw: M6	2.5 to 5.0		Nee	
			Mounting screw: M6	3.0 to 5.0		Nm	
Weight				350		g	

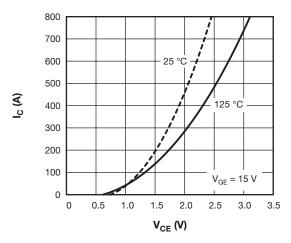


Fig. 1 - IGBT Output Characteristics

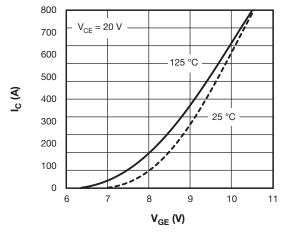


Fig. 2 - IGBT Transfer Characteristics

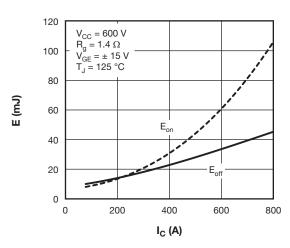


Fig. 3 - IGBT Switching Loss vs. I_C

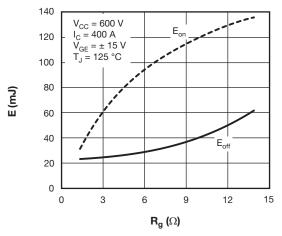


Fig. 4 - IGBT Switching Loss vs. R_q

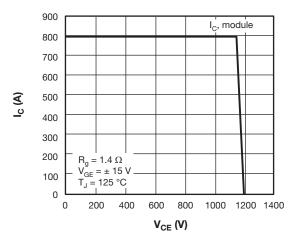


Fig. 5 - RBSOA

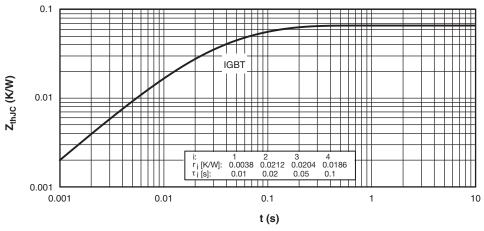


Fig. 6 - IGBT Transient Thermal Impedance

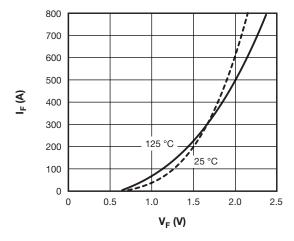


Fig. 7 - Diode Forward Characteristics

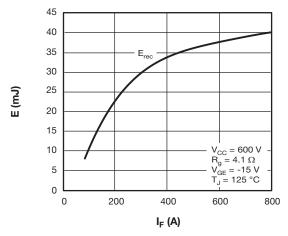


Fig. 8 - Diode Switching Loss vs. I_F

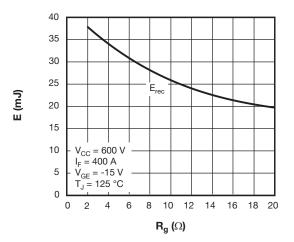


Fig. 9 - Diode Switching Loss vs. Rq

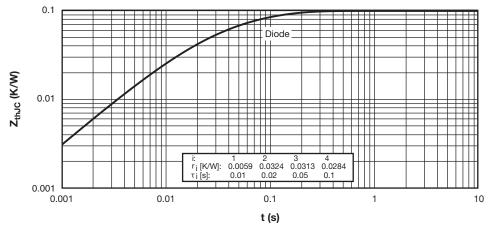
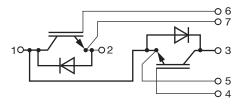


Fig. 10 - Diode Transient Thermal Impedances

CIRCUIT CONFIGURATION

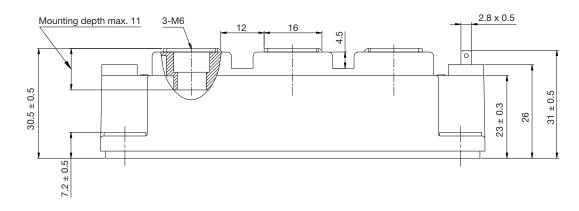


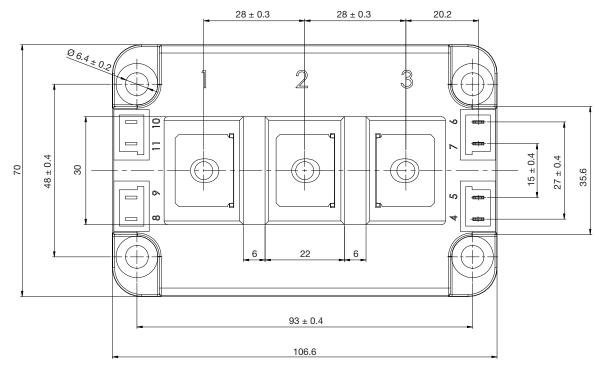
LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95538			



Double INT-A-PAK

DIMENSIONS in millimeters (inches)







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