

## 阅读申明

- 1.本站收集的数据手册和产品资料都来自互联网，版权归原作者所有。如读者和版权方有任何异议请及时告之，我们将妥善解决。
- 2.本站提供的中文数据手册是英文数据手册的中文翻译，其目的是协助用户阅读，该译文无法自动跟随原稿更新，同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。
- 3.本站提供的产品资料，来自厂商的技术支持或者使用者的心得体会等，其内容可能存在描述上的差异，建议读者做出适当判断。
- 4.如需与我们联系，请发邮件到marketing@iczoom.com，主题请标有“数据手册”字样。

## Read Statement

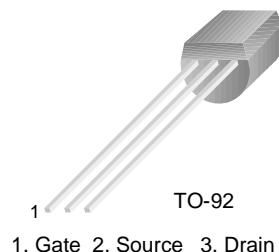
1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.
2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.
3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.
4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets" .

## 2N5952

2N5952

### N-Channel RF Amplifier

- This device is designed primarily for electronic switching applications such as low on resistance analog switching.
- Sourced from process 50.



### Absolute Maximum Ratings \* $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
$V_{DG}$	Drain-Gate Voltage	30	V
$V_{GS}$	Gate-Source Voltage	-30	V
$I_{GF}$	Forward Gate Current	10	mA
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 ~ +150	$^\circ\text{C}$

\* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

1. These ratings are based on a maximum junction temperature of 150 degrees C.
2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

### Electrical Characteristics $T_C=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristics</b>						
$V_{(BR)GSS}$	Gate-Source Breakdown Voltage	$V_{DS} = 0, I_G = -1.0\mu\text{A}$	-30			V
$I_{GSS}$	Gate Reverse Current	$V_{GS} = -15\text{V}, V_{DS} = 0$			-1.0	nA
$V_{GS(off)}$	Gate-Source Cutoff Voltage	$V_{DS} = 15\text{V}, I_D = 100\text{nA}$	-1.3		-3.5	V
<b>On Characteristics</b>						
$I_{DSS}$	Zero-Gate Voltage Drain Current *	$V_{DS} = 15\text{V}, V_{GS} = 0$	4.0		8.0	mA
<b>Small Signal Characteristics</b>						
$g_{fs}$	Forward Transfer Conductance	$V_{DS} = 15\text{V}, V_{GS} = 0, f = 1.0\text{kHz}$	2000		6500	$\mu\text{mhos}$
$g_{os}$	Output Conductance	$V_{DS} = 15\text{V}, V_{GS} = 0, f = 100\text{MHz}$			75	$\mu\text{mhos}$
$C_{iss}$	Input Capacitance	$V_{DS} = 15\text{V}, V_{GS} = 0, f = 1.0\text{MHz}$			6.0	pF
$C_{rss}$	Reverse Transfer Capacitance	$V_{DS} = 15\text{V}, V_{GS} = 0, f = 1.0\text{MHz}$			2.0	pF
NF	Noise Figure	$V_{DS} = 15\text{V}, R_G = 1.0\text{k}\Omega, f = 1.0\text{kHz}$			2.0	dB

\* Pulse Test: Pulse Width  $\leq 300\text{ms}$ , Duty Cycle  $\leq 1.0\%$

### Thermal Characteristics $T_A=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Max.	Units
$P_D$	Total Device Dissipation Derate above $25^\circ\text{C}$	350 2.8	mW mW/ $^\circ\text{C}$
$R_{\theta JC}$	Thermal Resistance, Junction to Case	125	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	$^\circ\text{C}/\text{W}$

# Package Dimensions

## TO-92



Dimensions in Millimeters

## TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE <sup>™</sup>	FACT <sup>™</sup>	ImpliedDisconnect <sup>™</sup>	PACMAN <sup>™</sup>	SPM <sup>™</sup>
ActiveArray <sup>™</sup>	FACT Quiet series <sup>™</sup>	ISOPLANAR <sup>™</sup>	POP <sup>™</sup>	Stealth <sup>™</sup>
Bottomless <sup>™</sup>	FAST <sup>®</sup>	LittleFET <sup>™</sup>	Power247 <sup>™</sup>	SuperSOT <sup>™</sup> -3
CoolFET <sup>™</sup>	FAST <sup>r</sup> <sup>™</sup>	MicroFET <sup>™</sup>	PowerTrench <sup>®</sup>	SuperSOT <sup>™</sup> -6
CROSSVOL <sup>™</sup>	FRFET <sup>™</sup>	MicroPak <sup>™</sup>	QFET <sup>™</sup>	SuperSOT <sup>™</sup> -8
DOME <sup>™</sup>	GlobalOptoisolator <sup>™</sup>	MICROWIRE <sup>™</sup>	QS <sup>™</sup>	SyncFET <sup>™</sup>
EcoSPARK <sup>™</sup>	GTO <sup>™</sup>	MSX <sup>™</sup>	QT Optoelectronics <sup>™</sup>	TinyLogic <sup>™</sup>
E <sup>2</sup> CMOS <sup>™</sup>	HiSeC <sup>™</sup>	MSXPro <sup>™</sup>	Quiet Series <sup>™</sup>	TruTranslation <sup>™</sup>
EnSigna <sup>™</sup>	I <sup>2</sup> C <sup>™</sup>	OCX <sup>™</sup>	RapidConfigure <sup>™</sup>	UHC <sup>™</sup>
Across the board. Around the world. <sup>™</sup>		OCXPro <sup>™</sup>	RapidConnect <sup>™</sup>	UltraFET <sup>®</sup>
The Power Franchise <sup>™</sup>		OPTOLOGIC <sup>®</sup>	SILENT SWITCHER <sup>®</sup>	VCX <sup>™</sup>
Programmable Active Droop <sup>™</sup>		OPTOPLANAR <sup>™</sup>	SMART START <sup>™</sup>	

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.