

阅读申明

- 1.本站收集的数据手册和产品资料都来自互联网，版权归原作者所有。如读者和版权方有任何异议请及时告之，我们将妥善解决。
- 2.本站提供的中文数据手册是英文数据手册的中文翻译，其目的是协助用户阅读，该译文无法自动跟随原稿更新，同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。
- 3.本站提供的产品资料，来自厂商的技术支持或者使用者的心得体会等，其内容可能存在描述上的差异，建议读者做出适当判断。
- 4.如需与我们联系，请发邮件到marketing@iczoom.com，主题请标有“数据手册”字样。

Read Statement

1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.
2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.
3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.
4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets" .

BC307B, BC307C

Amplifier Transistors

PNP Silicon

Features

- Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V_{CEO}	-45	Vdc
Collector – Base Voltage	V_{CBO}	-50	Vdc
Emitter – Base Voltage	V_{EBO}	-5.0	Vdc
Collector Current – Continuous	I_C	-100	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	350 2.8	mW mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	1.0 8.0	W mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-55 to +150	$^\circ\text{C}$

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

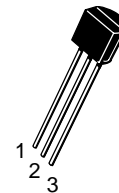
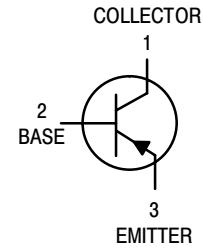
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	357	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	125	$^\circ\text{C/W}$

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



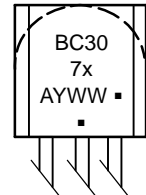
ON Semiconductor®

<http://onsemi.com>



TO-92
CASE 29
STYLE 17

MARKING DIAGRAM



BC307x = Device Code
x = B or C

A = Assembly Location

Y = Year

WW = Work Week

■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
BC307B	TO-92	5000 Units / Box
BC307BG	TO-92 (Pb-Free)	5000 Units / Box
BC307BRL1	TO-92	2000 / Tape & Reel
BC307BRL1G	TO-92 (Pb-Free)	2000 / Tape & Reel
BC307BZL1	TO-92	2000 / Ammo Box
BC307BZL1G	TO-92 (Pb-Free)	2000 / Ammo Box
BC307C	TO-92	5000 Units / Box
BC307CG	TO-92 (Pb-Free)	5000 Units / Box

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

BC307B, BC307C

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Breakdown Voltage (I _C = –2.0 mA _{dc} , I _B = 0)	V _{(BR)CEO}	–45	–	–	V _{dc}
Emitter–Base Breakdown Voltage (I _E = –100 μA _{dc} , I _C = 0)	V _{(BR)EBO}	–5.0	–	–	V _{dc}
Collector–Emitter Leakage Current (V _{CE} S = –50 V, V _{BE} = 0) (V _{CE} S = –50 V, V _{BE} = 0) T _A = 125°C	I _{CES}	–	–0.2	–15	nA _{dc} μA

ON CHARACTERISTICS

DC Current Gain (I _C = –10 μA _{dc} , V _{CE} = –5.0 V _{dc})	BC307B BC307C	h _{FE}	–	150	–	–
			–	270	–	–
(I _C = –2.0 mA _{dc} , V _{CE} = –5.0 V _{dc})	BC307	120	–	800		
	BC307B	200	290	460		
	BC307C	420	500	800		
(I _C = –100 mA _{dc} , V _{CE} = –5.0 V _{dc})	BC307B	–	180	–		
	BC307C	–	300	–		
Collector–Emitter Saturation Voltage (I _C = –10 mA _{dc} , I _B = –0.5 mA _{dc}) (I _C = –10 mA _{dc} , I _B = see Note 1) (I _C = –100 mA _{dc} , I _B = –5.0 mA _{dc})	V _{CE(sat)}	–	–0.10	–0.3	V _{dc}	
		–	–0.30	–0.6		
		–	–0.25	–		
Base–Emitter Saturation Voltage (I _C = –10 mA _{dc} , I _B = –0.5 mA _{dc}) (I _C = –100 mA _{dc} , I _B = –5.0 mA _{dc})	V _{BE(sat)}	–	–0.7	–	V _{dc}	
		–	–1.0	–		
Base–Emitter On Voltage (I _C = –2.0 mA _{dc} , V _{CE} = –5.0 V _{dc})	V _{BE(on)}	–0.55	–0.62	–0.7	V _{dc}	

DYNAMIC CHARACTERISTICS

Current–Gain – Bandwidth Product (I _C = –10 mA _{dc} , V _{CE} = –5.0 V _{dc} , f = 100 MHz)	f _T	–	280	–	MHz
Common Base Capacitance (V _{CB} = –10 V _{dc} , I _C = 0, f = 1.0 MHz)	C _{cbo}	–	–	6.0	pF
Noise Figure (I _C = –0.2 mA _{dc} , V _{CE} = –5.0 V _{dc} , R _S = 2.0 kΩ, f = 1.0 kHz)	NF	–	2.0	10	dB

1. I_C = –10 mA_{dc} on the constant base current characteristic, which yields the point I_C = –11 mA_{dc}, V_{CE} = –1.0 V.

TYPICAL CHARACTERISTICS

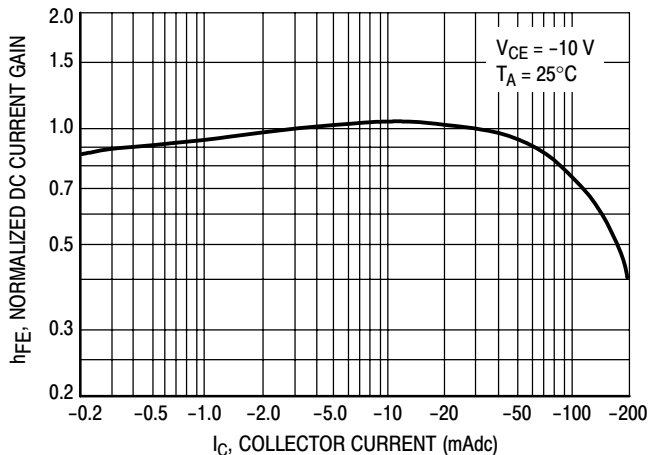


Figure 1. Normalized DC Current Gain

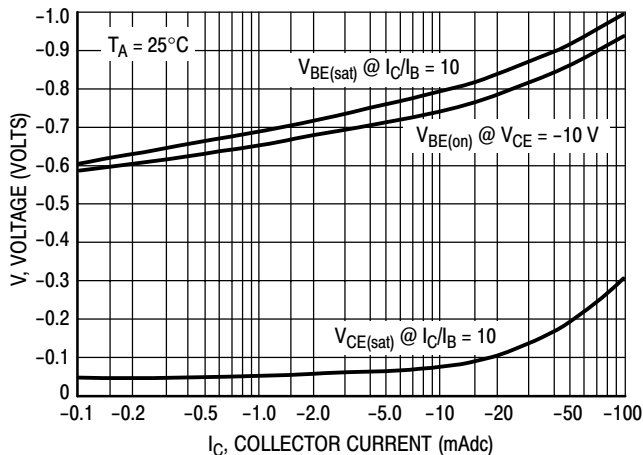


Figure 2. "Saturation" and "On" Voltages

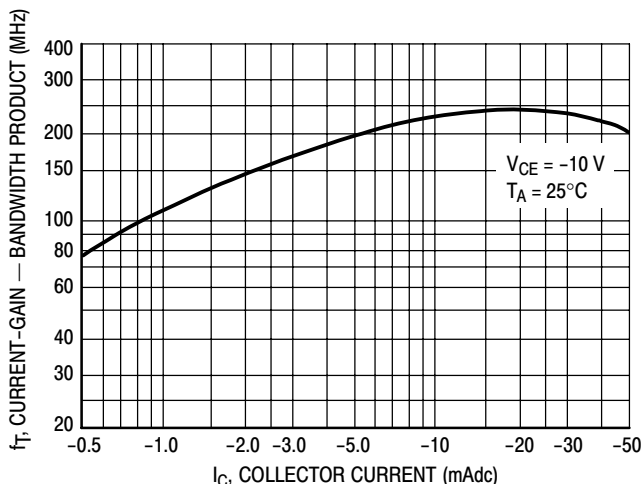


Figure 3. Current-Gain — Bandwidth Product

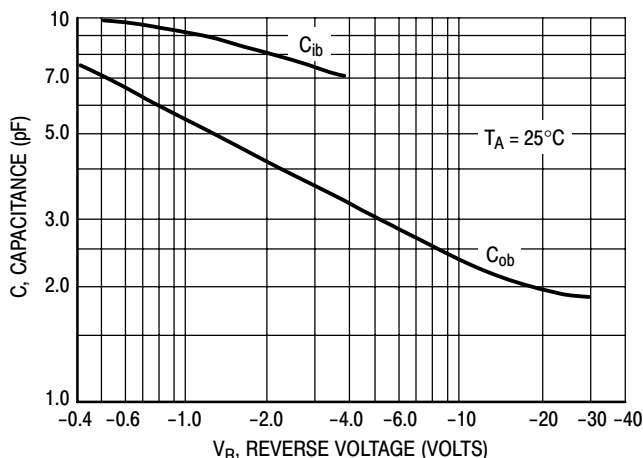


Figure 4. Capacitances

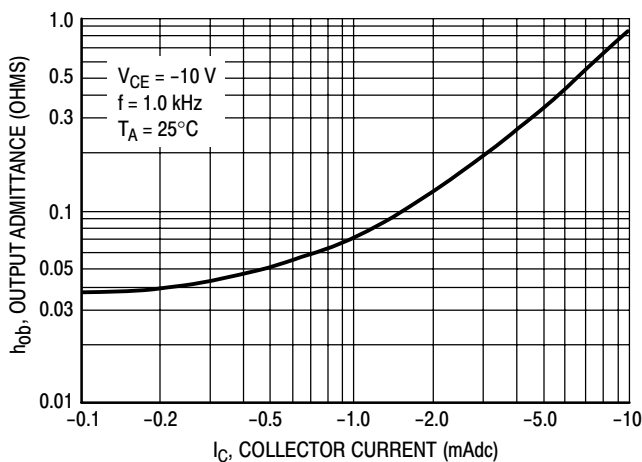


Figure 5. Output Admittance

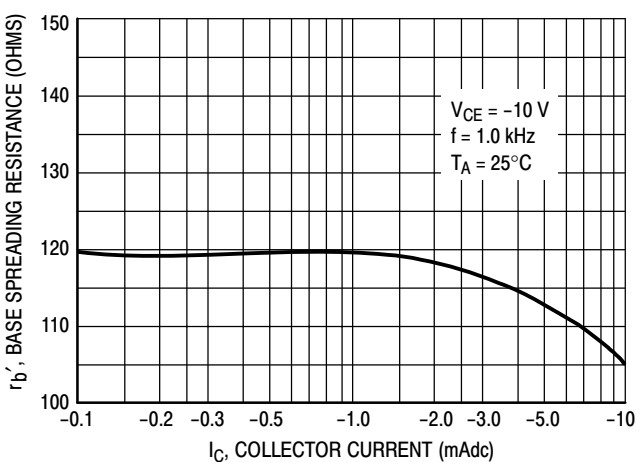
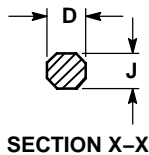
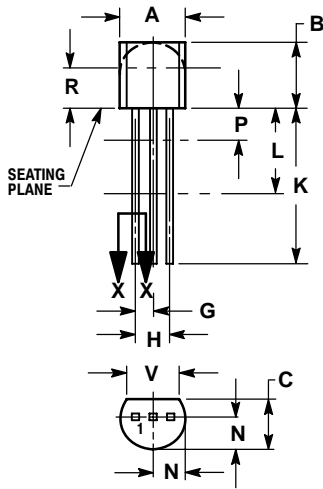


Figure 6. Base Spreading Resistance

BC307B, BC307C

PACKAGE DIMENSIONS

TO-92
(TO-226)
CASE 29-11
ISSUE AL



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	---	12.70	---
L	0.250	---	6.35	---
N	0.080	0.105	2.04	2.66
P	---	0.100	---	2.54
R	0.115	---	2.93	---
V	0.135	---	3.43	---

STYLE 17:

1. COLLECTOR
2. BASE
3. EMITTER

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor
P.O. Box 61312, Phoenix, Arizona 85082-1312 USA
Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada
Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center
2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051
Phone: 81-3-5773-3850

ON Semiconductor Website: <http://onsemi.com>

Order Literature: <http://www.onsemi.com/litorder>

For additional information, please contact your
local Sales Representative.