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RFMD
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RF3375

GENERAL PURPOSE AMPLIFIER

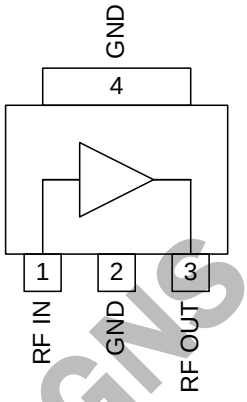
Package Style: SOT89

Features

- DC to >6000MHz Operation
- Internally Matched Input and Output
- 13.2dB Small Signal Gain
- +28dBm Output IP3
- +16.0dBm Output P1dB

Applications

- Basestation Applications
- Broadband, Low-Noise Gain Blocks
- IF or RF Buffer Amplifiers
- Driver Stage for Power Amplifiers
- Final PA for Low-Power Applications
- High Reliability Applications



Functional Block Diagram

Product Description

The RF3375 is a general purpose, low-cost RF amplifier IC. The device is manufactured on an advanced Gallium Arsenide Heterojunction Bipolar Transistor (HBT) process, and has been designed for use as an easily-cascadable 50Ω gain block. Applications include IF and RF amplification in wireless voice and data communication products operating in frequency bands up to 6000MHz. The device is self-contained with 50Ω input and output impedances and requires only two external DC-biasing elements to operate as specified.

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Ordering Information

RF3375	General Purpose Amplifier
RF337XPCBA-41X	Fully Assembled Evaluation Board

Optimum Technology Matching® Applied

☑ GaAs HBT	☐ SiGe BiCMOS	☐ GaAs pHEMT	☐ GaN HEMT
☐ GaAs MESFET	☐ Si BiCMOS	☐ Si CMOS	☐ RF MEMS
☐ InGaP HBT	☐ SiGe HBT	☐ Si BJT	☐ LDMOS

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Absolute Maximum Ratings

Parameter	Rating	Unit
Input RF Power	+13	dBm
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-60 to +150	°C
I_{CC}	80	mA

Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

RoHS status based on EUDirective2002/95/EC (at time of this document revision).

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Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Overall					T=25 °C, I_{CC}=65mA (See Note 1.)
Frequency Range		DC to >6000		MHz	
3dB Bandwidth		6		GHz	
Gain		13.5		dB	Freq=500MHz
		13.5		dB	Freq=1000MHz
	12.2	13.2		dB	Freq=2000MHz
		13.2		dB	Freq=3000MHz
		13.0			Freq=4000MHz
		12.4			Freq=6000MHz
Noise Figure		4.6		dB	Freq=2000MHz
Input VSWR		<1.9:1			In a 50Ω system, DC to 6000MHz
Output VSWR		<2.0:1			In a 50Ω system, DC to 500MHz
		<1.7:1			In a 50Ω system, 500MHz to 6000MHz
Output IP_3		+33.9		dBm	Freq=1000MHz
	+28.0	+30.0		dBm	Freq=2000MHz
Output P_{1dB}		+18.5		dBm	Freq=1000MHz
	+14.5	+16.0		dBm	Freq=2000MHz
Reverse Isolation		-18.0		dB	Freq=2000MHz
Thermal					I_{CC}=65mA (See Note 3.)
$Theta_{JC}$		175		°C/W	V_{PIN}=5.0V
Maximum Measured Junction Temperature at DC Bias Conditions		142		°C	T_{CASE}=+85°C
Mean Time to Failures		>100		years	T_{CASE}=+85°C
Power Supply					With 22Ω bias resistor, T=+25°C
Device Operating Voltage		5.0		V	At pin 8 with I_{CC}=65mA
		7.0		V	At Evaluation Board Connector I_{CC}=65mA
Operating Current		65	80	mA	See Note 2.

Note 1: All specification and characterization data has been gathered on standard FR-4 evaluation boards. These evaluation boards are not optimized for frequencies above 2.5GHz. Performance above 2.5GHz may improve if a high performance PCB is used.

Note 2: The RF3375 must be operated below 80mA. 60mA to 65mA is the recommended bias to ensure the highest possible reliability and electrical performance.

Note 3: Because of process variations from part to part, the current resulting from a fixed bias voltage will vary. As a result, caution should be used in designing fixed voltage bias circuits to ensure the worst case bias current does not exceed 80mA over all intended operating conditions.

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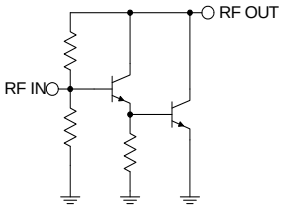
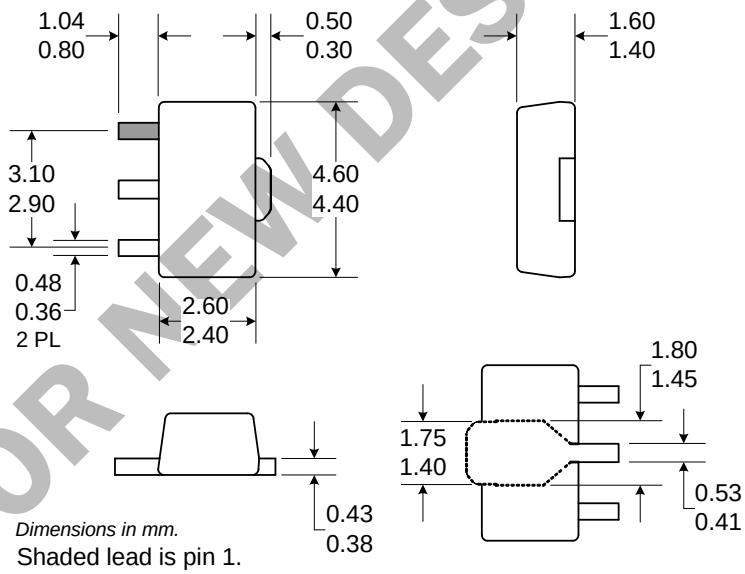
Pin	Function	Description	Interface Schematic
1	RF IN	RF input pin. This pin is NOT internally DC blocked. A DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. DC coupling of the input is not allowed, because this will override the internal feedback loop and cause temperature instability.	
2	GND	Ground connection.	
3	RF OUT	RF output and bias pin. Biasing is accomplished with an external series resistor and choke inductor to V_{CC}. The resistor is selected to set the DC current into this pin to a desired level. The resistor value is determined by the following equation: $$R = \frac{(V_{SUPPLY} - V_{DEVICE})}{I_{CC}}$$ Care should also be taken in the resistor selection to **ensure that the current into the part never exceeds 80mA over the planned operating temperature**. This means that a resistor between the supply and this pin is always required, even if a supply near 5.0V is available, to provide DC feedback to prevent thermal runaway. Because DC is present on this pin, a DC blocking capacitor, suitable for the frequency of operation, should be used in most applications. The supply side of the bias network should also be well bypassed.	
4	GND	Ground connection.	

Package Drawing



Dimensions in mm.
Shaded lead is pin 1.

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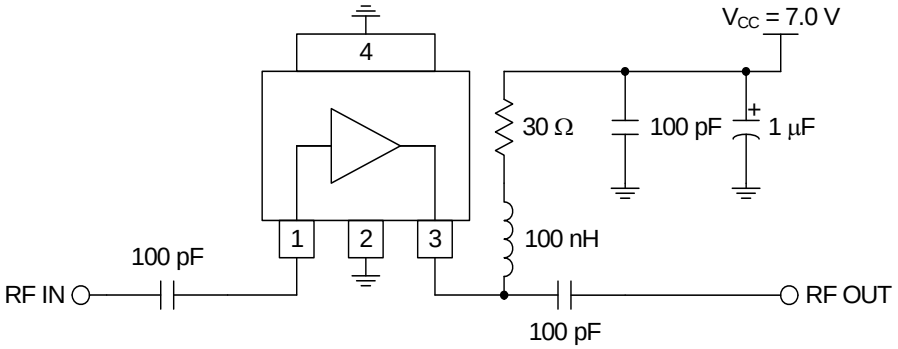
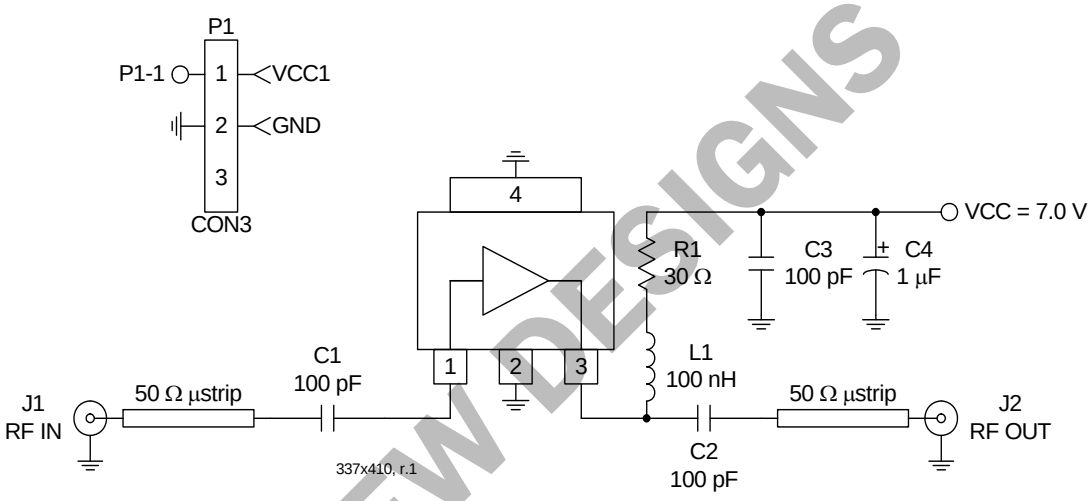
Application Schematic



Evaluation Board Schematic



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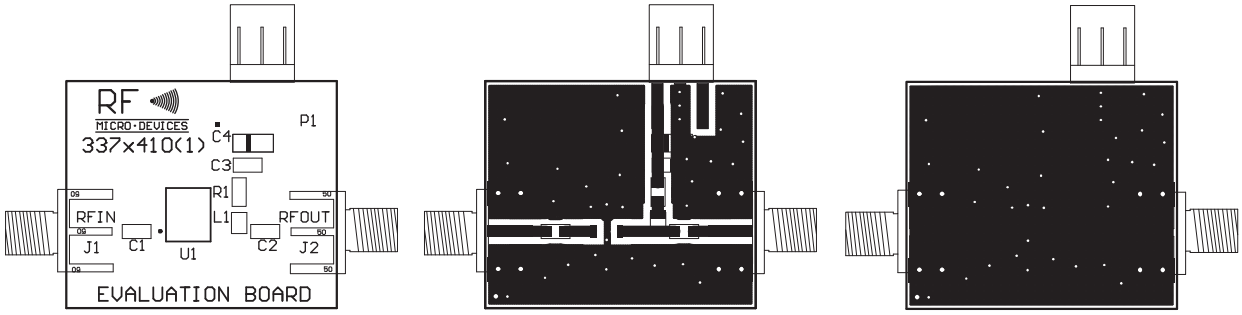
Evaluation Board Layout
Board Size 1.195" x 1.000"
Board Thickness 0.033", Board Material FR-4



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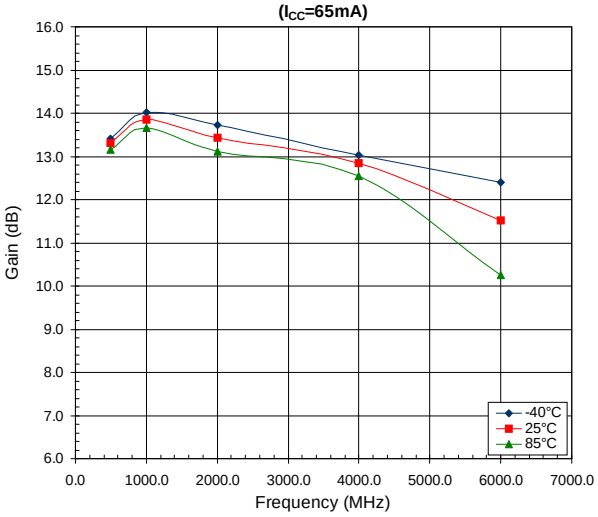
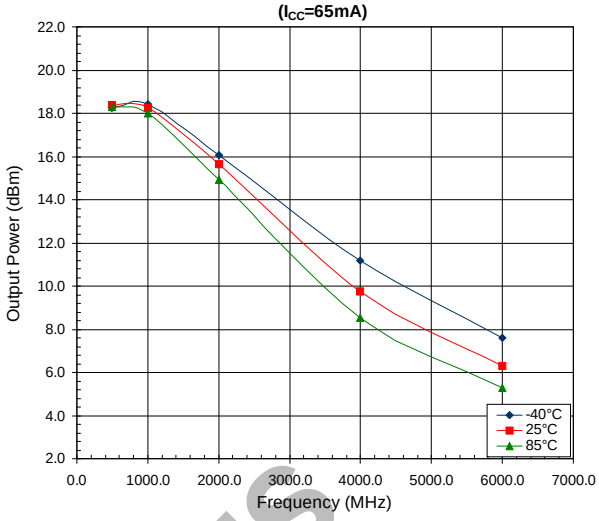
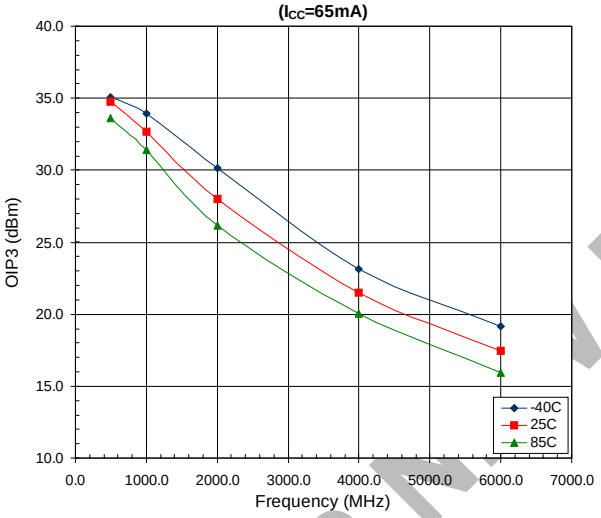
Gain versus Frequency Across Temperature
(I_{CC}=65mA)



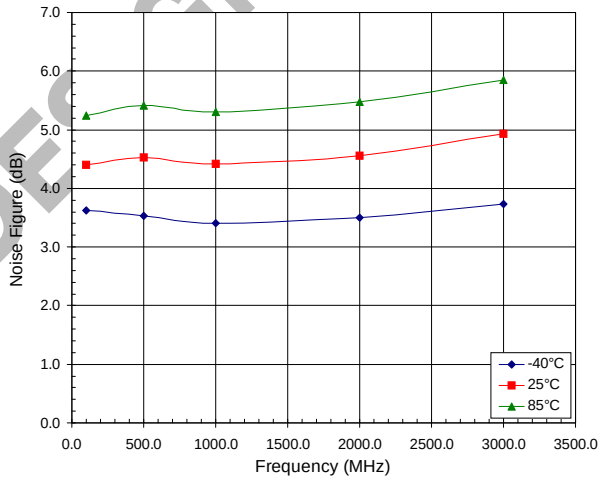
Output P1dB versus Frequency Across Temperature
(I_{CC}=65mA)



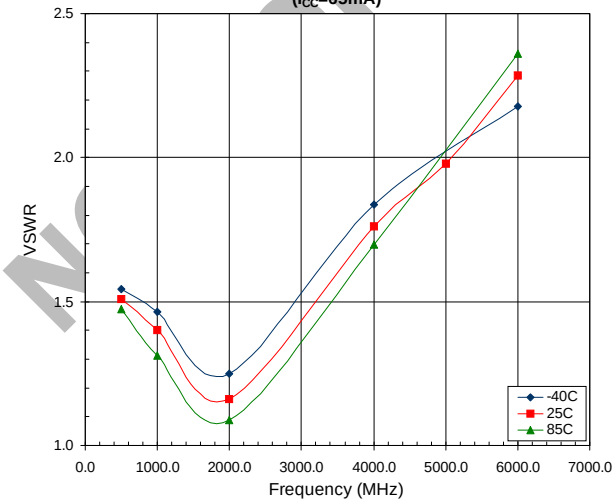
Output IP3 versus Frequency Across Temperature
(I_{CC}=65mA)



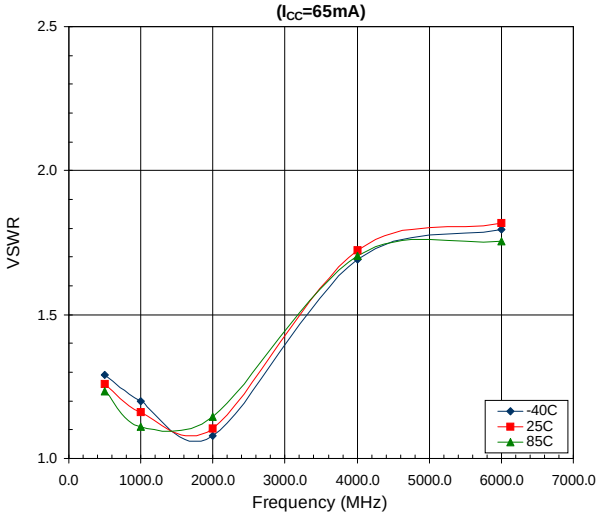
Noise Figure versus Frequency Across Temperature
(I_{CC}=65mA)



Input VSWR versus Frequency Across Temperature
(I_{CC}=65mA)



Output VSWR versus Frequency Across Temperature
(I_{CC}=65mA)



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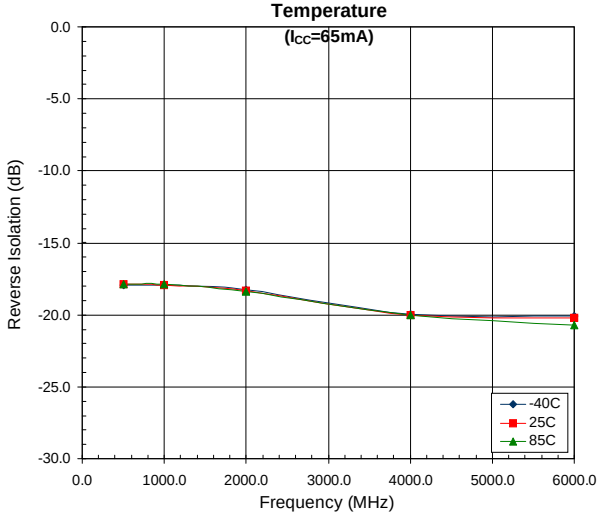
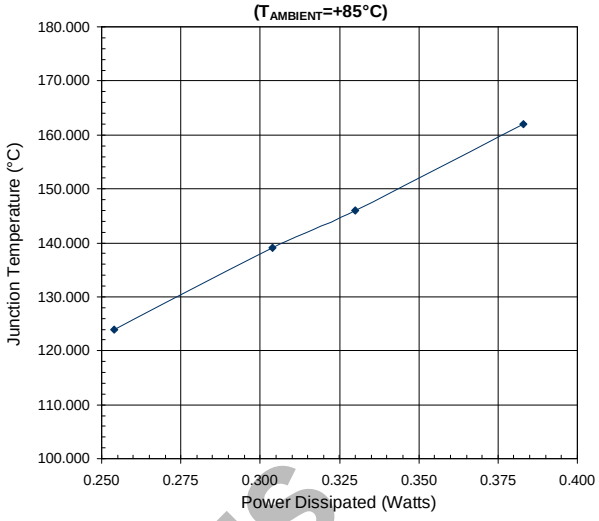
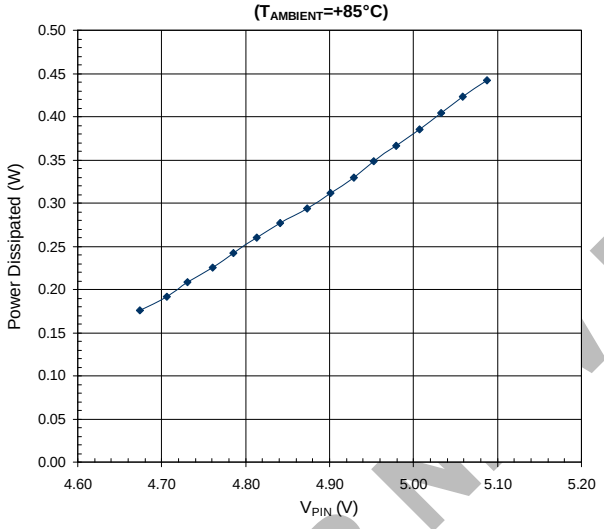
Reverse Isolation versus Frequency Across Temperature
(I_{CC}=65mA)



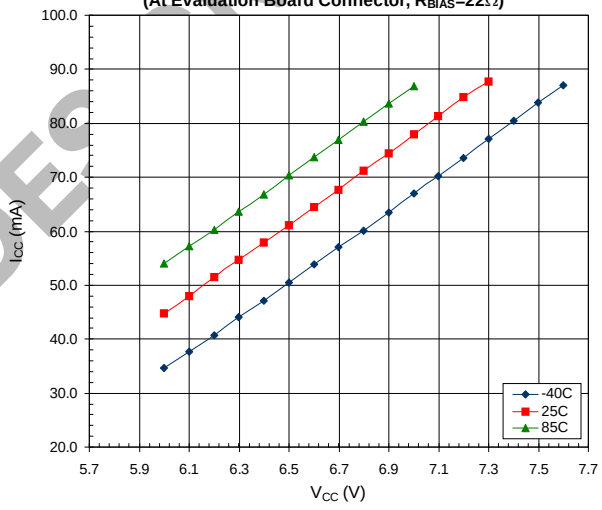
Junction Temperature versus Power Dissipated
($T_{AMBIENT}$=+85°C)



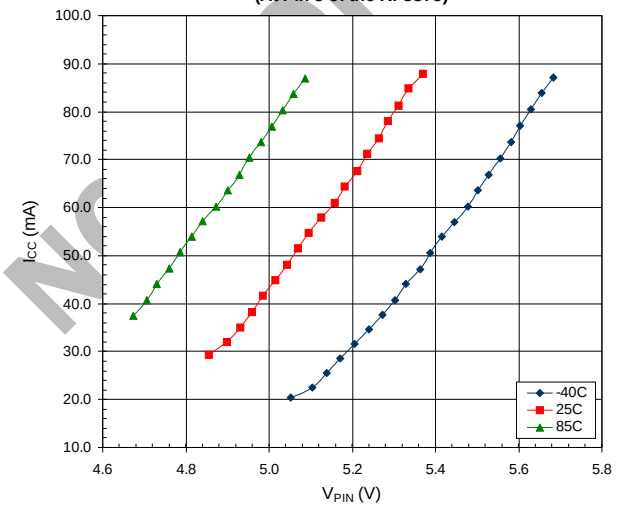
Power Dissipation versus Device Voltage Across Temperature
($T_{AMBIENT}$=+85°C)



Bias Current versus Supply Voltage Across Temperature
(At Evaluation Board Connector, R_{BIAS}=22Ω)



Bias Current versus Devices Voltage Across Temperature
(At Pin 3 of the RF3375)



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