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SHARP GP2W0114YPS

GP2W0114YPS

■ Features

- 1. Compliant with IrDA1.2 low power
- 2. Integrated package of transmitter/receiver. (9.3×2.6×height 2.35mm)
- 3. General purpose
- Low dissipation current due to shut-down function (Dissipation current at shut-down mode:Max. 0.1μA)
- 5. Soldering reflow type
- 6. Shield type

■ Applications

- 1. Cellular phones, PHS
- 2. Personal information tools

■ Absolute Maximum Ratings (T _a =25°C)				
Parameter	Symbol	Rating	Unit	
Supply voltage	V_{CC}	0 to 6.0	V	
LED Supply voltage	V_{LEDA}	0 to 7.0	V	
*1 Peak forward current	I_{FM}	60	mA	
Operating temperature	Topr	-40 to +85	°C	
Storage temperature	T_{stg}	-40 to +85	°C	

 T_{sol}

240

°C

■ Recommended Operating Conditions

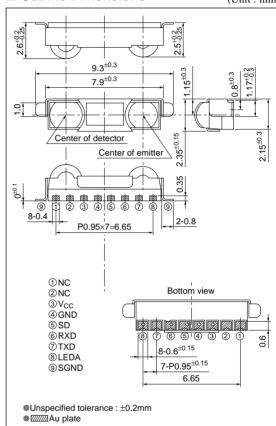
Parameter	Symbol	Rating	Unit
Supply voltage	V_{CC}	2.0 to 3.6	V
Transmission rate	BR	2.4 to 115.2	kb/s
High level input voltage (SD terminal)	V_{IHSD}	V_{CC} ×0.67 to V_{CC}	V
Low level input voltage (SD terminal)	V_{ILSD}	0 to V _{CC} ×0.1	V
*3 High level input voltage (TXD)	V_{IHTXD}	$V_{CC} \times 0.8$ to V_{CC}	V
*3 Low level input voltage (TXD)	V_{ILTXD}	0 to V _{CC} ×0.2	V

^{*3} Refer to Fig.8

IrDA Transceiver Module Compliant with IrDA1.2 Low Power

■ Outline Dimensions

(Unit: mm)



^{*2} Soldering temperature *1 Pulse width 78.1µs, Duty ratio:3/16

^{*2} For MAX. 10s

0.06

0.06

25

900

 μs

cm

mW/sr

nm

	$\blacksquare \ \ \text{Electrical Characteristics} \qquad \qquad (T_a=25^{\circ}\text{C}, \text{V}_{CC}=3.3\text{V})$					$V_{CC}=3.3V$	
	Parameter	Parameter Symbol Conditions MIN. TYP		TYP.	MAX.	Unit	
	Dissipation current at no input signal	I _{CC}	No input light, output terminal open, $V_{IHSD}=0V$	_	90	120	μΑ
e	S/D dissipation current	I _{CC-S}	No input light, output terminal open, $V_{\text{IHSD}}\!\!=\!\!V_{\text{CC}}$	-	0.001	0.1	μΑ
side	High level output voltage	V _{OH}	I_{OH} =200 μ A, V_{CC} =2.0 to 3.6 V^{*4}	V _{CC} -0.4	-	-	V
iver	Low level output voltage	V _{OL}	V_{CC} =2.0 to 3.6V, I_{OL} =200 μ A*4	-	ı	0.45	V
ece	Low level pules width	$t_{\rm w}$	BR=115.2kb/s, $\phi \le 15^{\circ}$, $C_L = 10pF^{*4}$	1.28	_	6.0	μs

BR=115.2kb/s, $\phi \le 15^{\circ}$, $C_L = 10pF^{*4}$

BR=115.2kb/s, $\phi \le 15^{\circ}$, $C_{L}=10pF^{*4}$

BR=115.2kb/s, $\phi \le 15^{\circ}$, $C_L = 10pF^{*4}$

BR=115.2kb/s, $\phi \le 15^{*5}$

 $(V_{LEDA}=3.3V, V_{IHTXD}=2.8V)$

Rise time

Fall time

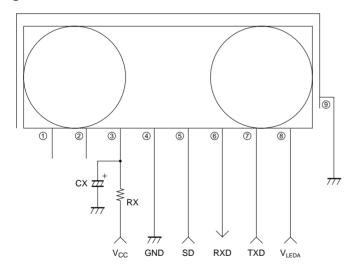
distance

Radiant intensity

Maximum communication

Peak emission wavelength

Fig.1 Recommended External Circuit



 $t_{\rm r}$

 $t_{\rm f}$

L

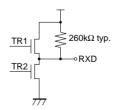
 I_E

 $\lambda_{\rm p}$

Logic	

SD	TXD	LED	Receiver	TR1	TR2	RXD
	High	ON	Don't care	-	-	Not valid
Low	Low	OFF	IrDA signal	OFF	ON	Low
	LOW	OFF	No signal	ON	OFF	High
High	Don't care	OFF	Don't care	OFF	OFF	Pull-up

*RXD Equipment circuit



① NC ② NC

870

21

4.0

850

- ③ V_{CC}
- ④ GND
- ⑤ SD
- ® RXD
- ⑦ TXD
- ® LEDA

Components	Recommended values
CX	1μF/6.3V (Note)

(Note) Please choose the most suitable CX according to the noise level and noise frequency of power supply.

Depend on noise level and noise frequency of power supply, CX does not work well.

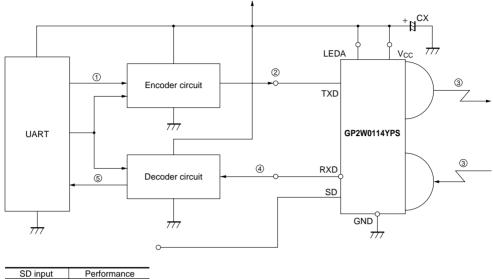
There are cases that some pulse noises from RXD other than signal will occur in certain communication area. Please check by finish product that there are no problem at all communication area and data rate.

If there are any problem, please check by inserting RX (1 to 10Ω) in the circuit drawing.

^{*4} Refer to Fig.4, 5, 6

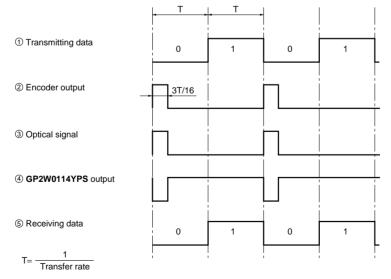
^{*5} Refer to Fig.7, 8, 9

Fig.2 System Configuration



SD input	Performance
Low	Normal mode
High	Shut down mode

Fig.3 Example of Signal Waveform



Transfer rate; 2.4kb/s,9.6kb/s,19.2kb/s,38.4kb/s,57.6kb/s,115.2kb/s

Fig.4 Input Signal Waveforrm (Receiver side)

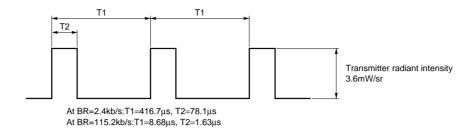


Fig.5 Output Waveform Specification (Receiver side)

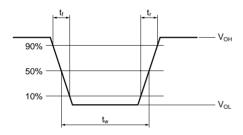
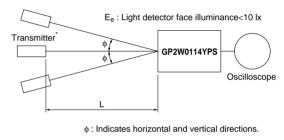
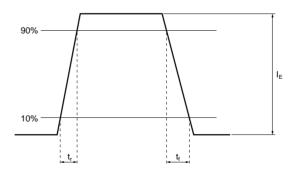


Fig.6 Standard Optical System (Receiver side)



* Transmitter shall use **GP2W0114YPS** (\(\hat{\p}=870nm\) TYP.) which is adjusted the radiation intensity at 3.6mW/sr

Fig.7 Output Waveform Specification (Transmitter side)



GP2W0114YPS

Fig.8 Standard Optical System (Transmitter side)

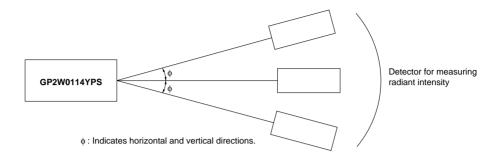
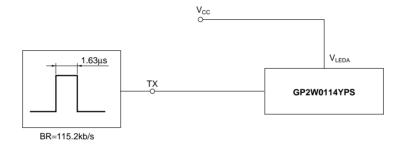


Fig.9 Recommended Circuit of Transmitter side



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