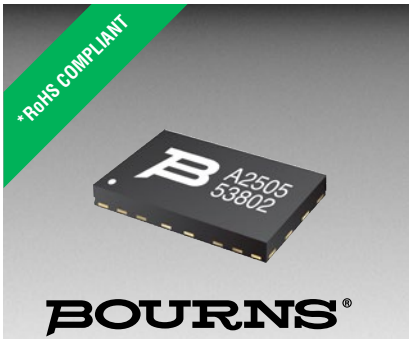


阅读申明

- 1.本站收集的数据手册和产品资料都来自互联网，版权归原作者所有。如读者和版权方有任何异议请及时告之，我们将妥善解决。
- 2.本站提供的中文数据手册是英文数据手册的中文翻译，其目的是协助用户阅读，该译文无法自动跟随原稿更新，同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。
- 3.本站提供的产品资料，来自厂商的技术支持或者使用者的心得体会等，其内容可能存在描述上的差异，建议读者做出适当判断。
- 4.如需与我们联系，请发邮件到marketing@iczoom.com，主题请标有“数据手册”字样。

Read Statement

1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.
2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.
3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.
4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets" .



Features

- Superior circuit protection
- Overcurrent and overvoltage protection
- Blocks surges up to rated limits
- High-speed performance
- Small SMT package
- Agency listing: [®]

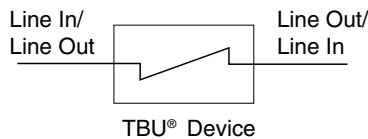
Applications

- Voice / VDSL cards
- Protection modules and dongles
- Process control equipment
- Test and measurement equipment
- General electronics

TBU-CA Series - TBU[®] High-Speed Protectors

General Information

The TBU-CA Series of Bourns[®] TBU[®] products are low capacitance single bidirectional high-speed protection components, constructed using MOSFET semiconductor technology, and designed to protect against faults caused by short circuits, AC power cross, induction and lightning surges.



Agency Listing

Description	
UL	File Number: E315805

The TBU[®] high-speed protector placed in the system circuit will monitor the current with the MOSFET detection circuit triggering to provide an effective barrier behind which sensitive electronics will not be exposed to large voltages or currents during surge events. The TBU[®] device is provided in a surface mount DFN package and meets industry standard requirements such as RoHS and Pb Free solder reflow profiles.

Absolute Maximum Ratings (@ T_A = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Part Number	Value	Unit
V _{imp}	Peak impulse voltage withstand with duration less than 10 ms	TBU-CA025-xxx-WH	250	V
		TBU-CA040-xxx-WH	400	
		TBU-CA050-xxx-WH	500	
		TBU-CA065-xxx-WH	650	
		TBU-CA085-xxx-WH	850	
V _{rms}	Continuous A.C. RMS voltage	TBU-CA025-xxx-WH	100	V
		TBU-CA040-xxx-WH	200	
		TBU-CA050-xxx-WH	250	
		TBU-CA065-xxx-WH	300	
		TBU-CA085-xxx-WH	425	
T _{op}	Operating temperature range		-55 to +125	°C
T _{stg}	Storage temperature range		-65 to +150	°C
T _{jmax}	Maximum Junction Temperature		+125	°C
ESD	HBM ESD protection per IEC 61000-4-2		±2	kV

BOURNS[®]

Asia-Pacific: Tel: +886-2 2562-4117 • Fax: +886-2 2562-4116

EMEA: Tel: +36 88 520 390 • Fax: +36 88 520 211

The Americas: Tel: +1-951 781-5500 • Fax: +1-951 781-5700

www.bourns.com

*RoHS Directive 2002/95/EC Jan. 27, 2003 including annex and RoHS Recast 2011/65/EU June 8, 2011. Specifications are subject to change without notice.

The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time. Users should verify actual device performance in their specific applications.

TBU-CA Series - TBU® High-Speed Protectors

BOURNS®

Electrical Characteristics (@ T_A = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Part Number	Min.	Typ.	Max.	Unit	
I _{trigger}	Current required for the device to go from operating state to protected state	TBU-CAxxx-050-WH	50	75	100	mA	
		TBU-CAxxx-100-WH	100	150	200		
		TBU-CAxxx-200-WH	200	300	400		
		TBU-CAxxx-300-WH	300	450	600		
		TBU-CAxxx-500-WH	500	750	1000		
R _{device}	Series resistance of the TBU device	V _{imp} = 250 V I _{trigger} (min.) = 50 mA	TBU-CA025-050-WH		13.3	15.3	Ω
		V _{imp} = 250 V I _{trigger} (min.) = 100 mA	TBU-CA025-100-WH		7.1	8.2	
		V _{imp} = 250 V I _{trigger} (min.) = 200 mA	TBU-CA025-200-WH		4.2	4.8	
		V _{imp} = 250 V I _{trigger} (min.) = 300 mA	TBU-CA025-300-WH		3.2	3.8	
		V _{imp} = 250 V I _{trigger} (min.) = 500 mA	TBU-CA025-500-WH		2.6	3.0	
		V _{imp} = 400 V I _{trigger} (min.) = 50 mA	TBU-CA040-050-WH		14.3	16.5	
		V _{imp} = 400 V I _{trigger} (min.) = 100 mA	TBU-CA040-100-WH		8.1	9.4	
		V _{imp} = 400 V I _{trigger} (min.) = 200 mA	TBU-CA040-200-WH		5.2	6.0	
		V _{imp} = 400 V I _{trigger} (min.) = 300 mA	TBU-CA040-300-WH		4.3	5.0	
		V _{imp} = 400 V I _{trigger} (min.) = 500 mA	TBU-CA040-500-WH		3.6	4.2	
		V _{imp} = 500 V I _{trigger} (min.) = 50 mA	TBU-CA050-050-WH		15.7	18.0	
		V _{imp} = 500 V I _{trigger} (min.) = 100 mA	TBU-CA050-100-WH		9.5	10.9	
		V _{imp} = 500 V I _{trigger} (min.) = 200 mA	TBU-CA050-200-WH		6.6	7.5	
		V _{imp} = 500 V I _{trigger} (min.) = 300 mA	TBU-CA050-300-WH		5.6	6.5	
		V _{imp} = 500 V I _{trigger} (min.) = 500 mA	TBU-CA050-500-WH		5.0	5.7	
		V _{imp} = 650 V I _{trigger} (min.) = 50 mA	TBU-CA065-050-WH		17.7	20.3	
		V _{imp} = 650 V I _{trigger} (min.) = 100 mA	TBU-CA065-100-WH		11.5	13.2	
		V _{imp} = 650 V I _{trigger} (min.) = 200 mA	TBU-CA065-200-WH		8.6	9.8	
		V _{imp} = 650 V I _{trigger} (min.) = 300 mA	TBU-CA065-300-WH		7.6	8.8	
		V _{imp} = 650 V I _{trigger} (min.) = 500 mA	TBU-CA065-500-WH		7.0	8.0	
		V _{imp} = 850 V I _{trigger} (min.) = 50 mA	TBU-CA085-050-WH		21.4	24.5	
		V _{imp} = 850 V I _{trigger} (min.) = 100 mA	TBU-CA085-100-WH		15.2	17.4	
		V _{imp} = 850 V I _{trigger} (min.) = 200 mA	TBU-CA085-200-WH		12.3	14.0	
		V _{imp} = 850 V I _{trigger} (min.) = 300 mA	TBU-CA085-300-WH		11.3	13.0	
V _{imp} = 850 V I _{trigger} (min.) = 500 mA	TBU-CA085-500-WH		10.7	12.2			
t _{block}	Time for the device to go from normal operating state to protected state				1	μs	
I _Q	Current through the triggered TBU® device with 50 Vdc circuit voltage		0.25	0.50	1.00	mA	
V _{reset}	Voltage below which the triggered TBU® device will transition to normal operating state		12	16	20	V	
R _{th(j-l)}	Junction to package pads - FR4 using recommended pad layout			98		°C/W	
R _{th(j-l)}	Junction to package pads - FR4 using heat sink on board (6 cm ²) (1 in ²)			40		°C/W	

Specifications are subject to change without notice.

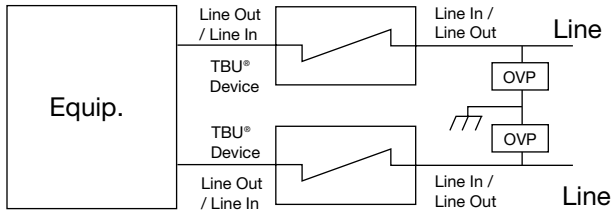
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time. Users should verify actual device performance in their specific applications.

TBU-CA Series - TBU[®] High-Speed Protectors

BOURNS[®]

Reference Application

The TBU[®] devices are general use protectors used in a wide variety of applications. The maximum voltage rating of the TBU[®] device should never be exceeded. Where necessary, an OVP should be employed to limit the maximum voltage. A cost-effective protection solution combines Bourns[®] TBU[®] protection devices with a pair of Bourns[®] MOVs. For bandwidth sensitive applications, a Bourns[®] GDT may be substituted for the MOV.



Basic TBU Operation

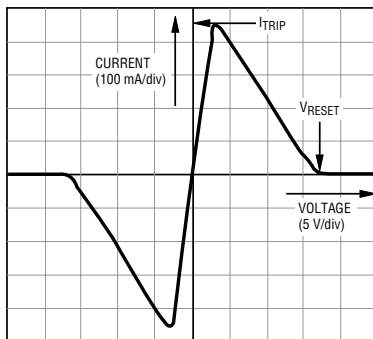
The TBU[®] device, constructed using MOSFET semiconductor technology, placed in the system circuit will monitor the current with the MOSFET detection circuit triggering to provide an effective barrier behind which sensitive electronics are not exposed to large voltages or currents during surge events. The TBU[®] device operates in approximately 1 μ s - once line current exceeds the TBU[®] device's trigger current $I_{trigger}$. When operated, the TBU[®] device will limit the current to less than the $I_{trigger}$ value within the t_{block} duration. If voltage above V_{reset} is continuously sustained, the TBU[®] device will subsequently reduce the current to a quiescent current level within a period of time that is dependent upon the applied voltage.

After the surge, the TBU[®] device resets when the voltage across the TBU[®] device falls to the V_{reset} level. The TBU[®] device will automatically reset on lines which have no DC bias or have DC bias below V_{reset} (such as unpowered signal lines).

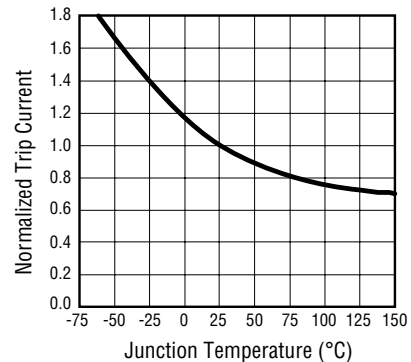
If the line has a normal DC bias above V_{reset} , the voltage across the TBU[®] device may not fall below V_{reset} after the surge. In such cases, special care needs to be taken to ensure that the TBU[®] device will reset, with software monitoring as one method used to accomplish this. Bourns application engineers can provide further assistance.

Performance Graphs

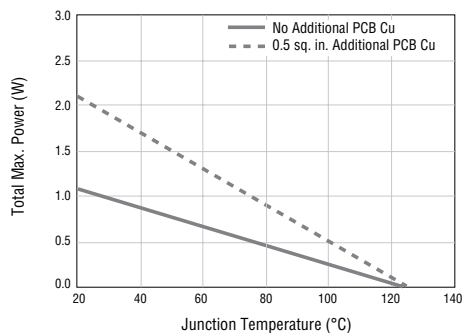
Typical V-I Characteristics (TBU-CA050-300-WH)



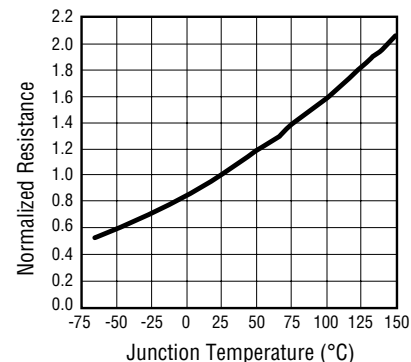
Typical Trigger Current vs. Temperature



Power Derating Curve



Typical Resistance vs. Temperature



Specifications are subject to change without notice.

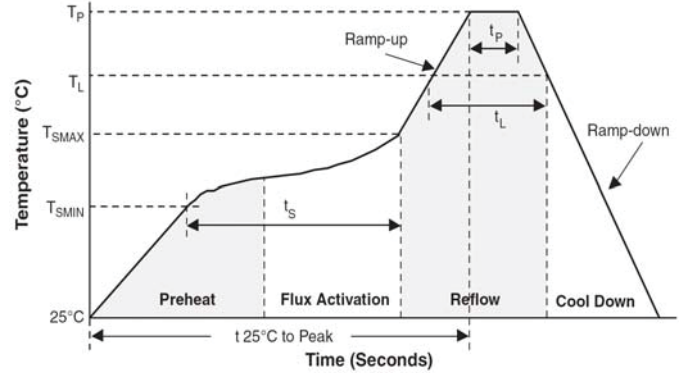
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time. Users should verify actual device performance in their specific applications.

TBU-CA Series - TBU® High-Speed Protectors

BOURNS®

Reflow Profile

Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate (T _{smax} to T _p)	3 °C/sec. max.
Preheat <ul style="list-style-type: none"> - Temperature Min. (T_{smin}) - Temperature Max. (T_{smax}) - Time (t_{smin} to t_{smax}) 	150 °C 200 °C 60-180 sec.
Time maintained above: <ul style="list-style-type: none"> - Temperature (T_L) - Time (t_L) 	217 °C 60-150 sec.
Peak/Classification Temperature (T _p)	260 °C
Time within 5 °C of Actual Peak Temp. (t _p)	20-40 sec.
Ramp-Down Rate	6 °C/sec. max.
Time 25 °C to Peak Temperature	8 min. max.



How to Order

TBU - CA 085 - 500 - WH

TBU® Product _____

Series _____
CA = Bi-Series

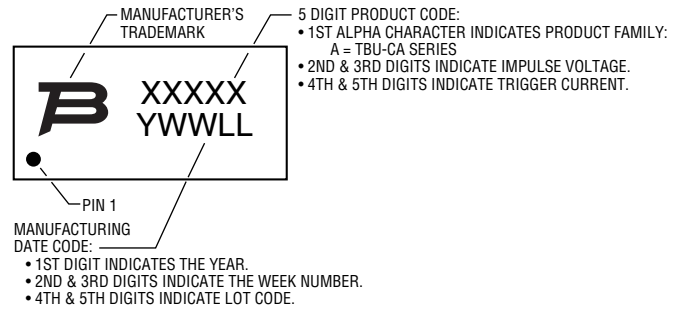
Impulse Voltage Rating _____
025 = 250 V
040 = 400 V
050 = 500 V
065 = 650 V
085 = 850 V

Trigger Current _____
050 = 50 mA
100 = 100 mA
200 = 200 mA
300 = 300 mA
500 = 500 mA

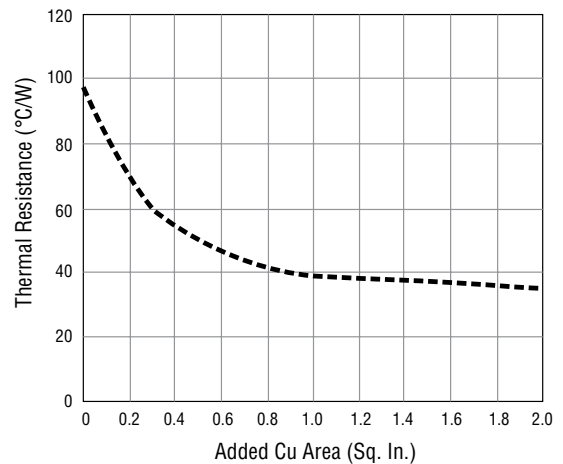
Hold to Trip Ratio Suffix _____
W = Hold to Trip Ratio

Package Suffix _____
H = DFN Package

Typical Part Marking



Thermal Resistance vs Additional PCB Cu Area

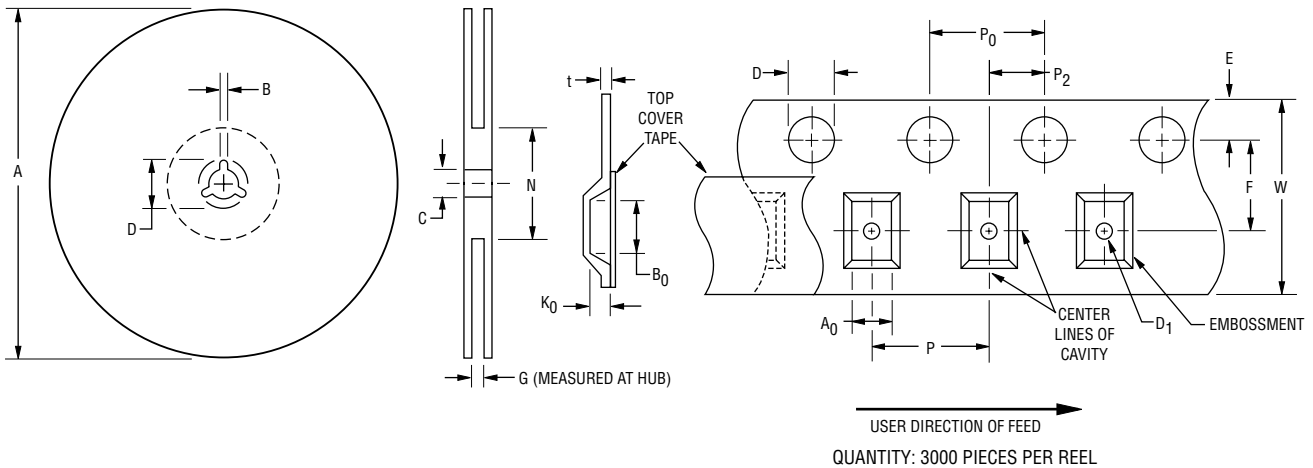


Specifications are subject to change without notice.
The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.
Users should verify actual device performance in their specific applications.

TBU-CA Series - TBU® High-Speed Protectors

BOURNS®

Packaging Specifications



A		B		C		D		G	N
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Ref.	Ref.
326	330	1.5	2.5	12.8	13.5	20.2	-	16.5	102
(12.835)	(13.002)	(.059)	(.098)	(.504)	(.531)	(.795)		(.650)	(4.016)

A ₀		B ₀		D		D ₁		E		F	
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	max.
4.3	4.5	6.7	6.9	1.5	1.6	1.5	-	1.65	1.85	7.4	7.6
(.169)	(.177)	(.264)	(.272)	(.059)	(.063)	(.059)		(.065)	(.073)	(.291)	(.299)
K ₀		P		P ₀		P ₂		t		W	
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
1.0	1.2	7.9	8.1	3.9	4.1	1.9	2.1	0.25	0.35	15.7	16.3
(.039)	(.047)	(.311)	(.319)	(.159)	(.161)	(.075)	(.083)	(.010)	(.014)	(.618)	(.642)

DIMENSIONS: $\frac{\text{MM}}{\text{(INCHES)}}$

REV. 01/17

"TBU" is a registered trademark of Bourns, Inc. in the United States and other countries.

Specifications are subject to change without notice.

The device characteristics and parameters in this data sheet can and do vary in different applications and actual device performance may vary over time.

Users should verify actual device performance in their specific applications.