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ASM3P2598A

Peak EMI Reducing Solution

Features

- Generates a 1X low EMI optimized clock signal at the output.
- Integrated loop filter components.
- Operates with a 3.3 / 2.5V Supply.
- Operating current less than 5mA.
- CMOS design.
- Input frequency range:
  - 60MHz to 120MHz for 2.5V
  - 60MHz to 120MHz for 3.3V
- Frequency deviation: ±1.5 (Typ) @ 85MHz Output frequency.
- Available in 6L-TSOP (6L-TSOT-23) Package.

Product Description

The ASM3P2598A is a versatile spread spectrum frequency modulator designed specifically for a wide range of clock frequencies. The ASM3P2598A reduces electromagnetic interference (EMI) at the clock source, allowing system wide reduction of EMI of all clock dependent signals. The ASM3P2598A allows significant system cost savings by reducing the number of circuit board layers, ferrite beads and shielding that are traditionally required to pass EMI regulations.

The ASM3P2598A uses the most efficient and optimized modulation profile approved by the FCC and is implemented by using a proprietary all digital method.

The ASM3P2598A modulates the output of a single PLL in order to “spread” the bandwidth of a synthesized clock, and more importantly, decreases the peak amplitudes of its harmonics. This result in significantly lower system EMI compared to the typical narrow band signal produced by oscillators and most frequency generators. Lowering EMI by increasing a signal’s bandwidth is called ‘spread spectrum clock generation.’

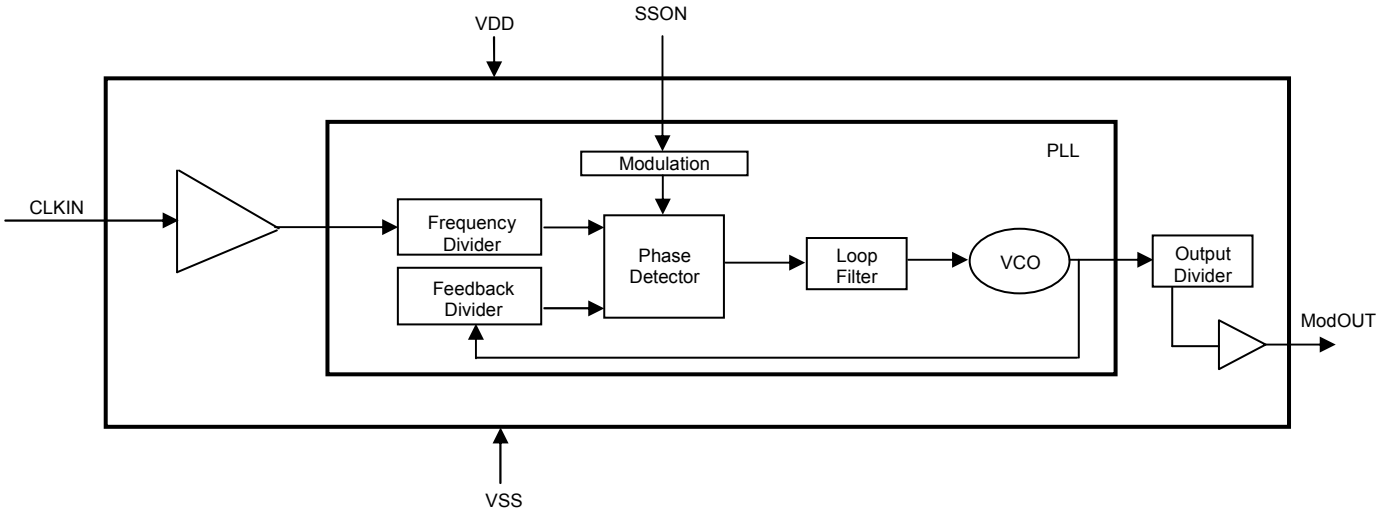
Applications

The ASM3P2598A is targeted towards all portable devices with very low power requirements like MP3 players, MFP, LCD Panel Module and digital still cameras.

Key Specifications

Description	Specification
Supply voltages	VDD = 3.3V / 2.5V
Cycle-to-Cycle Jitter	±360pS (Typ)
Output Duty Cycle	45/55%
Modulation Rate Equation	F_{IN}/2560
Frequency Deviation	±1.5% (Typ) @ 85MHz Output

Block Diagram



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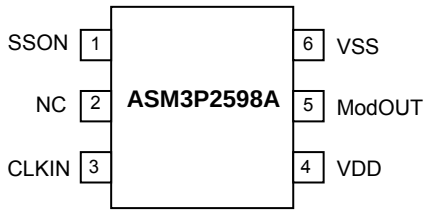
OCTOBER 2010 - Rev. 1.1

Publication Order Number:
ASM3P2598/D

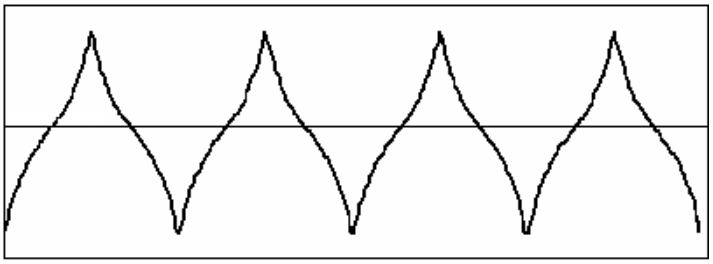
Pin Configuration (6L-TSOP Package)



Pin Description

Pin#	Pin Name	Type	Description
1	SSON	I	When SSON is HIGH, the spread spectrum is enabled and when LOW, it turns off the spread spectrum. Connect the pin to ground When Spread Spectrum feature is not required.
2	NC	-	No Connect.
3	CLKIN	I	Clock Input.
4	VDD	P	Power supply for the entire chip.
5	ModOUT	O	Spread spectrum clock output.
6	VSS	P	Ground connection.

Modulation Profile



Specifications

Description		Specification
Frequency Range	For 2.5V Supply	60MHz < CLKIN < 120MHz
	For 3.3V Supply	60MHz < CLKIN < 120MHz
Modulation Equation		$F_{IN}/2560$
Frequency Deviation		±1.5% (Typ) @ 85MHz Output

Absolute Maximum Ratings

Symbol	Parameter	Rating	Unit
VDD, V_{IN}	Voltage on any pin with respect to Ground	-0.5 to +4.6	V
T_{STG}	Storage temperature	-65 to +125	℃
T_s	Max. Soldering Temperature (10 sec)	260	℃
T_J	Junction Temperature	150	℃
T_{DV}	Static Discharge Voltage (As per JEDEC STD22- A114-B)	2	KV

Note: These are stress ratings only and are not implied for functional use. Exposure to absolute maximum ratings for prolonged periods of time may affect device reliability.

Operating Conditions

Parameter	Description	Min	Max	Unit
VDD	Supply Voltage	2.375	3.6	V
T_A	Operating Temperature (Ambient Temperature)	0	70	℃
C_L	Load Capacitance		15	pF
C_{IN}	Input Capacitance		7	pF

DC Electrical Characteristics for 2.5V Supply

Symbol	Parameter	Min	Typ	Max	Unit
V_{IL}	Input low voltage	VSS-0.3		0.8	V
V_{IH}	Input high voltage	2.0		VDD+0.3	V
I_{IL}	Input low current			-35	µA
I_{IH}	Input high current			35	µA
V_{OL}	Output low voltage (VDD = 2.5V, I_{OL} = 8mA)			0.6	V
V_{OH}	Output high voltage (VDD = 2.5V, I_{OH} = 8mA)	1.8			V
IDD	Static supply current[1]		1.8		mA
I_{CC}	Dynamic supply current (2.5V, 85MHz and no load)		4.0		mA
VDD	Operating voltage	2.375	2.5	2.625	V
t_{ON}	Power-up time (first locked cycle after power-up)			5	mS
Z_{OUT}	Output impedance		50		Ω

Note: 1. CLKIN pin is pulled low.

AC Electrical Characteristics for 2.5V Supply

Symbol	Parameter		Min	Typ	Max	Unit
CLKIN	Input frequency		60		120	MHz
ModOUT	Output frequency		60		120	MHz
f_d	Frequency Deviation	Input Frequency = 60MHz		±1.6		%
		Input Frequency = 120MHz		±1.1		
t_{LH}[1]	Output rise time (measured from 0.7V to 1.7V)		0.7	1.8	2.6	nS
t_{HL}[1]	Output fall time (measured from 1.7V to 0.7V)		0.4	0.9	1.1	nS
t_{JC}	Jitter (Cycle-to-cycle)			±360		pS
t_D	Output duty cycle		45	50	55	%

Note: 1. t_{LH} and t_{HL} are measured into a capacitive load of 15pF.

DC Electrical Characteristics for 3.3V Supply

Symbol	Parameter	Min	Typ	Max	Unit
V_{IL}	Input low voltage	VSS-0.3		0.8	V
V_{IH}	Input high voltage	2.0		VDD+0.3	V
I_{IL}	Input low current			-35	µA
I_{IH}	Input high current			35	µA
V_{OL}	Output low voltage (VDD = 3.3V, I_{OL} = 8mA)			0.4	V
V_{OH}	Output high voltage (VDD = 3.3V, I_{OH} = 8mA)	2.5			V
IDD	Static supply current[1]		2.2		mA
I_{CC}	Dynamic supply current (3.3V, 85MHz and no load)		4.5		mA
VDD	Operating voltage	3.0	3.3	3.6	V
t_{ON}	Power-up time (first locked cycle after power-up)			5	mS
Z_{OUT}	Output impedance		45		Ω

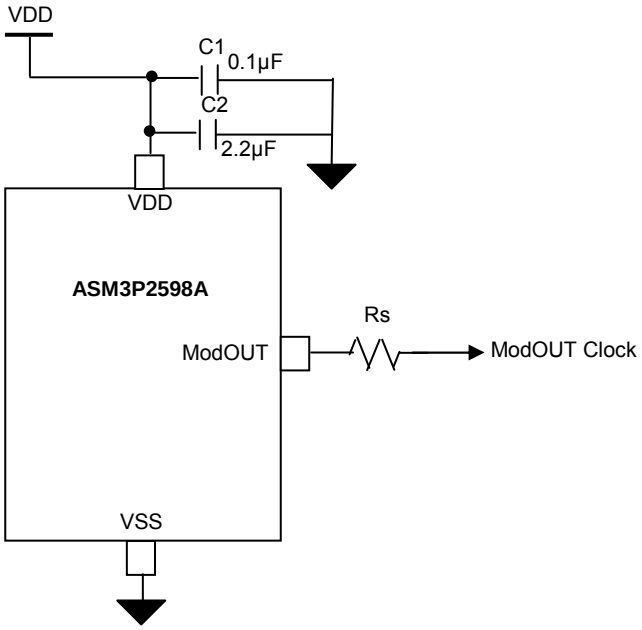
Note: 1. CLKIN pin is pulled low.

AC Electrical Characteristics for 3.3V Supply

Symbol	Parameter		Min	Typ	Max	Unit
CLKIN	Input frequency		60		120	MHz
ModOUT	Output frequency		60		120	MHz
f_d	Frequency Deviation	Input Frequency = 60MHz		±1.6		%
		Input Frequency = 120MHz		±1.1		
t_{LH}[1]	Output rise time (measured from 0.8 to 2.0V)		0.5	1.2	1.8	nS
t_{HL}[1]	Output fall time (measured at 2.0V to 0.8V)		0.3	0.8	1.1	nS
t_{JC}	Jitter (cycle-to-cycle)			±360		pS
t_D	Output duty cycle		45	50	55	%

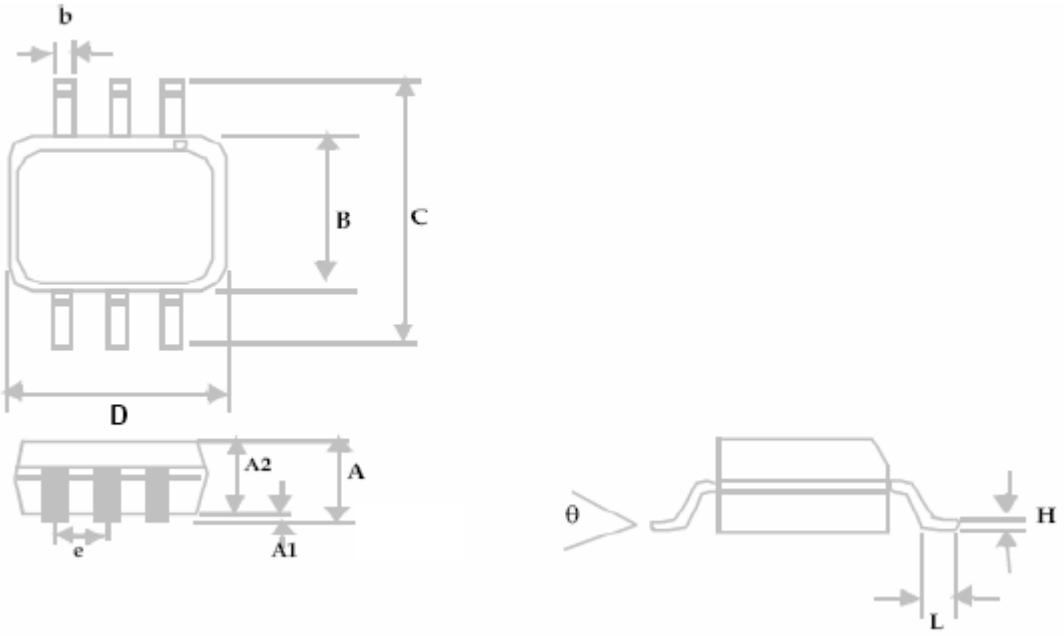
Note: 1. t_{LH} and t_{HL} are measured into a capacitive load of 15pF.

Typical Application Schematic



Package Information

6L-TSOP Package



Symbol	Dimensions			
	Inches		Millimeters	
	Min	Max	Min	Max
A		0.04		1.00
A1	0.00	0.004	0.00	0.10
A2	0.033	0.036	0.84	0.90
b	0.012	0.02	0.30	0.50
H	0.005 BSC		0.127 BSC	
D	0.114 BSC		2.90 BSC	
B	0.06 BSC		1.60 BSC	
e	0.0374 BSC		0.950 BSC	
C	0.11 BSC		2.80 BSC	
L	0.0118	0.02	0.30	0.50
θ	0°	4°	0°	4°

Ordering Information

Part Number	Marking	Package Type	Temperature
ASM3P2598AF-06OR	A11	6L-TSOP (6L-TSOT-23), TAPE & REEL, Pb Free	0℃ to +70℃

A “microdot” placed at the end of last row of marking or just below the last row toward the center of package indicates Pb-free.

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