## 阅读申明

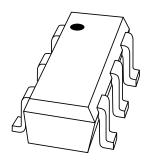
- 1.本站收集的数据手册和产品资料都来自互联网,版权归原作者所有。如读者和版权方有任何异议请及时告之,我们将妥善解决。
- 2.本站提供的中文数据手册是英文数据手册的中文翻译,其目的是协助用户阅读,该译文无法自动跟随原稿更新,同时也可能存在翻译上的不当。建议读者以英文原稿为参考以便获得更精准的信息。
- 3.本站提供的产品资料,来自厂商的技术支持或者使用者的心得体会等,其内容可能存在描 叙上的差异,建议读者做出适当判断。
- 4.如需与我们联系,请发邮件到marketing@iczoom.com,主题请标有"数据手册"字样。

# **Read Statement**

- 1. The datasheets and other product information on the site are all from network reference or other public materials, and the copyright belongs to the original author and original published source. If readers and copyright owners have any objections, please contact us and we will deal with it in a timely manner.
- 2. The Chinese datasheets provided on the website is a Chinese translation of the English datasheets. Its purpose is for reader's learning exchange only and do not involve commercial purposes. The translation cannot be automatically updated with the original manuscript, and there may also be improper translations. Readers are advised to use the English manuscript as a reference for more accurate information.
- 3. All product information provided on the website refer to solutions from manufacturers' technical support or users the contents may have differences in description, and readers are advised to take the original article as the standard.
- 4. If you have any questions, please contact us at marketing@iczoom.com and mark the subject with "Datasheets" .

## DISCRETE SEMICONDUCTORS

# DATA SHEET



# **BGA2709**MMIC wideband amplifier

Product specification Supersedes data of 2002 Feb 05 2002 Aug 06



## **MMIC** wideband amplifier

**BGA2709** 

#### **FEATURES**

- Internally matched to 50  $\Omega$
- Very wide frequency range (3.6 GHz at 3 dB bandwidth)
- Flat 23 dB gain (DC to 2.6 GHz at 1 dB flatness)
- 12.5 dBm saturated output power at 1 GHz
- High linearity (22 dBm OIP3 at 1 GHz)
- Unconditionally stable (K > 1.2).

### **APPLICATIONS**

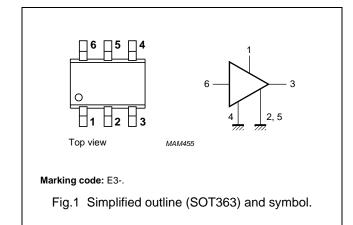
- · Cable systems
- · LNB IF amplifiers
- · General purpose
- ISM.

#### **DESCRIPTION**

Silicon Monolithic Microwave Integrated Circuit (MMIC) wideband amplifier with internal matching circuit in a 6-pin SOT363 SMD plastic package.

#### **PINNING**

PIN	DESCRIPTION
1	Vs
2, 5	GND2
3	RF out
4	GND1
6	RF in



#### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
Vs	DC supply voltage		5	6	V
I <sub>S</sub>	DC supply current		23.5	_	mA
s <sub>21</sub>   <sup>2</sup>	insertion power gain	f = 1 GHz	22.7	_	dB
NF	noise figure	f = 1 GHz	4	_	dB
P <sub>L(sat)</sub>	saturated load power	f = 1 GHz	12.5	_	dBm

## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Vs	DC supply voltage	RF input AC coupled	_	6	V
I <sub>S</sub>	supply current		_	35	mA
P <sub>tot</sub>	total power dissipation	T <sub>s</sub> ≤ 90 °C	_	200	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	operating junction temperature		_	150	°C
P <sub>D</sub>	maximum drive power		_	10	dBm

## **CAUTION**

This product is supplied in anti-static packing to prevent damage caused by electrostatic discharge during transport and handling.

## MMIC wideband amplifier

**BGA2709** 

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-s</sub>	thermal resistance from junction to solder point	$P_{tot} = 200 \text{ mW}; T_s \le 90 ^{\circ}\text{C}$	300	K/W

## **CHARACTERISTICS**

 $V_S$  = 5 V;  $I_S$  = 23.5 mA;  $T_j$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	PARAMETER CONDITIONS		TYP.	MAX.	UNIT
I <sub>S</sub>	supply current		19	23.5	32	mA
s <sub>21</sub>   <sup>2</sup>	insertion power gain	f = 100 MHz	21	22.2	23	dB
		f = 1 GHz	21	22.7	24	dB
		f = 1.8 GHz	22	23.0	24	dB
		f = 2.2 GHz	21	23.0	24	dB
		f = 2.6 GHz	20	22.1	23	dB
		f = 3 GHz	18	21.1	22	dB
R <sub>L IN</sub>	return losses input	f = 1 GHz	9	11	_	dB
		f = 2.2 GHz	9	11	_	dB
R <sub>L OUT</sub>	return losses output	f = 1 GHz	17	20	_	dB
		f = 2.2 GHz	20	24	_	dB
s <sub>12</sub>   <sup>2</sup>	isolation	f = 1.6 GHz	31	33	_	dB
		f = 2.2 GHz	34	36	_	dB
NF	noise figure	f = 1 GHz	_	4.0	4.4	dB
		f = 2.2 GHz	_	4.4	4.9	dB
BW	bandwidth	at $ s_{21} ^2 - 3$ dB below flat gain at 1 GHz	3.1	3.6	_	GHz
K	stability factor	f = 1 GHz	1.3	1.7	_	_
		f = 2 GHz	1.8	2.2	_	_
P <sub>L(sat)</sub>	saturated load power	f = 1 GHz	11	12.5	_	dBm
		f = 2.2 GHz	5	7.5	_	dBm
P <sub>L 1 dB</sub>	load power	at 1 dB gain compression; f = 1 GHz	7	8.3	_	dBm
		at 1 dB gain compression; f = 2.2 GHz	3	5.4	_	dBm
IP3 <sub>(in)</sub>	input intercept point	f = 1 GHz	-3	-1	_	dBm
		f = 2.2 GHz	-7	-9	_	dBm
IP3 <sub>(out)</sub>	output intercept point	f = 1 GHz	20	22	_	dBm
, ,		f = 2.2 GHz	12	14	_	dBm

## MMIC wideband amplifier

**BGA2709** 

#### APPLICATION INFORMATION

Figure 2 shows a typical application circuit for the BGA2709 MMIC. The device is internally matched to 50  $\Omega$ , and therefore does not need any external matching. The value of the input and output DC blocking capacitors C2, C3 should be not more than 100 pF for applications above 100 MHz. However, when the device is operated below 100 MHz, the capacitor value should be increased.

The nominal value of the RF choke, L1 is 100 nH. At frequencies below 100 MHz this value should be increased to 220 nH. At frequencies above 1 GHz a much lower value must be used (e.g. 10 nH) to improve return losses. For optimal results, a good quality chip inductor such as the TDK MLG 1608 (0603), or a wire-wound SMD type should be chosen.

Both the RF choke, L1 and the 22 nF supply decoupling capacitor, C1 should be located as closely as possible to the MMIC.

Separate paths must be used for the ground planes of the ground pins GND1, GND2, and these paths must be as short as possible. When using vias, use multiple vias per pin in order to limit ground path inductance.

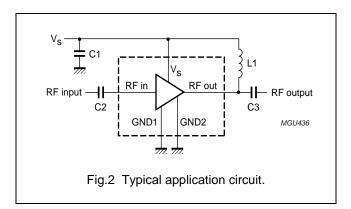
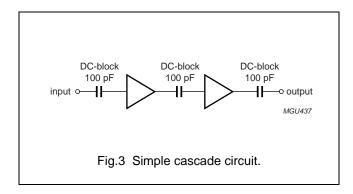


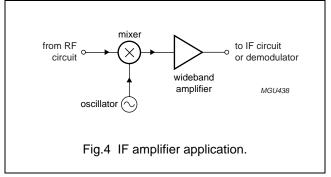
Figure 3 shows two cascaded MMICs. This configuration doubles overall gain while preserving broadband characteristics. Supply decoupling and grounding conditions for each MMIC are the same as those for the circuit of Fig.2.

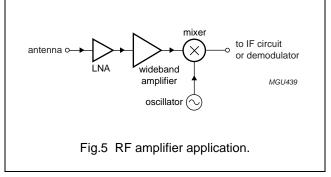
The excellent wideband characteristics of the MMIC make it and ideal building block in IF amplifier applications such as LBNs (see Fig.4).

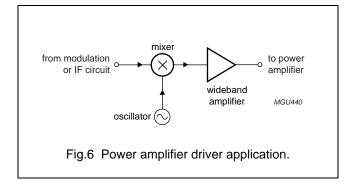
As a buffer amplifier between an LNA and a mixer in a receiver circuit, the MMIC offers an easy matching, low noise solution (see Fig.5).

In Fig.6 the MMIC is used as a driver to the power amplifier in part of a transmitter circuit. Good linear performance and matched input and output offer quick design solutions in such applications.







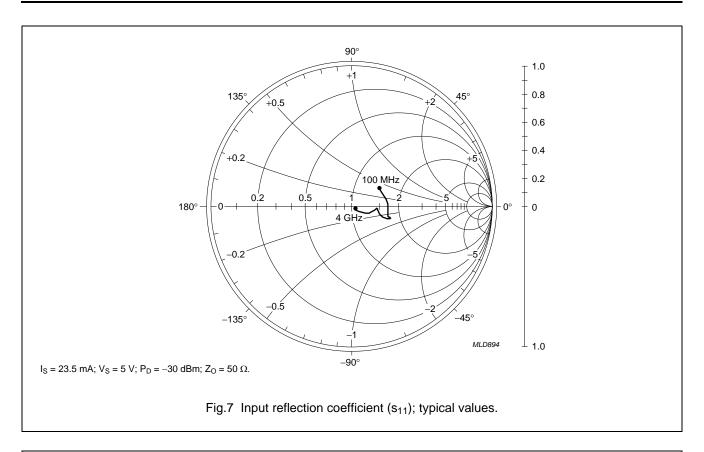


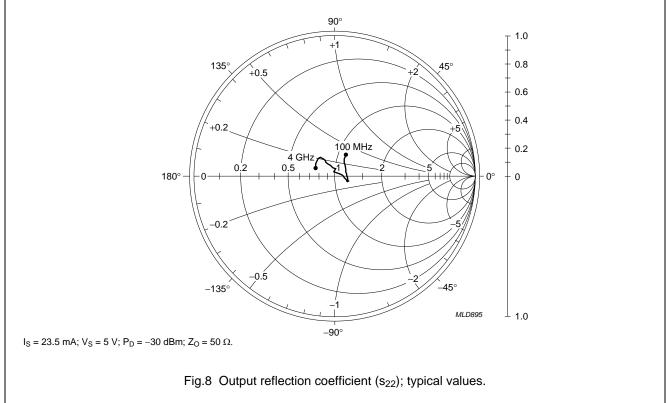
2002 Aug 06

4

## MMIC wideband amplifier

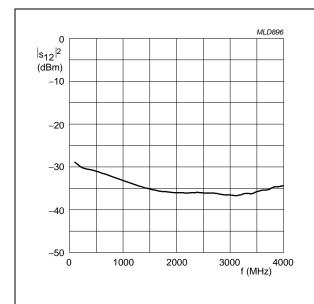
**BGA2709** 





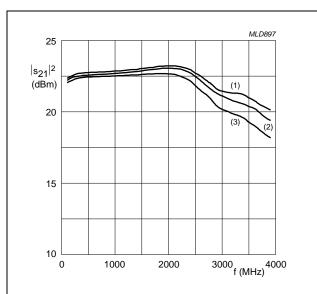
## MMIC wideband amplifier

**BGA2709** 



 $I_S$  = 23.5 mA;  $V_S$  = 5 V;  $P_D$  = –30 dBm;  $Z_O$  = 50  $\Omega.$ 

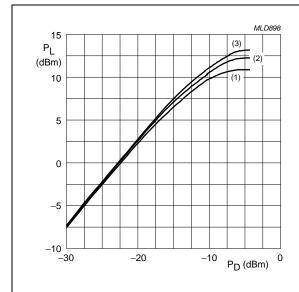
Fig.9 Isolation ( $|s_{12}|^2$ ) as a function of frequency; typical values.



 $P_D = -30 \text{ dBm}$ ;  $Z_O = 50 \Omega$ .

- (1)  $I_S = 28.4 \text{ mA}$ ;  $V_S = 5.5 \text{ V}$
- (2)  $I_S = 23.5 \text{ mA}$ ;  $V_S = 5 \text{ V}$
- (3)  $I_S = 18.8 \text{ mA}$ ;  $V_S = 4.5 \text{ V}$

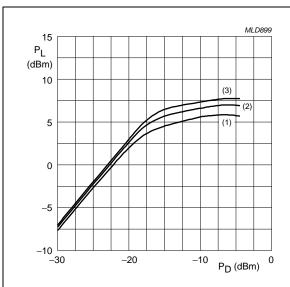
Fig.10 Insertion gain  $(|s_{21}|^2)$  as a function of frequency; typical values.



 $f = 1 \text{ GHz}; Z_O = 50 \Omega.$ 

- (1)  $V_S = 4.5 V$
- (2)  $V_S = 5 V$
- (3)  $V_S = 5.5 V$

Fig.11 Load power as a function of drive power at 1 GHz; typical values.



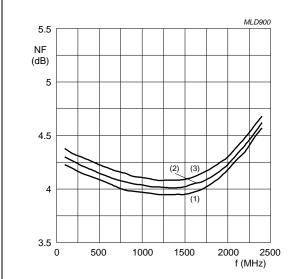
 $f = 2.2 \text{ GHz}; Z_O = 50 \Omega.$ 

- (1)  $V_S = 4.5 V$
- (2)  $V_S = 5 V$
- (3)  $V_S = 5.5 V$

Fig.12 Load power as a function of drive power at 2.2 GHz; typical values.

## MMIC wideband amplifier

**BGA2709** 



 $P_D$  = -30 dBm;  $Z_O$  =  $50~\Omega.$ 

- (1)  $I_S = 18.8 \text{ mA}$ ;  $V_S = 4.5 \text{ V}$
- (2)  $I_S = 23.5 \text{ mA}$ ;  $V_S = 5 \text{ V}$
- (3)  $I_S = 28.4 \text{ mA}$ ;  $V_S = 5.5 \text{ V}$

Fig.13 Noise figure as a function of frequency; typical values.

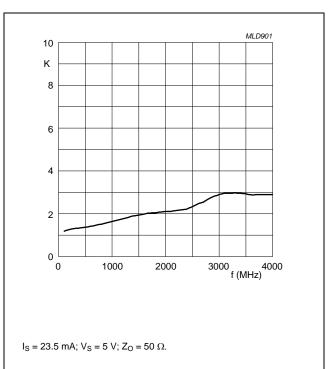


Fig.14 Stability factor as a function of frequency; typical values.

**NXP Semiconductors** 

**Table 1** Scattering parameters:  $I_S = 23.5 \text{ mA}$ ;  $V_S = 5 \text{ V}$ ;  $P_D = -30 \text{ dBm}$ ;  $Z_O = 50 \Omega$ ;  $T_{amb} = 25 ^{\circ}\text{C}$ MMIC wideband amplifier S<sub>11</sub> S<sub>12</sub> S<sub>22</sub> Kf (MHz) **MAGNITUDE MAGNITUDE ANGLE MAGNITUDE ANGLE** ANGLE( **ANGLE** MAGNITUDE **FACTOR** deg) (deg) (ratio) (ratio) (deg) (ratio) (ratio) (deg) 0.23362 32.281 12.90523 21.565 0.036496 16.408 0.16296 1.2 100 61.578 200 0.25252 11.824 13.22858 4.852 0.032314 5.728 0.13501 60.573 1.3 0.25838 -2.14913.43580 -5.8651.3 400 -10.310.029604 0.10353 41.717 0.25990 13.51088 600 -8.784-21.140.027122 -11.450.085075 16.95 1.4 0.26278 13.56715 -15.081.5 800 -12.76-30.930.024611 0.088892 -1.879-40.37-16.331.7 1000 0.26695 -14.8813.65916 0.022107 0.09716 -13.361.8 1200 0.27404 -16.3013.74736 -49.83-15.67-20.250.019986 0.10279 1400 0.27921 -16.5113.85661 -59.47-13.420.10385 -23.241.9 0.018217 1600 0.28486 -16.7814.03414 -69.500.017049 -9.9270.099148 -24.082.0 1800 0.28749 -17.2514.16012 -80.230.016409 -5.9680.089633 -20.582.1 2000 0.28601 -17.7614.23586 -91.650.015912 -2.040.076785 -14.482.1 2200 0.27487 -18.9814.14430 -103.90.015829 1.077 0.062455 -4.5072.2 3.361 2.2 2400 0.25176 -19.9413.70546 -117.00.016054 0.044552 11.808 2.5 2600 0.21405 -17.0912.75365 -129.70.015801 3.145 0.023668 98.126 2.7 2800 0.19288 -11.8511.96153 -138.70.015406 7.602 0.057779 104.35 3000 0.18347 -6.22811.33015 -147.60.015049 11.411 2.9 0.094848 119.98 3200 0.17459 -6.32710.94943 0.015098 15.52 0.12948 123.28 3.0 -156.3

0.015529

0.017107

0.018529

0.019276

20.649

23.92

23.226

18.403

0.15325

0.16627

0.16317

0.14602

126.56

131.67

140.54

157.03

3.0

2.8

2.8

2.9

3400

3600

3800

4000

0.15344

0.10799

0.05984

0.025953

-14.14

-26.12

-39.66

-28.87

10.65459

10.28106

9.56897

8.97718

-167.3

-179.1

170.0

157.3

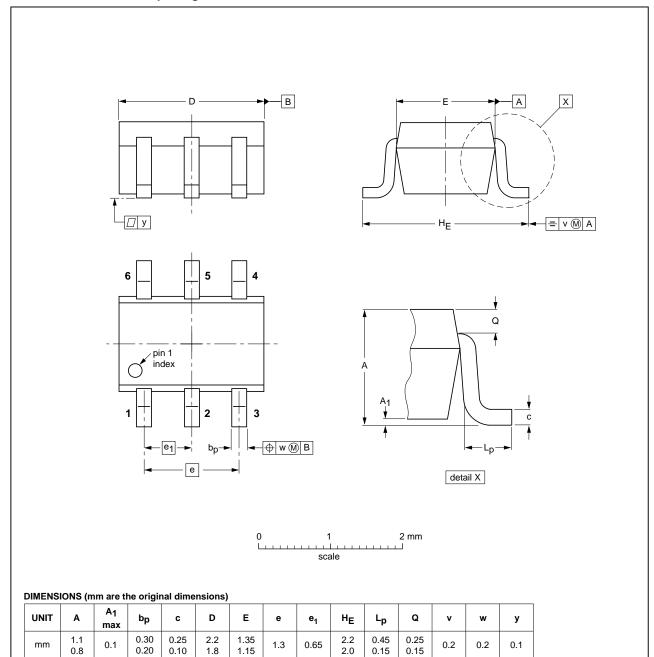
## MMIC wideband amplifier

**BGA2709** 

## **PACKAGE OUTLINE**

## Plastic surface-mounted package; 6 leads

**SOT363** 



OUTLINE	REFERENCES			EUROPEAN	ISSUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT363			SC-88			<del>-04-11-08-</del> 06-03-16

## MMIC wideband amplifier

**BGA2709** 

#### **DATA SHEET STATUS**

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published
  and may differ in case of multiple devices. The latest product status information is available on the Internet at
  URL http://www.nxp.com.

#### **DEFINITIONS**

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### **DISCLAIMERS**

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

## MMIC wideband amplifier

**BGA2709** 

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

## NXP Semiconductors

provides High Performance Mixed Signal and Standard Product solutions that leverage its leading RF, Analog, Power Management, Interface, Security and Digital Processing expertise

#### **Customer notification**

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

#### **Contact information**

For additional information please visit: http://www.nxp.com
For sales offices addresses send e-mail to: salesaddresses@nxp.com

© NXP B.V. 2010

All rights are reserved. Reproduction in whole or in part is prohibited without the prior written consent of the copyright owner.

The information presented in this document does not form part of any quotation or contract, is believed to be accurate and reliable and may be changed without notice. No liability will be accepted by the publisher for any consequence of its use. Publication thereof does not convey nor imply any license under patent- or other industrial or intellectual property rights.

Printed in The Netherlands R77/02/pp12 Date of release: 2002 Aug 06

## **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

NXP:

OM7604/BGA2709,598